

KENWOOD

AUDIO ANALYZER

# VA-2230

## SERVICE MANUAL

*NOT IMPORT*  
KENWOOD CORPORATION



## **WARNING**

The following instructions are for use by qualified personnel only. To avoid electric shock, do not perform any servicing other than contained in the operating instructions unless you are qualified to do so.

## **CONTENTS**

<b>SPECIFICATIONS .....</b>	<b>3</b>
<b>SAFETY .....</b>	<b>15</b>
<b>CIRCUIT DESCRIPTION .....</b>	<b>16</b>
<b>BLOCK DIAGRAM .....</b>	<b>25</b>
<b>ADJUSTMENT .....</b>	<b>33</b>
<b>TROUBLESHOOTING .....</b>	<b>40</b>
<b>PARTS LIST .....</b>	<b>44</b>
<b>DISASSEMBLY .....</b>	<b>45</b>
<b>PARTS LIST (ELECTRICAL) .....</b>	<b>46</b>
<b>SCHEMATIC DIAGRAM .....</b>	<b>56</b>
<b>P.C. BOARD .....</b>	<b>71</b>
<b>SEMICONDUCTORS .....</b>	<b>80</b>

# SPECIFICATIONS

Signal Generator (Common to L and R channel outputs)

Items	Specifications	Conditions & remarks
Frequency		
Frequency band Ranges	5Hz to 110kHz Range 1: 5Hz to 20.09kHz Range 2: 20.1kHz to 110.0kHz	DDS oscillation RC oscillation
Set resolutions	1Hz (5Hz to 2009Hz) 10Hz (2.01kHz to 20.09kHz) 100Hz (20.1kHz to 110.0kHz)	
Accuracy	$\pm 5 \times 10^{-5}$ $\pm 3\%$ of set value	DDS oscillation RC oscillation
Display	4 digits max.	
Spot freq.	Spot 1: 20Hz ( $\pm 5 \times 10^{-5}$ ) Spot 2: 1kHz ( $\pm 5 \times 10^{-5}$ ) Spot 3: 20kHz ( $\pm 5 \times 10^{-5}$ )	
Output		
Output ranges	14.0dBV to -85.9dBV 16.2dBm to -83.7dBm	0dBV=1Vrms 600 $\Omega$ 0dBm=1mW 600 $\Omega$
Set resolution	0.1dB resolution	
Display	3 digits with - sign	
Accuracy	Set value $\pm 0.5\text{dB} \geq -40\text{dBV}$ Set value $\pm 0.8\text{dB} < -40\text{dBV}$	At 1kHz as standard, 600 $\Omega$ load
Flatness	$\pm 0.05\text{dB}$ (20Hz to 20.09kHz) $\pm 0.5\text{dB}$ (5Hz to 110kHz)	At 1kHz as standard, 600 $\Omega$ load
Output impedance	Approx. 600 $\Omega$	
Power off noises	$\leq 10\mu\text{Vrms}$	
Distortion rates	$\leq 0.001\%$ (-100dB) (THD+N) 80kHzBW $\leq 0.001\%$ (-100dB) (THD) 80kHzBW $\leq 0.005\%$ (-86dB) (THD+N) 80kHzBW $\leq 0.01\%$ (-80dB) (THD+N)	Spots 1 & 2 Spot 3 20Hz to 20kHz All ranges

# SPECIFICATIONS

## Measuring Circuit Function

Items	Specifications	Conditions & remarks
Measurement functions	Frequency measurement AC level measurement W display Relative display Total distortion rate measurement (THD+N) Harmonic analysis (THD) S/N ratio measurement SINAD measurement Ratio measurement (R/L & L/R) DC level measurement	Select second to tenth harmonics.

## Frequency Measurement

Items	Specifications	Conditions & remarks
Frequency band	5 Hz to 210kHz	In AC level measurement
Display	5 digits max.	
Resolutions	100kHz $\cong$ freq. ..... 10Hz 10kHz $\cong$ freq. <100kHz ... 1Hz 1kHz $\cong$ freq. <10kHz .... 0.1Hz Freq. < 1kHz ..... 0.01Hz	
Input signal level	1mVrms to 100Vrms	AC level measurement 1/3 of full scale or more in each range
Accuracy	$\pm 5 \times 10^{-4} \pm 1$ digit	
Measurement method	Basic frequency is found from sampling data through FFT processing.	
AC measurement Lch Rch L- & R-ch	L channel input signal is measured. R channel input signal is measured. L channel input signal is measured.	
Ratio measurement R/L measurement L/R measurement	L channel input signal is measured. R channel input signal is measured.	

# SPECIFICATIONS

## AC Level Measurement (Common to L and R channel inputs)

Items	Specifications	Conditions & remarks																																							
Input level measuring ranges	<table style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: left;">In V(Resolution)</th> <th style="text-align: left;">In dBV</th> <th style="text-align: left;">In dBm(Resolution)</th> </tr> </thead> <tbody> <tr><td>100V (10mV)</td><td>40.0dBV</td><td>42.2dBm(0.1dB)</td></tr> <tr><td>30V (1mV)</td><td>29.5dBV</td><td>31.7dBm(0.1dB)</td></tr> <tr><td>10V (1mV)</td><td>20.0dBV</td><td>22.2dBm(0.1dB)</td></tr> <tr><td>3V (100<math>\mu</math>V)</td><td>9.5dBV</td><td>11.7dBm(0.1dB)</td></tr> <tr><td>1V (100<math>\mu</math>V)</td><td>0dBV</td><td>2.2dBm(0.1dB)</td></tr> <tr><td>300mV (10<math>\mu</math>V)</td><td>-10.5dBV</td><td>-8.3dBm(0.1dB)</td></tr> <tr><td>100mV (10<math>\mu</math>V)</td><td>-20.0dBV</td><td>-17.8dBm(0.1dB)</td></tr> <tr><td>30mV (1<math>\mu</math>V)</td><td>-30.5dBV</td><td>-28.3dBm(0.1dB)</td></tr> <tr><td>10mV (1<math>\mu</math>V)</td><td>-40.0dBV</td><td>-37.8dBm(0.1dB)</td></tr> <tr><td>3mV(0.1<math>\mu</math>V)</td><td>-50.5dBV</td><td>-48.3dBm(0.1dB)</td></tr> <tr><td>1mV(0.1<math>\mu</math>V)</td><td>-60.0dBV</td><td>-57.8dBm(0.1dB)</td></tr> <tr><td>300<math>\mu</math>V(0.1<math>\mu</math>V)</td><td>-70.5dBV</td><td>-68.3dBm(0.1dB)</td></tr> </tbody> </table> <p>Over-range: Approx. 20%</p>	In V(Resolution)	In dBV	In dBm(Resolution)	100V (10mV)	40.0dBV	42.2dBm(0.1dB)	30V (1mV)	29.5dBV	31.7dBm(0.1dB)	10V (1mV)	20.0dBV	22.2dBm(0.1dB)	3V (100 $\mu$ V)	9.5dBV	11.7dBm(0.1dB)	1V (100 $\mu$ V)	0dBV	2.2dBm(0.1dB)	300mV (10 $\mu$ V)	-10.5dBV	-8.3dBm(0.1dB)	100mV (10 $\mu$ V)	-20.0dBV	-17.8dBm(0.1dB)	30mV (1 $\mu$ V)	-30.5dBV	-28.3dBm(0.1dB)	10mV (1 $\mu$ V)	-40.0dBV	-37.8dBm(0.1dB)	3mV(0.1 $\mu$ V)	-50.5dBV	-48.3dBm(0.1dB)	1mV(0.1 $\mu$ V)	-60.0dBV	-57.8dBm(0.1dB)	300 $\mu$ V(0.1 $\mu$ V)	-70.5dBV	-68.3dBm(0.1dB)	<p>12 ranges Auto meas. : 1mv to 100V vange</p> <p>Except 100V range</p>
In V(Resolution)	In dBV	In dBm(Resolution)																																							
100V (10mV)	40.0dBV	42.2dBm(0.1dB)																																							
30V (1mV)	29.5dBV	31.7dBm(0.1dB)																																							
10V (1mV)	20.0dBV	22.2dBm(0.1dB)																																							
3V (100 $\mu$ V)	9.5dBV	11.7dBm(0.1dB)																																							
1V (100 $\mu$ V)	0dBV	2.2dBm(0.1dB)																																							
300mV (10 $\mu$ V)	-10.5dBV	-8.3dBm(0.1dB)																																							
100mV (10 $\mu$ V)	-20.0dBV	-17.8dBm(0.1dB)																																							
30mV (1 $\mu$ V)	-30.5dBV	-28.3dBm(0.1dB)																																							
10mV (1 $\mu$ V)	-40.0dBV	-37.8dBm(0.1dB)																																							
3mV(0.1 $\mu$ V)	-50.5dBV	-48.3dBm(0.1dB)																																							
1mV(0.1 $\mu$ V)	-60.0dBV	-57.8dBm(0.1dB)																																							
300 $\mu$ V(0.1 $\mu$ V)	-70.5dBV	-68.3dBm(0.1dB)																																							
Level accuraty	$\pm 2\%$ of full scale	1kHz																																							
Frequency response	$\pm 5\%$ (20Hz to 20kHz) $\pm 10\%$ (5Hz to 110kHz) $\pm 20\%$ (5Hz to 210kHz)	At 1kHz, full scale as standard																																							
Residual noises	$\leq 4\mu$ Vrms	80kHzBW																																							
Indicative response	Root mean square value																																								
Relative measurement range	$\pm 130$ dB 0.0001% to 999.9%	Measurement range is restricted by reference level.																																							
Wattage display	Wattage is found from measured AC level and virtual load resistor ( $R_L$ ). No actual load built in.																																								
Wattage display	5 digits max.																																								
$R_L$ setting range	2 to 500 $\Omega$																																								

# SPECIFICATIONS

## Total Distortion Rate Measurement (Common to L and R channel inputs)

Items	Specifications	Conditions & remarks																				
Measurement mode	Total distortion rate (THD+N)																					
Fundamental wave measurement freq.	10Hz to 110kHz	Restricted by input level measurement ranges.																				
Distortion meas. ranges	<table style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: left;">In % (Resolution)</th> <th style="text-align: left;">In dB (Resolution)</th> </tr> </thead> <tbody> <tr><td>100% (0.01%)</td><td>0 dB (0.1dB)</td></tr> <tr><td>30% (0.001%)</td><td>-10.5dB (0.1dB)</td></tr> <tr><td>10% (0.001%)</td><td>-20dB (0.1dB)</td></tr> <tr><td>3% (0.0001%)</td><td>-30.5dB (0.1dB)</td></tr> <tr><td>1% (0.0001%)</td><td>-40dB (0.1dB)</td></tr> <tr><td>0.3% (0.0001%)</td><td>-50.5dB (0.1dB)</td></tr> <tr><td>0.1% (0.0001%)</td><td>-60dB (0.1dB)</td></tr> <tr><td>0.03% (0.0001%)</td><td>-70.5dB (0.1dB)</td></tr> <tr><td>0.01% (0.0001%)</td><td>-80dB (0.1dB)</td></tr> </tbody> </table>	In % (Resolution)	In dB (Resolution)	100% (0.01%)	0 dB (0.1dB)	30% (0.001%)	-10.5dB (0.1dB)	10% (0.001%)	-20dB (0.1dB)	3% (0.0001%)	-30.5dB (0.1dB)	1% (0.0001%)	-40dB (0.1dB)	0.3% (0.0001%)	-50.5dB (0.1dB)	0.1% (0.0001%)	-60dB (0.1dB)	0.03% (0.0001%)	-70.5dB (0.1dB)	0.01% (0.0001%)	-80dB (0.1dB)	9 ranges
In % (Resolution)	In dB (Resolution)																					
100% (0.01%)	0 dB (0.1dB)																					
30% (0.001%)	-10.5dB (0.1dB)																					
10% (0.001%)	-20dB (0.1dB)																					
3% (0.0001%)	-30.5dB (0.1dB)																					
1% (0.0001%)	-40dB (0.1dB)																					
0.3% (0.0001%)	-50.5dB (0.1dB)																					
0.1% (0.0001%)	-60dB (0.1dB)																					
0.03% (0.0001%)	-70.5dB (0.1dB)																					
0.01% (0.0001%)	-80dB (0.1dB)																					
Display units Input signal level Distortion rate	V, dBV & dBm % & dB																					
Indicative response Input signal level Distortion rate	Root mean square value Root mean square value																					
Fundamental wave reduction rate	Fundamental waves are reduced with notch filter (approx. -60dB) and FFT processing. (110 dB reduction)																					
2nd harmonics deviation	±1dB ±3dB	20Hz to 20kHz All ranges																				
Residual noises & distortion rate	Unbalanced input																					
Input ranges 100V, 30V, 10V, 3V 1/3 or more of 1V	<table style="width: 100%; border-collapse: collapse;"> <tbody> <tr> <td style="width: 20%;"><math>\leq -94\text{dB}</math> (<math>\leq 0.002\%</math>)</td> <td style="width: 20%;">10Hz to 20kHz</td> <td style="width: 60%;">80kHzBW</td> </tr> <tr> <td><math>\leq -80\text{dB}</math> (<math>\leq 0.01\%</math>)</td> <td>10Hz to 110kHz</td> <td>500kHzBW</td> </tr> </tbody> </table>	$\leq -94\text{dB}$ ( $\leq 0.002\%$ )	10Hz to 20kHz	80kHzBW	$\leq -80\text{dB}$ ( $\leq 0.01\%$ )	10Hz to 110kHz	500kHzBW															
$\leq -94\text{dB}$ ( $\leq 0.002\%$ )	10Hz to 20kHz	80kHzBW																				
$\leq -80\text{dB}$ ( $\leq 0.01\%$ )	10Hz to 110kHz	500kHzBW																				
1/3 or more of 300mV	<table style="width: 100%; border-collapse: collapse;"> <tbody> <tr> <td style="width: 20%;"><math>\leq -86\text{dB}</math> (<math>\leq 0.005\%</math>)</td> <td style="width: 20%;">10Hz to 20kHz</td> <td style="width: 60%;">80kHzBW</td> </tr> <tr> <td><math>\leq -66\text{dB}</math> (<math>\leq 0.05\%</math>)</td> <td>10Hz to 110kHz</td> <td>500kHzBW</td> </tr> </tbody> </table>	$\leq -86\text{dB}$ ( $\leq 0.005\%$ )	10Hz to 20kHz	80kHzBW	$\leq -66\text{dB}$ ( $\leq 0.05\%$ )	10Hz to 110kHz	500kHzBW															
$\leq -86\text{dB}$ ( $\leq 0.005\%$ )	10Hz to 20kHz	80kHzBW																				
$\leq -66\text{dB}$ ( $\leq 0.05\%$ )	10Hz to 110kHz	500kHzBW																				
1/3 of more of 100mV	<table style="width: 100%; border-collapse: collapse;"> <tbody> <tr> <td style="width: 20%;"><math>\leq -74\text{dB}</math> (<math>\leq 0.02\%</math>)</td> <td style="width: 20%;">10Hz to 20kHz</td> <td style="width: 60%;">80kHzBW</td> </tr> <tr> <td><math>\leq -60\text{dB}</math> (<math>\leq 0.1\%</math>)</td> <td>10Hz to 110kHz</td> <td>500kHzBW</td> </tr> </tbody> </table>	$\leq -74\text{dB}$ ( $\leq 0.02\%$ )	10Hz to 20kHz	80kHzBW	$\leq -60\text{dB}$ ( $\leq 0.1\%$ )	10Hz to 110kHz	500kHzBW															
$\leq -74\text{dB}$ ( $\leq 0.02\%$ )	10Hz to 20kHz	80kHzBW																				
$\leq -60\text{dB}$ ( $\leq 0.1\%$ )	10Hz to 110kHz	500kHzBW																				
20mV of 30mV	$\leq -65\text{dB}$ ( $\leq 0.056\%$ ) 1kHz 20kHzBW																					
7mV of 10mV	$\leq -55\text{dB}$ ( $\leq 0.18\%$ ) 1kHz 20kHzBW																					
2mV of 3mV	$\leq -45\text{dB}$ ( $\leq 0.56\%$ ) 20Hz to 10kHz 20kHzBW																					

# SPECIFICATIONS

Items	Specifications	Conditions & remarks																																	
Residual noises & distortion rate	Balanced input																																		
Input ranges 100V, 30V, 10V, 3V 1/3 or more of 1V	$\leq -86\text{dB}$ ( $\leq 0.005\%$ ) 10Hz to 20kHz 80kHzBW $\leq -80\text{dB}$ ( $\leq 0.01\%$ ) 10Hz to 110kHz 500kHzBW																																		
1/3 or more of 300mV	$\leq -80\text{dB}$ ( $\leq 0.01\%$ ) 10Hz to 20kHz 80kHzBW $\leq -66\text{dB}$ ( $\leq 0.05\%$ ) 10Hz to 110kHz 500kHzBW																																		
1/3 or more of 100mV	$\leq -74\text{dB}$ ( $\leq 0.02\%$ ) 10Hz to 20kHz 80kHzBW $\leq -60\text{dB}$ ( $\leq 0.1\%$ ) 10Hz to 110kHz 500kHzBW																																		
Input level range	1mV to 100Vrms	Distortion rate measurement range is restricted by input signal level.																																	
Input level meas. ranges	<table style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: left;">In V(Resolution)</th> <th style="text-align: left;">In dBV</th> <th style="text-align: left;">In dBm(Resolution)</th> </tr> </thead> <tbody> <tr><td>100V (10mV)</td><td>40.0dBV</td><td>42.2dBm(0.1dB)</td></tr> <tr><td>30V (1mV)</td><td>29.5dBV</td><td>31.7dBm(0.1dB)</td></tr> <tr><td>10V (1mV)</td><td>20.0dBV</td><td>22.2dBm(0.1dB)</td></tr> <tr><td>3V (100<math>\mu</math>V)</td><td>9.5dBV</td><td>11.7dBm(0.1dB)</td></tr> <tr><td>1V (100<math>\mu</math>V)</td><td>0dBV</td><td>2.2dBm(0.1dB)</td></tr> <tr><td>300mV (10<math>\mu</math>V)</td><td>-10.5dBV</td><td>-8.3dBm(0.1dB)</td></tr> <tr><td>100mV (10<math>\mu</math>V)</td><td>-20.0dBV</td><td>-17.8dBm(0.1dB)</td></tr> <tr><td>30mV (1<math>\mu</math>V)</td><td>-30.5dBV</td><td>-28.3dBm(0.1dB)</td></tr> <tr><td>10mV (1<math>\mu</math>V)</td><td>-40.0dBV</td><td>-37.8dBm(0.1dB)</td></tr> <tr><td>3mV(0.1<math>\mu</math>V)</td><td>-50.5dBV</td><td>-48.3dBm(0.1dB)</td></tr> </tbody> </table>	In V(Resolution)	In dBV	In dBm(Resolution)	100V (10mV)	40.0dBV	42.2dBm(0.1dB)	30V (1mV)	29.5dBV	31.7dBm(0.1dB)	10V (1mV)	20.0dBV	22.2dBm(0.1dB)	3V (100 $\mu$ V)	9.5dBV	11.7dBm(0.1dB)	1V (100 $\mu$ V)	0dBV	2.2dBm(0.1dB)	300mV (10 $\mu$ V)	-10.5dBV	-8.3dBm(0.1dB)	100mV (10 $\mu$ V)	-20.0dBV	-17.8dBm(0.1dB)	30mV (1 $\mu$ V)	-30.5dBV	-28.3dBm(0.1dB)	10mV (1 $\mu$ V)	-40.0dBV	-37.8dBm(0.1dB)	3mV(0.1 $\mu$ V)	-50.5dBV	-48.3dBm(0.1dB)	Auto meas.: 100mV to 100V range  Distortion rate meas. band of 3 to 30mV input level meas. range is 10Hz to 20kHz.
In V(Resolution)	In dBV	In dBm(Resolution)																																	
100V (10mV)	40.0dBV	42.2dBm(0.1dB)																																	
30V (1mV)	29.5dBV	31.7dBm(0.1dB)																																	
10V (1mV)	20.0dBV	22.2dBm(0.1dB)																																	
3V (100 $\mu$ V)	9.5dBV	11.7dBm(0.1dB)																																	
1V (100 $\mu$ V)	0dBV	2.2dBm(0.1dB)																																	
300mV (10 $\mu$ V)	-10.5dBV	-8.3dBm(0.1dB)																																	
100mV (10 $\mu$ V)	-20.0dBV	-17.8dBm(0.1dB)																																	
30mV (1 $\mu$ V)	-30.5dBV	-28.3dBm(0.1dB)																																	
10mV (1 $\mu$ V)	-40.0dBV	-37.8dBm(0.1dB)																																	
3mV(0.1 $\mu$ V)	-50.5dBV	-48.3dBm(0.1dB)																																	
Input signal level accuracy	$\pm 2\%$ of full scale	1kHz																																	
Input signal level freq. response	$\pm 5\%$ $\pm 10\%$	20Hz to 20kHz ALL ranges																																	

# SPECIFICATIONS

## Harmonic Distortion Rate Measurement (Common to L and R channel inputs)

Items	Specifications	Conditions & remarks
Measurement mode	Harmonic distortion rate (THD) measurement 2nd to 10th harmonics are selectively detected and measured.	
Residual noises & distortion rate	Unbalanced input	
Input ranges 100V, 30V, 10V, 3V 1/3 or more of 1V	$\leq -100\text{dB} (\leq 0.001\%)$ 10Hz to 20kHz 80kHzBW $\leq -80\text{dB} (\leq 0.01\%)$ 10Hz to 110kHz 500kHzBW	
1/3 or more of 300mV	$\leq -94\text{dB} (\leq 0.002\%)$ 10Hz to 20kHz 80kHzBW $\leq -74\text{dB} (\leq 0.02\%)$ 10Hz to 110kHz 500kHzBW	
1/3 or more of 100mV	$\leq -86\text{dB} (\leq 0.005\%)$ 10Hz to 20kHz 80kHzBW $\leq -74\text{dB} (\leq 0.02\%)$ 10Hz to 110kHz 500kHzBW	
Residual noises & distortion rate	Balanced input (+ & - to inputs at equal level) distortion rate	
Input ranges 100V, 30V, 10V, 3V 1/3 or more of 1V	$\leq -94\text{dB} (\leq 0.002\%)$ 10Hz to 20kHz 80kHzBW $\leq -80\text{dB} (\leq 0.01\%)$ 10Hz to 110kHz 500kHzBW	
1/3 or more of 300mV	$\leq -86\text{dB} (\leq 0.005\%)$ 10Hz to 20kHz 80kHzBW $\leq -74\text{dB} (\leq 0.02\%)$ 10Hz to 110kHz 500kHzBW	
1/3 or more of 100mV	$\leq -86\text{dB} (\leq 0.005\%)$ 10Hz to 20kHz 80kHzBW $\leq -74\text{dB} (\leq 0.02\%)$ 10Hz to 110kHz 500kHzBW	
	Other specifications are the same as in total distortion rate measurement.	

## Harmonic Analysis (Common to L and R channel inputs)

Items	Specifications	Conditions & remarks
Measurement mode	Total distortion rate of specified harmonics (2nd to 10th) is measured.	
	Other specifications are the same as in total distortion rate measurement.	



# SPECIFICATIONS

## SINAD Measurement (Common to L and R channel inputs)

Items	Specifications	Conditions & remarks
SINAD measurement range	0 to 40dB	1 range
Display units S component level SINAD	V, dBV, dBm dB	
Other items	Some as in total distortion rate measurement, except that D-RNG is fixed to 100 %.	

## S/N Ratio Measurement (Common to L and R channel inputs)

Items	Specifications	Conditions & remarks																								
Input level range	Approx. $30\mu V_{rms}$ to $100V_{rms}$ for both signal (S) and noise (N) components	N level must be lower than S level.																								
S/N ratio meas. range	0 to 130dB																									
S/N ratio meas. limit Unbalanced input	S/N ratio measurement range is restricted by S component level in input signals as shown below.  <table style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>S level (<math>\leq 10kHz</math>)</th> <th>S/N meas. limit</th> </tr> </thead> <tbody> <tr><td><math>\geq 30dBV</math></td><td><math>&gt; 130dB</math></td></tr> <tr><td><math>\geq 20dBV</math></td><td><math>&gt; 120dB</math></td></tr> <tr><td><math>\geq 10dBV</math></td><td><math>&gt; 110dB</math></td></tr> <tr><td><math>\geq 0 dBV</math></td><td><math>&gt; 100dB</math></td></tr> <tr><td><math>\geq -10dBV</math></td><td><math>&gt; 90dB</math></td></tr> <tr><td><math>\geq -20dBV</math></td><td><math>&gt; 80dB</math></td></tr> <tr><td><math>\geq -30dBV</math></td><td><math>&gt; 70dB</math></td></tr> <tr><td><math>\geq -40dBV</math></td><td><math>&gt; 60dB</math></td></tr> <tr><td><math>\geq -50dBV</math></td><td><math>&gt; 50dB</math></td></tr> <tr><td><math>\geq -60dBV</math></td><td><math>&gt; 40dB</math></td></tr> <tr><td><math>\geq -70dBV</math></td><td><math>&gt; 30dB</math></td></tr> </tbody> </table>	S level ( $\leq 10kHz$ )	S/N meas. limit	$\geq 30dBV$	$> 130dB$	$\geq 20dBV$	$> 120dB$	$\geq 10dBV$	$> 110dB$	$\geq 0 dBV$	$> 100dB$	$\geq -10dBV$	$> 90dB$	$\geq -20dBV$	$> 80dB$	$\geq -30dBV$	$> 70dB$	$\geq -40dBV$	$> 60dB$	$\geq -50dBV$	$> 50dB$	$\geq -60dBV$	$> 40dB$	$\geq -70dBV$	$> 30dB$	80kHzBW
S level ( $\leq 10kHz$ )	S/N meas. limit																									
$\geq 30dBV$	$> 130dB$																									
$\geq 20dBV$	$> 120dB$																									
$\geq 10dBV$	$> 110dB$																									
$\geq 0 dBV$	$> 100dB$																									
$\geq -10dBV$	$> 90dB$																									
$\geq -20dBV$	$> 80dB$																									
$\geq -30dBV$	$> 70dB$																									
$\geq -40dBV$	$> 60dB$																									
$\geq -50dBV$	$> 50dB$																									
$\geq -60dBV$	$> 40dB$																									
$\geq -70dBV$	$> 30dB$																									
Display unit S component level S/N ratio	V, dB, dBm dB																									
S component level accuracy	$\pm 2\%$ of full scale	1kHz																								
S/N ratio accuracy	$\pm 1.2dB$	1kHz																								

## SPECIFICATIONS

Items	Specifications	Conditions & remarks
Indicative response	Root mean square value	
Delay time in S/N ratio meas.	1.5 sec. + meas. time 2 sec. + meas. time 3 sec. + meas. time 5 sec. + meas. time 7 sec. + meas. time 10 sec. + meas. time	SS=1.5s SS= 2 s SS= 3 s SS= 5 s SS= 7 s SS=10s

### Ratio Measurement (L/R & R/L)

Items	Specifications	Conditions & remarks
Input level range	Approx. 30 $\mu$ Vrms to 100Vrms for both L and R input signals	
Relative level meas. range	0 to $\pm$ 130dB 0.0001% to 999.9%	Measurement range is restricted by reference level.
Ratio meas. range denominator signal level	Ratio measurement range is restricted by denominator level as shown below.  40dBV      0 to -130dB 30dBV      10 to -120dB 20dBV      20 to -110dB 10dBV      30 to -100dB 0 dBV      40 to -90dB -10dBV     50 to -80dB -20dBV     60 to -70dB -30dBV     70 to -60dB -40dBV     80 to -50dB -50dBV     90 to -40dB -60dBV     100 to -30dB -70dBV     110 to -20dB -80dBV     120 to -10dB -90dBV     130 to 0dB	80kHzBW f $\leq$ 20kHz
Display unit L & R level ratio	V, dBV, dBm dB, %	
Ratio accuracy	$\pm$ 1.2dB	1kHz
Indicative response	Root mean square value	
Measurement switching time	1.5 sec. + meas. time 2 sec. + meas. time 3 sec. + meas. time 5 sec. + meas. time 7 sec. + meas. time 10 sec. + meas. time	SS=1.5s SS= 2 s SS= 3 s SS= 5 s SS= 7 s SS=10s

# SPECIFICATIONS

## DC Level Measurement

Items	Specifications	Conditions & remarks
Input channel	For DC input: 1 channel	
Input impedance	For DC input: Approx. $1M\Omega$	
Input level display unit	V	
Input level meas. range	Display in V (Resolution) 100V (100mV) 30V (10mV) 3V (1mV) 300mV (0.1mV) 30mV (0.01mV) Over-range: Approx. 20%	5 ranges      Except 100 range
Level meas. accuracy	$\pm 0.3\%$ of full scale +0.75% of measured value $\pm 3\%$ of full scale +0.75% of measured value	Over 300mV range 30mV range

# SPECIFICATIONS

## Common Matters to Measurement Functions

Items	Specifications	Conditions & remarks
Input channel	For AC input: 2 channels (Balanced and unbalanced switchable)	
Input impedance	For AC input: Approx. 10k $\Omega$ /100k $\Omega$	
Max. allowable input	150VDC+ACpeak (AC input impedance: 10k $\Omega$ , preset level : 30Vrms)	1kHz
Ground	Floating and non-floating switchable (DC, AC & GEN)	
Filters	<Characteristics in the fixed input range>	
100Hz HPF	-3dB cut-off freq. 75 $\pm$ 15Hz	Analog filter
200Hz HPF	Roll-off characteristics: $\leq$ -40dB at 25Hz -3dB cut-off freq. 180 $\pm$ 25Hz Roll-off characteristics: 60dB/decade	Analog filter
15kHz LPF	Pass band characteristics: $\pm$ 1dB below 15kHz	Digital filter
20kHz LPF	Attenuation band char: $\leq$ -30dB over 19kHz Pass band characteristics: $\pm$ 1.5dB below 20kHz $\pm$ 1.0dB below 19.8kHz	Analog filter
80kHz LPF	Attenuation band char: $\leq$ -30dB over 24.1kHz -3dB cut-off freq. 80 $\pm$ 10Hz Roll-off characteristics: 60dB/decade	Analog filter
PSOPHO A	A characteristics compliant with IEC standard	Digital filter
PSOPHO CCIR ARM	CCIR ARM characteristics	Digital filter
PSOPHO AUDIO	Audio characteristics compliant with DIN 45405	Digital filter
Monitor output	Output impedance: Approx. 600 $\Omega$	ACV monitor
Averaging function	N=OFF, 2, 4, 8 & 16	Additive averaging

\* The above-shown specifications are applicable to non-floating operation in GEN, AC or DC mode.

# SPECIFICATIONS

## Memory Function

Items	Specifications	Conditions & remarks
Points Settings	100 points (May be divided into 10 groups.) ① Panel setting ② EXT CONTROL I/O ③ Batch storage of limit data into memory	

## Sequence Function

Items	Specifications	Conditions & remarks
Auto sequence functions	① Single up ② Single down ③ Repetitive up ④ Repetitive down	

## GP-IB Interface

Items	Specifications	Conditions & remarks
Functions	① Panel status ② Measured value output ③ Memory dump ④ Memory synchronization	

## EXT I/O Interface function

Items	Specifications	Conditions & remarks
Functions	① Remote sequential recalling ② Remote modification ③ Remote direct recalling ④ Limit judgment output ⑤ Control output ⑥ Memory list print-out ⑦ 8-bit data reading ⑧ Measured data print-out	

## SPECIFICATIONS

Items	Specifications	Conditions & remarks
Dimensions	Frame : 426(W)×400(D)×99(H) mm Max. dimensions: 426(W)×449(D)×113(H) mm	
Weight	Approx. 9.7kg	
Line voltage	100V/120V/220V/240VAC±10%, 50/60Hz	
Power consumption	Approx. 48W	On 100VAC
Fuse capacity	100V, 120V area : 630mA (Time lag fuse) 220V, 240V area : 315mA (Time lag fuse)	
Guarantee temp. & humid	10°C to 35°C, 85% or less (No dew condensation)	
Operating temp. & humid	10°C to 35°C, 85% or less (No dew condensation) * Max. humidity at operating temperature over 30°C is restricted according to LCD environmental condition graph. Use in allowable range.	
Insulation resistance	Between source and case : 500VDC, 30MΩ Between AC GND and case : 500VDC, 30MΩ Between DC GND and case : 500VDC, 30MΩ Between GEN GND and case: 500VDC, 30MΩ	
Dielectric strength	Between AC input and case: 1500VAC, 50/60Hz, 1 minute	

# SAFETY

## SAFETY

Before connecting the instrument to a power source, carefully read the following information, then verify that the proper power cord is used and the proper line fuse is installed for power source. The specified voltage is shown at the fuse holder of the AC inlet. If the power cord is not applied for specified voltage, there is always a certain amount of danger from electric shock.

### Line voltage

This instrument operates using ac-power input voltages that 100/120/220/240 V at frequencies from 50 Hz to 60 Hz.

### Power cord

The ground wire of the 3-wire ac power plug places the chassis and housing of the instrument at earth ground. Do not attempt to defeat the ground wire connection or float the instrument to do so may pose a great safety hazard. The appropriate power cord is supplied by an option that is specified when the instrument is ordered.

The optional power cords are shown as follows in Fig. 1.

### Line fuse

The fuse holder is located on the rear panel and contains the line fuse. Verify that the proper fuse is installed by replacing the line fuse.

### Voltage conversion

This oscilloscope may be operated from either a 100 V to 240 V, 50/60 Hz power source. Use the following procedure to change from 100 to 240 volt operation or vice versa.

1. Remove the fuse holder.
2. Replace fuse F 1 with a fuse of appropriate value, 630 m amp for 100 VAC to 120 VAC operation, 315 m amp for 220 VAC to 240 VAC operation.
3. Reinsert it for appropriate voltage range.
4. When performing the reinsertion of fuse holder for the voltage conversion, the appropriate power cord should be used. (See Fig. 1.)







Plug configuration	Power cord and plug type	Factory installed instrument fuse	Parts No. for power cord
	North American 120 volt/60 Hz Rated 15 amp (12 amp max; NEC)	630 mA, 250 V Slow blow 5 x 20 mm	E30-1951-05
	Universal Europe 230 volt/50 Hz Rated 16 amp	315 mA, 250 V Slow blow 5 x 20 mm	E30-1952-05
	U.K. 240 volt/50 Hz Rated 13 amp	315 mA, 250 V Slow blow 5 x 20 mm	E30-1963-05
	Australian 240 volt/50 Hz Rated 10 amp	315 mA, 250 V Slow blow 5 x 20 mm	E30-1821-15
	North American 240 volt/60 Hz Rated 15 amp (12 amp max; NEC)	315 mA, 250 V Slow blow 5 x 20 mm	—
	Switzerland 240 volt/50 Hz Rated 10 amp	315 mA, 250 V Slow blow 5 x 20 mm	—

Fig. 1 Power Input Voltage Configuration

# CIRCUIT DESCRIPTION

## Panel Unit (X66-1410-00)

The Panel Unit controls the key inputs and LED display on the panel.

Lower Higher	CC 0	CC 1	CC 2	CC 3	CC 4	CC 5	CC 6	CC 7
RR 0	J CW	J CCW			S CCW-full	S CCW	S CW	S CW-full
RR 1	F 1	F 2	F 3	F 4	F 5			SHIFT
RR 2	SEQ	RCL	∧	∨		7	4	1
RR 3	8	5	2	•	9	6	3	—
RR 4	kHz	Hz	CLR	ENT	△	◁	▷	▽
RR 5	MEVV	GEN	AC-V	DIST	S/N			
RR 6								
RR 7	OUT	CH(GEN)	CH(AC)		GPIB			

Notes: J CCW (CW) indicates the rotation of the jog/shuttle dial in the counterclockwise (clockwise) direction.

S CCW-full (CW-full) indicates the rotation of the shuttle dial in the fully counterclockwise (fully clockwise) position.

S CCW (CW) indicates the rotation of the shuttle dial in the counterclockwise (clockwise) direction.

V14 is the ROM for use in decoding the key matrix signals. When a key or jog/shuttle dial is operated, an address is selected according to the table above and the data written in that address is output at DD0 to DD7. The output at U15 pin ⑧ is the CPU interrupt signal \*REM0\_KRQ, which turns from L → H when a key is pressed or the jog/shuttle dial is operated. Upon detection of the interrupt, the CPU sets \*REM0\_KEN at V16E pin ① to L level to open the gate of U13, and read the key matrix signal to read the key press or jog/shuttle dial status.

Signal name	LD 00	LD 01	LD 03	LD 04	LD 06	LD 07	LD 10	LD 14	LD 15	LD 17
LED	GEN L	GEN R	AC 2 (UNB)	ACR (UNB)	ACL (BAL)	ACR (BAL)	OUT	GPIB	SHIFT	SEQ

U10 and U2 are ports for lighting the LEDs. The signal names and the LEDs corresponding to each of them are shown in the above table. These ports are controlled by the CPU.

Signal name	LD 02	LD 05
LED	GEN GND	AC GND

The GEN GND and AC GND LEDs shown above are lighted by the Slight SWs on the Switch Unit (X69-1350-00) located on the rear of the set.

## CPU Unit (X77-1940-00)

The CPU Unit is the system controller managing the audio analyzer system. The main functions include the key input processing, LCD display, EXT I/O control, DSP control, GP-IB control and control of the ports of various units.

### Key processing

The data exchange with panel keys is performed using PD0 to PD7 of I/O port U12, REM0\_A/\*D, REM0\_STB, REM0\_KEN and key or rotary encoder interrupt signal \*REM0\_KRQ.

### LCD display

The LCD display is controlled by LCD controller U18 and VRAM U19. The CPU transfers necessary data for display to U18 at the timing of interrupt signal \*INTP1.

### EXT I/O control

The data exchange inside the I/O Unit (X72-1200-00) is performed using AB00 to AB15, DB0 to DB7, \*STBB and R/\*WB. The external key interrupt signal is \*INT4.

The data exchange with the external rotary encoder is performed using PD0 to PD7 of I/O port U12, \*REM1\_KEN and rotary encoder interrupt signal \*REM1\_KRQ.

### GP-IB control

The GP-IB is controlled by GP-IB controller U14 and drives U15 and U16.

The GP-IB interrupt signal is INT7.

### DSP control

From the viewpoint of the CPU, the DSP is an I/O port.

The data exchange with DSP is performed using AB15 to AB00, DB0 to DB7, R/\*WB, \*STBB, PS5, PS3, PS2 and \*INTE0 which is the interrupt signal from DSP (DSP interrupt signal \*DAK1 is not used).

### Unit port control

The ports of the Control Unit (X77-1930-00), DSP Unit (X79-1230-00) and Oscillator Unit (X71-1230-00) are set using signals \*SCKB, SDTB, SSTB and SR/WB.

### Back-up

The notch filter calibration constants are stored in EEPROM U34.

The contents of RAM U11 are backed up by BT1. Reset IC U2 controls signal MWE so that the contents of RAM U11 are not destroyed at the time the power is switched on or off. MWE is a signal which becomes +5 V before the CPU starts when the power is turned on, and 0 V before the CPU is shut off when the power is turned off. VRAM U19 is not backed up.



# CIRCUIT DESCRIPTION

## I/O Unit (X72-1200-00)

The I/O unit is used to control the EXT I/O.

Signals EB085 to 089 and EB091 to 093 are connected with the EXT I/O pins as shown in the following table.

Signal name	EB 085	EB 086	EB 087	EB 088	EB 089	EB 091	EB 092	EB 093
EXT I/O	UP	DOWN	CLR	FREQ	AMPL	K 5	K 6	K 7

These signals are sent through buffer U7 and 8-to-3 priority encoder U6 and input to input port U5. When any of the above signals turns from H to L, signal GS at U6 pin ⑭ goes L level to generate interrupt EB004 (INT4) to the CPU, which then reads the pin data through U5.

Signal name	EB 039	EB 040
EXT I/O	EXTRE 1	EXTRE 2

Signals EB039 and EB040 are connected with the EXT I/O pins as shown in the above table. As the external rotary encode is connected to these pins, interrupt EB097 (\*REM1\_KRW) to the CPU is generated at U8B, U8D, U8A, U8C, U3D, U9A and U9B. The CPU reads the rotation direction of the rotary encode by opening the gate of U10 using EB098 (\*REM1\_KEN).

Signal name	EB 036	EB 037	EB 038
EXT I/O	UNDER	PASS	OVER

Signals EB036, EB037 and EB038 are connected to the EXT I/O as shown in the above table. These pins are connected with external LEDs and used in GO/NO-GO judgment. The CPU latches these signals at U4.

Signal name	EB 084	EB 083	EB 082	EB 081	EB 080	EB 079	EB 078	EB 077
EXT I/O	P27	P26	P25	P24	P23	P22	P21	P20

Signal name	EB 034	EB 033	EB 032	EB 031	EB 030	EB 029	EB 028	EB 027
EXT I/O	P17	P16	P15	P14	P13	P12	P11	P10

Signal name	EB 026	EB 035
EXT I/O	STB	BUSY

As shown in table above, I/O ports P00 to P17 of U5 are used in the data I/O with port 1 and port 2 of the EXT I/O.

## Control Unit (X77-1930-00)

The Control Unit is used to control the contrast and brightness of the LCD.

The output from pin ② of D/A converter U3 is the DC output for the LCD contrast adjustment, while the output from its pin ① is the DC output for the LCD brightness adjustment. This D/A converter is set using serial data SSTB, \*SCKB and SDTB from the CPU.

The output from U5A pin ① is designed to be about -6 V when the contrast setting made on the panel is 0 and about -8.5 V when the panel setting is 9, provided that the ambient temperature is 23 deg.C. (The voltage is adjusted with V1, but as the actual voltage is variable due to variance in LCD products, it is recommended to set the panel setting to

5 and adjust visually to the easiest-to-seen contrast).

TH1 is the thermister for compensating for change in the LCD contrast due to temperatures.

U4A and U4B form a CR oscillator with oscillating frequency of about 280 Hz. Based on the brightness adjustment DC signal output from U3 pin ①. With the brightness adjustment, the 150 V AC voltage supplied to the LCD lamp is controlled by switching the input to DC/AC converter U6 ON/OFF by varying the duty of the 280 Hz clock at the output of U4D using the oscillator.

## DSP Unit (X79-1230-00)

This unit is used to process the analog signals from the AC or DC input to signals which are easy to be A/D converted by the DSP Unit (X79-1250-00).

The unit can be divided roughly into the AC signal processor and DC signal processor blocks. The blocks are isolated between each other.

### AC measurements (including distortion measurement)

The AC input circuitry is compatible with BAL and UNBAL input modes, and equipped with two lines for the R CH and L CH of stereo.

The signal input through R is supplied to K4A or K4B (in case of BAL input) to have the input impedance converted from 100k to 10k. The signal input through L is supplied to K7A or K7B (BAL input) to have the input impedance converted from 100k to 10k. The L/R inputs are switched over with K5A, K6A, K5B (BAL input) and K6B (BAL input) and with K8A, K9A, K8B (BAL input) and K9B (BAL input). The input from the CH which is not used for single-CH measurement or CH which is not being measured in L&R measurement is terminated by R5. 17 (BAL input), R29 and 34 (BAL input).

K10A and K10 B (BAL input) are used to switch over the input signal gain between 0 dB/-20 dB, and K11A and K11B (BAL input) are used to switch over the input signal gain between 0 dB/-10 dB. The gain can be switched according to ranges.

TC1, TC2, TC3 and TC4 are used for high-frequency compensation when the above attenuation is inserted. D1, Q13, D4, Q14, D5, Q15, D8 and Q16 are used to protect the input of preamp U1 in case a signal with an excessive level is input by mistake.

U1 is a preamplifier (X73-2080-00). The relationship between its gain and the logic of pins ⑭, ⑮ and ⑯ is as shown below.

Gain	U1 ⑭	U1 ⑮	U ⑯
+30 dB	H	H	H
+20 dB	L	H	H
+10 dB	L	L	H
0 dB	L	L	L

The gain above can be switched according to the ranges.

# CIRCUIT DESCRIPTION

As the preamp is required to have a distortion below 0.001%, The preamp used in gain switching reduces the ON resistance by using two parallel transistors for FETs Q5, Q11, Q10 and Q26. In addition, to control the offset in the preamp output to no more than 300 mV when the gain is 30 dB, first-stage FETs Q1 to Q4 are selected with VGS (so that the difference in the FETs forming the pair is no more than 3 mV).

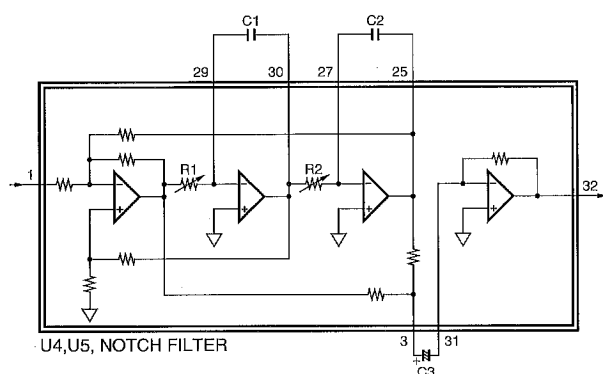
When the input mode is BAL, U2A is combined with K12A and K12B to convert the BAL signal into UNBAL signal.

U3A is a +40 dB amp which is combined with K14A and K14B. This amp is operated with distortion measurement input ranges of 3 mV, 10 mV and 30 mV.

K15B is used to switch between the 0/-40 dB attenuators, and operated when the input range in AC-V measurement is between 100 mV and 100 V or when the distortion range in distortion measurement is between 3% and 100%. VR2 is the -40 dB adjustment VR.

This input outputs the RANGE ONITO signal, which is used as the input signal measuring signal in distortion measurements.

U4 and U5 are notch filters, which are combined in AC-V measurements to act as a +40 dB amp. Each of these filters is composed of the state-variable filters as shown below.



In the following, the filter shown in the figure above will be described taking U4 as example.

In the actual circuit diagram, C1 and C2 in the figure above are composed respectively of C24, 25, 27 and 29 and of C31, 33, 35 and 37. They are switched over by a relay according to the frequency ranges as shown in the following table.

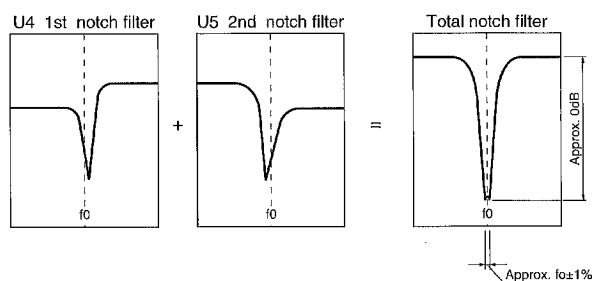
ITEM	Frequency range	C24, C31,	C25, C33,	C27, C35,	C29, C37,
ACV	All ranges	Shorted	OFF	OFF	OFF
THD+N or THD or HD	10 Hz to 200 Hz	ON	OFF	OFF	OFF
	200 Hz to 2k	OFF	ON	OFF	OFF
	2 k to 20 k	OFF	OFF	ON	OFF
	20 k to 110 k	OFF	OFF	OFF	ON

In the actual circuit diagram, VRs R1 and R2 in the figure above are composed of R100, R101 and a discrete ladder resistance. The resistance value can be switched with the FET SWs of U4 to U7 and U8 to U11. The relationship between the notch frequency and U10 port settings is as shown below though there are slight difference in bits depending on sets (depending on the notch filter calibration conditions).

Signal name	f	2 k	5 k	9 k	10 k
N 11		H	H	H	H
N 10		H	H	L	L
N 09		H	L	L	L
N 08		L	L	H	L
N 07		H	L	L	H
N 06		L	H	L	H
N 05		H	L	H	L
N 04		H	H	L	L
N 03		H	H	L	H
N 02		H	L	H	L
N 01		H	L	L	L
N 00		H	H	H	L

The settings of U5 are identical to U4.

The attenuation characteristics differ between U4 and U5 as shown below. As for the characteristics when U4 and U5 are connected in series, the attenuation is about -60 dB and attenuation width is about  $\pm 1\%$  of  $f_0$ .



U16B is used for HPF filtering. C78, C79, R98 and R99 form the 5 Hz two-stage Butterworth HPF, C80, C81, R110 and R101 form the 30 Hz two-stage Butterworth HPF, C2, C83, C84, R103, 104 and R105 form the 200 three-stage Butterworth HPF, and C87, C88, C(9, 108, 109, R110, V16A, C85, C86, R106 and R107 form 100 Hz five-stage Butterworth HPF. An optional HPF can be connected to P8.

VR9 is the VR for offset adjustment of U16B.

# CIRCUIT DESCRIPTION

V17A is used for LPF filtering of 800 kHz and 80 kHz. R114, R115, R116, C92, C93 and C94 from the 500 kHz three-stage Butterworth LPF, and R117, R118, R119, C95, C96 and C97 from the 80 kHz three-stage Butterworth LPF. An optional LPF can be connected through connector P8; The optional PSO PHO filter can be connected between the U17A output and the next stage through connector P8.

U20 is a 20 kHz nine-stage Chebyshev LPF manufactured by Murata. This filter is usable even when the 20 kHz LPF in the menu is set to OFF. If TP11 is observed, it may be seen that analog SW U15G is switched at a certain interval (which differs depending on the FAST and SLOW modes). This is because the 20k LPF should be switched OFF for the 500 kHz sampling measurements and ON for the 44 kHz sampling adjustment.

VR10 is the VR for use in gain adjustment when the 20k LPF is ON.

The filter composed of U33 and U33B is used to provide the filtering characteristics of 20 kHz or more for the DIN-AUDIO filter among the PSO PHO filters. The filter characteristics below 20 kHz of the DIN-AUDIO filter, those of other PSO PHO filters and those of the 15 kHz LPF are provided by digital processing after A/D conversion.

Although the combination of U21F and U22A allow switching the input signal gain between +10 dB/-10 dB, the gain is currently fixed at -10 dB with both AC-V and distortion measurements.

VR11 is the VR for use in offset adjustment of U22A.

The combination of U21E and U22B allow switching the input signal gain between 0 dB/+10 dB/+20 dB. With the AC-V measurement, the gain is +20 dB only in the 300 mV range and +10 dB in other ranges. With distortion measurement, the gain is variable depending on the distortion ranges.

VR12 is the VR for use in offset adjustment of U22B. VR5 is used to adjust the gain of U22B. U23A, C113, C114, R147 and R148 from the 3 Hz HPF.

U24A and U24B are permanently ON during AC-V measurement. In distortion measurement, they are switched alternately with V24C and C24D to measure the input level and distortion signal level and the distortion rate is calculated with the DSP.

U23B is a buffer amp.

VR6 and VR13 are the gain adjustment VR and offset adjustment VR, which allow gain and offset adjustments when combined with U17 in the DSP Unit (X79-1250-00). Care is required in the gain adjustment because varying VR6 also affects the gain in DC-V measurement.

## DC measurements

The DD measuring circuit is composed of the input attenuator and DC amp

The signal input from DC IN is supplied to K1A, where the input signal gain is switched between 0 dB/-20 dB. With K2A, the input signal gain can be switched between

0 dB/-10 dB. VR3 and VR4 are used to adjust the -20 dB and -10 dB attenuators respectively. These attenuators are used in the 100 V and 30 V ranges.

K3A is used to short-circuit the U12A Input to the measuring circuitry GND during AC-V measurement.

K3B is used to connect the measuring circuitry GND to the DC input GND during DC measurement or to AC input GND during AC measurement.

Note: The AC input GND and DC input GND can be connected to the chassis GND independently through the Switch Unit (X69-1350-00) on the rear panel.

Q17 and Q18 are used to protect the input of U12A in case a signal with an excessive level is input by mistake.

V12A is the DC amp. The relationship between its gain and the logic of U6A pins ⑦ and ⑭ is as shown below.

Amp gain	U 6A⑦	U 6A⑭	U 13A	U 13B	U13C	U13D
0 dB	H	H	ON	OFF	ON	OFF
20 dB	H	L	ON	OFF	OFF	ON
40 dB	L	L	OFF	ON	OFF	ON

The gain above can be switched according to the ranges.

VR14 is the VR used for offset adjustment of U12A.

R75 and R300 are used to adjust the DC level in order to align the sensitivity in DC measurement and the input signal level sensitivity in distortion measurement.

During DC measurement, V25D is turned ON and connected to buffer amp U23B.

## AC-V monitor circuit

To monitor the input signal during AC-V measurement, the AC-V monitor outputs signal with an output impedance of 600-ohm and output level of 1 Vrms (at full-scale input of each range with 600-ohm termination) from the BNC connector on the rear panel. The output from pin ⑳ of 2nd notch filter U5 is used for monitoring. The level is adjusted with VR100.

## Port setting

The ports are set by supplying serial data (SDT), clock (SCK) and latch pulse (SST) to U30A, U15A, U21A, U6A, U7A, U8A, U9A, U10A and U11A.

To pass the data above, the gate of U18B opens in case the setting is made from the DSP Unit, and the gate of U18A in case it is made from the CPU Unit.

PH1A, PH1B and PH2A are the photocouplers for use in isolation between the CPU and DSP GND and the measuring circuitry GND.

# CIRCUIT DESCRIPTION

## ±12.5 V regulator

The ±12.5 V regulator use opamps U27A and U27B. Q11 and Q12 are attached onto the chassis for use in heat radiation.

## Gain distribution table

The following tables show the gain distribution in each of the stages from input to output in the DC-V, AC-V and distortion measurements. (The gain is expressed in dB, where 0 dB = 1 Vrms.)

## DSP Unit (X79-1230-00): Gain Distribution Table

### DC measuring ranges

Input range	Input level	ATT (K1A K2A)	AMP (U12A)	OUTPUT (TP14)
100 V	40	-30	0	10
30 V	30	-20	0	10
3 V	10	0	0	10
300 mV	-10	0	20	10
30 mV	-30	0	40	10

### AC measuring ranges

Input range	Input level	ATT (K 10A K 10B K 11A K 11B)	AMP (X73-2080-00)	+40 dB GAIN (K14A K 14B ) U 3A	-40 dB ATT (K15B)	1 st & 2 nd Notch filter (X69-1330-00, X69-1340-00)	+10 dB -10 dB GAIN (U22B) U21E	+ 0 dB + 10dB + 20 dB GAIN (U22B) U21E	OUTPUT (TP14)
100 V	40	-30	0	0	-40	40	-10	10	10
30 V	30	-20	0	0	-40	40	-10	10	10
10 V	20	-10	0	0	-40	40	-10	10	10
3 V	10	0	0	0	-40	40	-10	10	10
1 V	0	0	10	0	-40	40	-10	10	10
300 mV	-10	0	20	0	-40	40	-10	10	10
100 mV	-20	0	30	0	-40	40	-10	10	10
30 mV	-30	0	0	0	0	40	-10	10	10
10 mV	-40	0	10	0	0	40	-10	10	10
3 mV	-50	0	20	0	0	40	-10	10	10
1 mV	-60	0	30	0	0	40	-10	10	10
300 μV	-70	0	30	0	0	40	-10	20	10

# CIRCUIT DESCRIPTION

## Distortion measuring input ranges

Input range	Input level	ATT ( K 10A K 10B K 10B K 11A K 11B)	AMP (X73-2080-00)	+40 dB GAIN (K14A K 14B ) U 3A	OUTPUT (TP14)
100 V	40	-30	0	0	10
30 V	30	-20	0	0	10
10 V	20	-10	0	0	10
3 V	10	0	0	0	10
1 V	0	0	10	0	10
300 mV	-10	0	20	0	10
100 mV	-20	0	30	0	10
30 mV	-30	0	0	40	10
10 mV	-40	0	10	40	10
3 mV	-50	0	20	40	10

## Distortion ranges

Distortion range	-40 dB ATT (K15B) distortion input	-40 dB ATT (K15B)	1 st & 2 nd Notch filter (X69-1330-00 X69-1340-00)	+10 dB -10 dB GAIN (U22A) U21E	+ 0 dB + 10dB + 20 dB GAIN (U22B) U21E	OUTPUT (TP14)
100 %	10	-40	40	-10	0	0
30 %	0	-40	40	-10	+10	0
10 %	-10	-40	40	-10	+20	0
3 %	-20	-40	40	+10	+10	0
1 %	-30	0	40	-10	0	0
0.3 %	-40	0	40	-10	+10	0
0.1 %	-50	0	40	-10	+20	0
0.03 %	-60	0	40	-10	+20	-10
0.01 %	-70	0	40	-10	+20	-20

# CIRCUIT DESCRIPTION

## DSP Unit 2 (X79-1240-00)

This unit is used to analyze the measured signal data, generate the DDS signal, generate the monitor signal and control the ports.

### Measured signal data analysis

The measured signal which has been converted into serial data by the DSP Unit (X79-1250-00) is converted into parallel signal by U14 and U15, and loaded in FIFO memory U16 and U17 at the timing of ADWCK. ADWCK is generated by using DSP Unit (X79-1250-00) U27 which decodes the signal generated by the A/D PLL of the DSP Unit (X79-1250-00). Every time counter of U21 indicates that 1k word of data has been loaded in the memory, interrupt INT2 to the DSP is generated, so the DSP reads and analyzes the data. When the data has been analyzed, the DSP generates interrupt OINT to the Main Cpu (X77-1940-00) to inform it that the measured value has been set.

### DDS signal generation

The DDS signal generated by the DSP is written in FIFO memory U22 and U23. This write operation is performed when the DSP receives interrupt INT1 which is generated when the counter formed with U19B, U19C, U18C and U20A has read data from the 128-word FIFO memory. Clock DDSRCK which is used to read data from the FIFO memory is generated by using U30 which decodes the signal generated by the DDS PPL of the DSP Unit (X79-1250-00).

The data read from FIFO memory U22 and U23 is converted from parallel into serial by U24 and U25, oversampled by digital filter U26, sent through selector U27 (which is fixed on the input of the digital filter side) and buffer U28, and supplied to the Oscillator Unit (X71-1230-00).

### Monitor signal generation

The THD monitoring signal generated by the DSP is written in FIFO memory U32. The written data is read at the timing of the about 60 Hz clock generated by U31A and U31B, converted into analog signal by D/A converter U33 and I-V converter amp U34, and output externally. (This circuit is omitted from the term of February '95.)

### Response of DSP at the time of CPU booting

When the CPU (X77-1940-00) is booted after the power is switched ON, the CPU sets the  $\overline{\text{IORES}}$  and HLD lines to L levels to request the DSP (U1) to open the data bus and address bus. The DSP monitors these lines and, when the L level is detected, the DSP sets signal  $\overline{\text{HLD}}_A$  to L level to inform the CPU that the buses are open. After being informed of the opening of the buses, the CPU transfers the program (initialize program) to AM U3 and U4 through

ports U8, U9, U10 and U11. After the transfer, the CPU sets  $\overline{\text{IORES}}$  and HLD to H level. When the DSP detects it, the DSP start execution of the transferred initialize program and interrupts the CPU with signal  $\overline{\text{OINT}}$  and loads the main program in RAM U3 and U4 using  $\overline{\text{HLD}}_A$ , HLD and the buses. After loading, the main program starts and DSP starts execution of data analysis, DDS signal generation, etc. Hereafter, the DSP interrupts the CPU with  $\overline{\text{OINT}}$  every time measured data is analyzed, and data transfer between the CPU and DSP is performed using HLD,  $\overline{\text{HLD}}_A$  and the buses.

### Port control

In addition to read, write and computation processing of the data in the A/D FIFO memory, DDS FIFO memory and MONITOR FIFO memory, the DSP also generates signals MDT, MCK,  $\overline{\text{MLE}}_1$ ,  $\overline{\text{MLE}}_2$  and  $\overline{\text{MLE}}_4$  which are used to set the I/O ports of the DSP Unit (X79-1230-00) and DSP Unit (X79-1250-00).

## DSP Unit (X79-1250-00)

This unit is used to convert the signal after analog processing by the DSP Unit (X79-1250-00) from A to D, generate the timing signal for the A/D conversion (A/D PLL) and generate the timing signal for use in generation of the digital signal.

### A/D conversion

The signal output from connector P12 of the DSP Unit (X79-1230-00) is input into connector P1 of the DSP Unit (X79-1240-00). To optimize the signal which is input into A/D converter V21 through amp V17A, the input resistance of V17B is switched with analog SW V18A. The gain is managed by the DSP.

The signal input to pin ④ of 12-bit A/D converter U21 is converted from A to D using the pulse supplied to U21 pin ⑳ as the sampling frequency.

After A/D conversion, the digital signal is converted by U22 and U23 from parallel to serial and the serial data is transferred using the clock (which is adjusted to 8.3 MHz with V1) generated by U20C, U20A and C20D. The transfer timing signals of data signal SDAT are the 8 MHz and  $\overline{\text{LOAD}}$  signals. After passing through buffer U5A, the above signal is isolated by photocouplers PH1 and PH3 (isolation between CPU, DSP and GND and the measuring circuitry GND), sent through selector U26 (which is used as the buffer) and supplied to the DSP Unit (X79-1250-00).

# CIRCUIT DESCRIPTION

## DDS PLL

The DDS PLL is composed of loop filter U1, a VCO (an LC oscillator with the oscillation frequency determined by coil L1 and varicaps D1 to D5), RF amp V3 which amplifies the CO output, frequency divider and phase detector U6. The dividing ratio of U6 is set by the DSP so that the VCO output is constant at 100.66 MHz. The reference clock for the input at U6 pin ① is 10 MHz (which is generated by the X'tal oscillator in the DSP).

The VCO oscillation output is amplified by RF amp U2 and divided by U4 into 12.582 MHz. This clock is used as the reference for the timing signal supplied to the DDS oscillator (5 Hz to 20.09 kHz).

Timing signals required for the DDS oscillator, such as VCO0, VCQA, CLK,  $\overline{\text{CLK}}$  and RCY, are generated by U9D, U9C, U9A, U9B, U8B, U7A and U8A.

## A/D PLL

The A/D PLL is composed of loop filter U11, a VCO (an LC oscillator with the oscillation frequency determined by coil L8 and varicap D150), amp U12B which amplifies the VCO output, frequency divide and phase detector U10. The VCO output frequencies are switched over at the intervals determined in AC-V measurement and distortion measurement as well as according to the FAST or SLOW mode. Therefore, the dividing ratio of U10 is also determined dynamically by the DSP according to this interval. The reference clock for the input at U10 pin ① is 10 MHz (which is generated by the X'tal oscillator in the DSP).

Timing signals such as FS500, FS125 and FS31 are used in D/A conversions and generated by U13A and U13B.

## Oscillator Unit (X71-1230-00)

The Oscillator Unit is composed of the 5 Hz to 20.09 kHz variable oscillator using a digital oscillator, three spot oscillators including SPOT1 (20 Hz), SPOT2 (1 kHz) and SPOT3 (20 kHz) and the 10.1 Hz to 110 kHz variable oscillator using a CR oscillator.

## DDS oscillator

The digital sine wave generated by the DSP Unit (X79-1240-00) is input through connector P1. This sine wave uses 3 signal lines which are data (serial data), data transfer clock (12.582 MHz) and latch signal for use in converting serial data into parallel.

These signals are input to photocouplers PH1 and PH2 in order to isolate between the GEN GND circuitry and the CPU, DSP and GND circuitry. The signals output from the photocouplers are sent through Schmitt buffer of U3A and U3B to U3C and U3D and input to D/A converter U4. The level of the output at pin ⑨ is variable with R78. U6A, U6B and U7A from a six-stage Butterworth LPF with  $f_c = 28$  kHz. For the output from U7A pin ⑦, it is required to guarantee a flatness of  $\pm 0.05$  dB a 20 Hz to 20.09 kHz, and the adjustment of trimmer TC1 is necessary for this purpose.

U7B is the 20 kHz LPF for SPOT3 (20 kHz).

The LPF has a high Q in order to reduce the distortion in the output at U7B pin ①.

VR4 is used in the level adjustment of SPOT3.

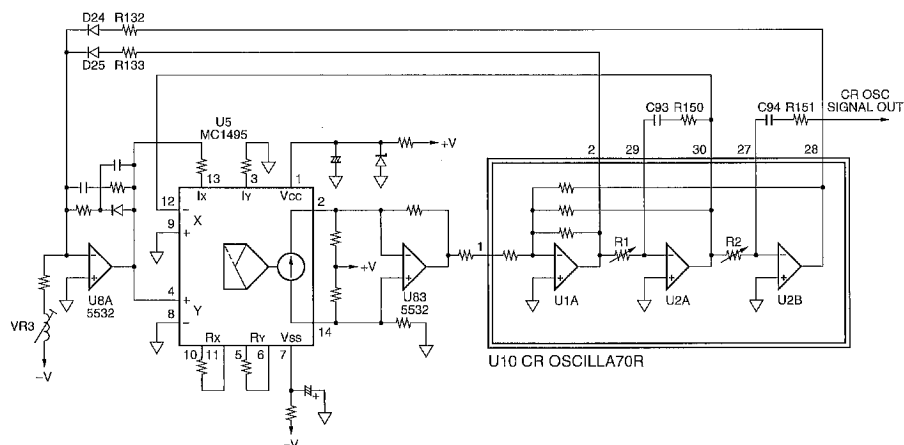
R19, VR5, R152, R17, R18, C95, C90, V11A and V3A in U10 (X71-1220-00) form the 20 Hz LPF for SPOT1 (20 Hz). This LPF also has a high Q to reduce distortion.

R27, VR6, R114, R28, 33, C96, C91, V11B and V3A in U10 form the 1 kHz LPF for SPOT2 (1 kHz). This LPF also has a high Q to reduce distortion.

## RC oscillator

The RC oscillator uses a state-variable filter and is used in the range from 20.1 kHz to 110 kHz.

It has a circuit configuration as shown below.



# CIRCUIT DESCRIPTION

The state-variable filter is composed of three opamps of U1A, U2A and U2B. In the actual circuit diagram, VRs R1 and R2 in the figure above are composed of R100, R101 and a discrete ladder resistance. The resistance value can be switched with the FET SWs of U4 to U7 and U8 to U11.

U5 is the multiplier IC for use in controlling the feedback amount. The sine wave output at U10 pin ⑩ is input to U5 pin ⑫. The amplitude and phase of this sine wave are controlled by the DC level to U5 pin ④, and they are fed back to U10 pin ① via U8B. This feedback is intended to make the oscillation amplitude level constant (AGC).

The sine wave oscillation output at U10 pin ② and the sine wave oscillation output with 180-degree opposite phase at U10 pin ③ are half-wave rectified by R113 and D25 and by 132 and D24, and supplied to integrator U8A. On the other hand, from VR3, a constant current flows in the opposite direction which allows to make the oscillation amplitude constant. The oscillation amplitude is determined by the value of this current (which can be adjusted with VR3).

**The relationship between the frequency set for the C oscillator and U10 ports is as shown below.**

H= 5 V  
L= 0 V

Signal name \ f	25 k	50 k	75 k	100 k	110 k
D 27	H	H	H	H	L
D 26	H	H	L	L	H
D 25	H	L	H	L	H
D 24	L	L	L	L	H
D 23	L	H	H	H	H
D 22	H	L	L	L	H
D 21	H	L	L	H	H
D 20	L	L	H	L	L
D 19	H	L	H	H	H
D 18	H	L	L	H	L
D 17	H	H	L	H	H
D 16	L	H	H	L	L

## Output stage

The DDS or CR oscillated signal is input to the amp formed by Q1, Q2, U24A and U24B. Vr2 is used to adjust the gain of this amp. This amp functions as an inverter amp. With the input resistance switchable by switching ladder resistance R200 using ET SWs U25, U26 and U27 and the gain variable up to 3 dB in 0.1 dB steps. the amp is used as a part of the output attenuator circuit. At the output of this amp, 48 dB, 24 dB, 12 dB, 6 dB, 3 dB and 24 dB attenuators are provided in the order mentioned. They can be switched ON and OFF using relays.

GEN set level [dB]	Attenuator condition [dB] and port setting					
	48	24	12	6	3	24
	(E 08)	(E 09)	(E 10)	(E 11)	(E 12)	(E 13)
14	H	H	H	H	H	H
10.9	H	H	H	H	L	H
7.9	H	H	H	L	H	H
4.9	H	H	H	L	L	H
1.9	H	H	L	H	H	H
-1.1	H	H	L	H	L	H
-4.1	H	H	L	L	H	H
-10.1	H	L	H	H	H	H
-34.1	L	H	H	H	H	H
-82.1	L	L	H	H	L	L

GEN level [dB]	U28 port settings of 0.1 dB step attenuator							
	E 07	E 06	E 05	E 04	E 03	E 02	E 01	E 00
14.0	L	L	H	H	H	L	L	H
13.9	L	H	L	L	L	L	L	L
13.8	L	H	L	L	L	H	H	H
13.7	L	H	L	L	H	H	H	L
13.6	L	H	L	H	L	H	L	L
13.5	L	H	L	H	H	L	H	H
13.0	L	H	H	H	H	H	L	L
12.0	H	L	H	H	L	H	H	H
11.0	H	H	H	L	H	L	H	H
10.9	L	H	L	L	L	L	L	L

When the GEN output is switched OFF, the GEN output is connected to 600-ohm resistors R76 and R77 by means of relays K7A and K7B.

## Port setting

The ports are set by supplying serial data (SDT), clock (SCK) and latch pulse (SST) to U15, U16, U17, U18, U19, U25, U26 and U27.

PH3 and PH4 are the photocouplers for use in isolation between the CPU and DPS GND and the GEN GND.

## ±12.5 V and ±17.5 V regulators

These ±12.5 and ±17.5 V regulators use opamps U36A and U36B. For heat radiation, transistors Q4 and Q3 are attached onto the PCB with screws.

## Power Supply Unit (X68-1760-00)

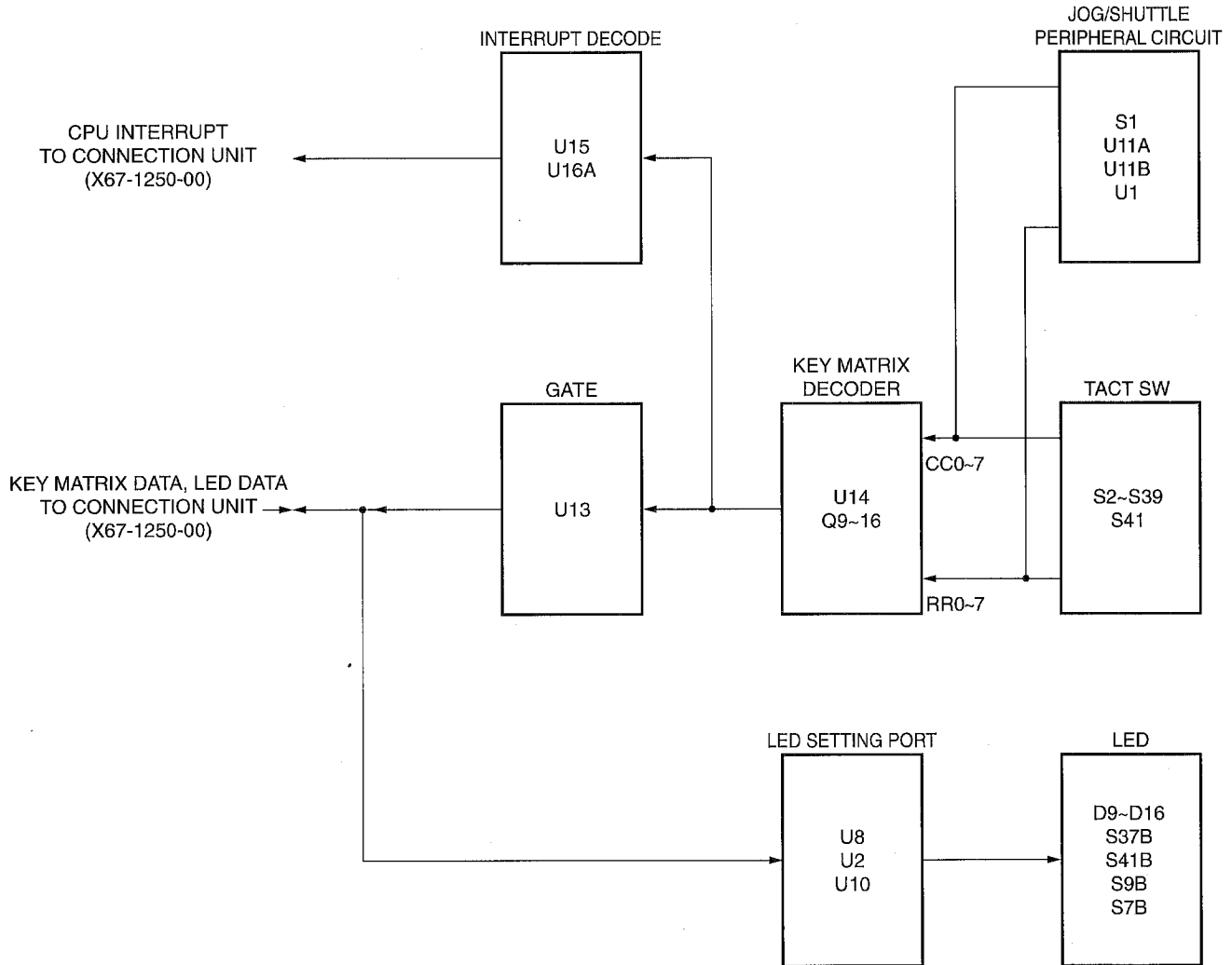
The power is supplied in 3 lines for the measurement circuitry, GEN circuitry and CPU/DSP circuitry.

As shown in the block diagram, the DSP Unit (X79-1230-00) is supplied with the +5 V regulating voltage and +16 V and -16 V non-regulating voltages. The Oscillator Unit (X71-1230-00) is supplied with the +5 V regulating voltage and the +20 V and -20 V non-regulating voltages. The Connection Unit (X67-1250-00) is supplied with the +5 V, +15 V and -15 V regulating voltages.



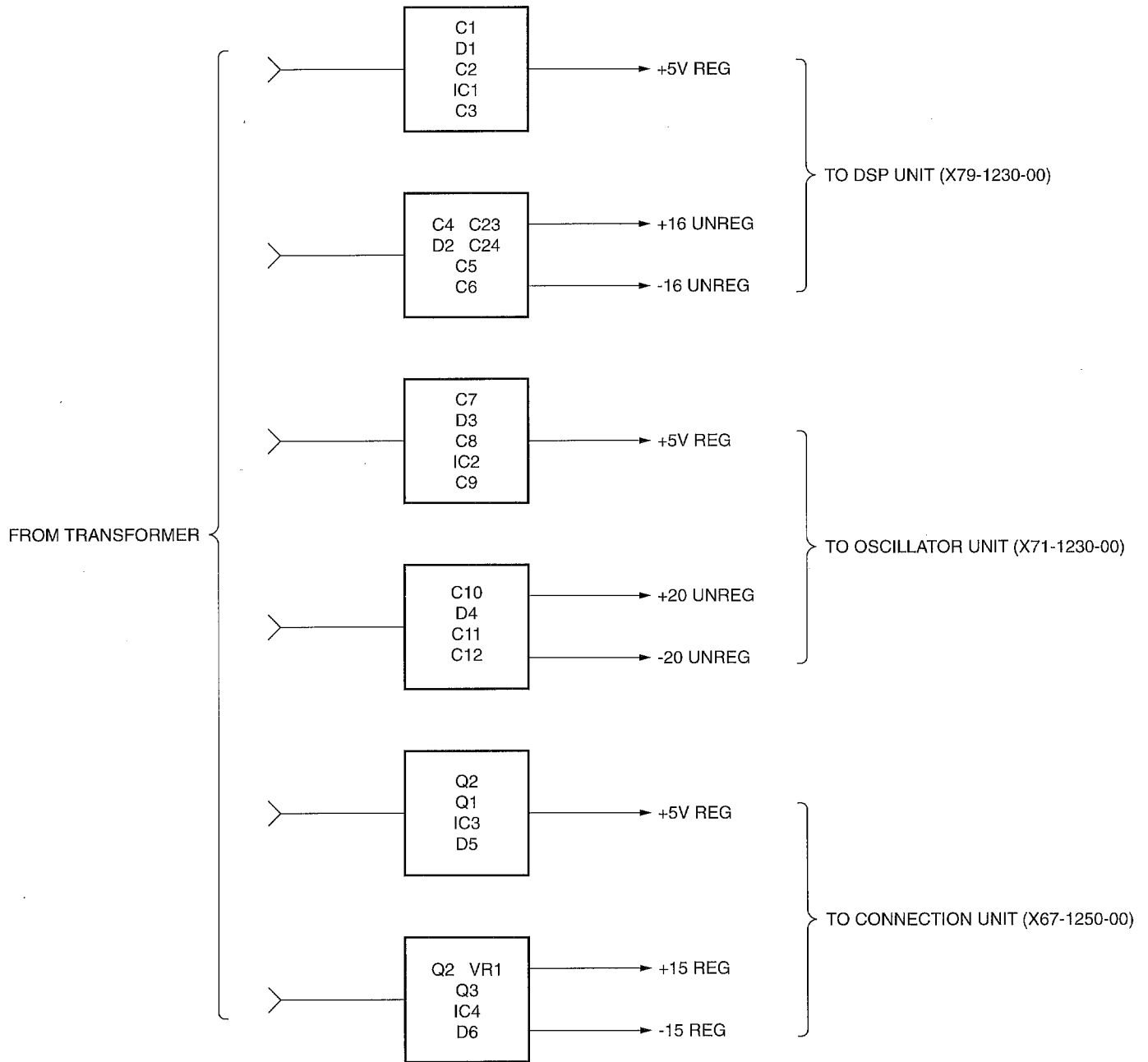
# BLOCK DIAGRAM

## PANEL UNIT (X66-1410-00)



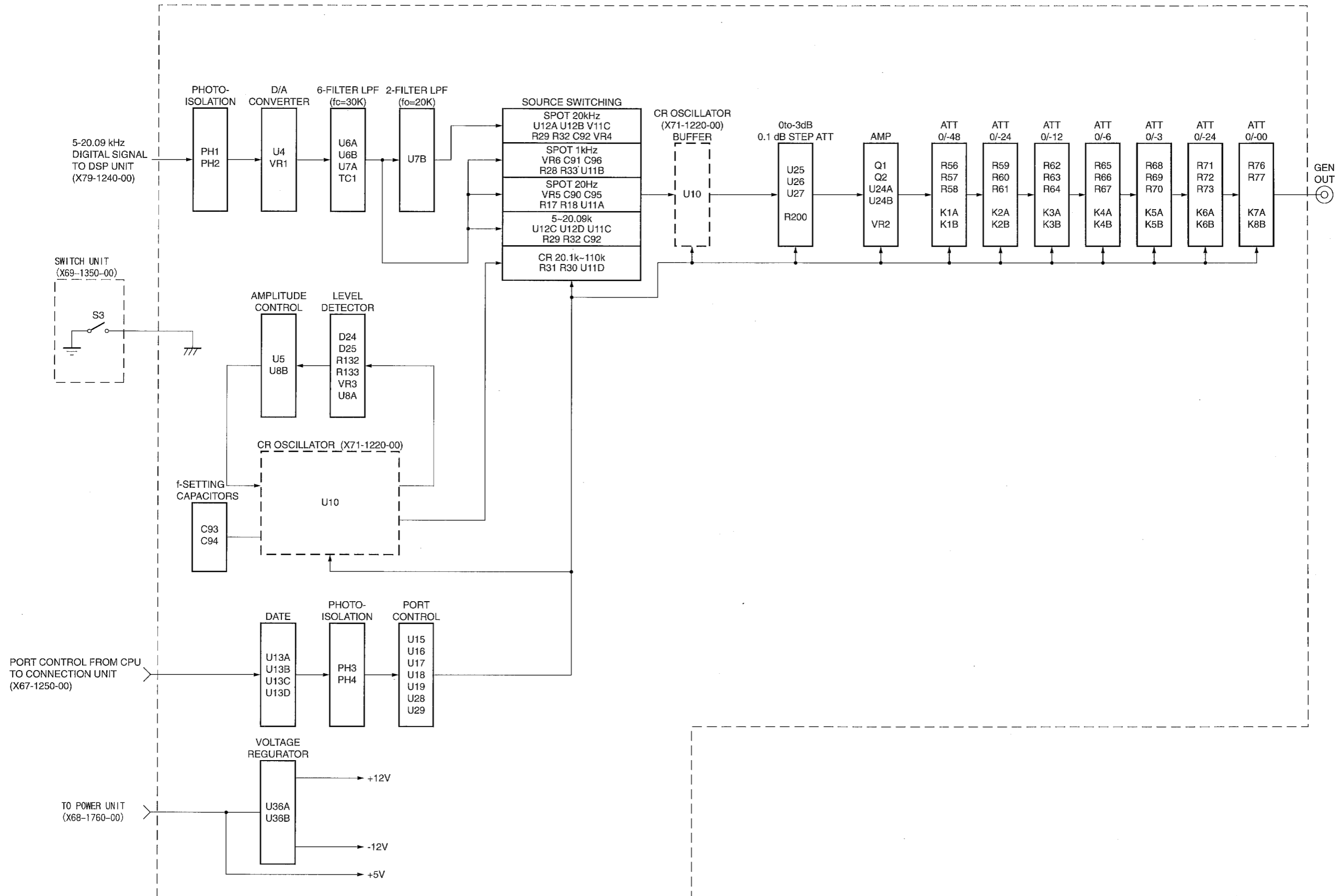
# BLOCK DIAGRAM

## POWER UNIT (X68-1760-00)



# BLOCK DIAGRAM

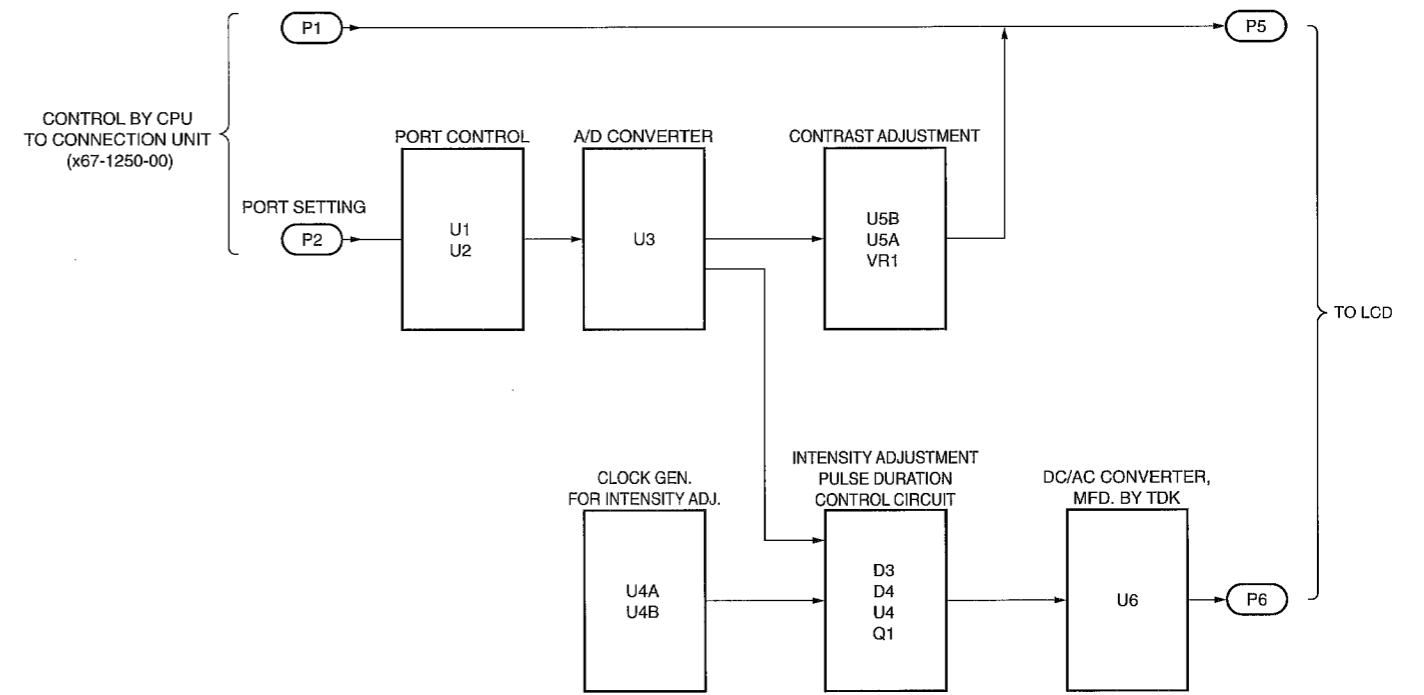
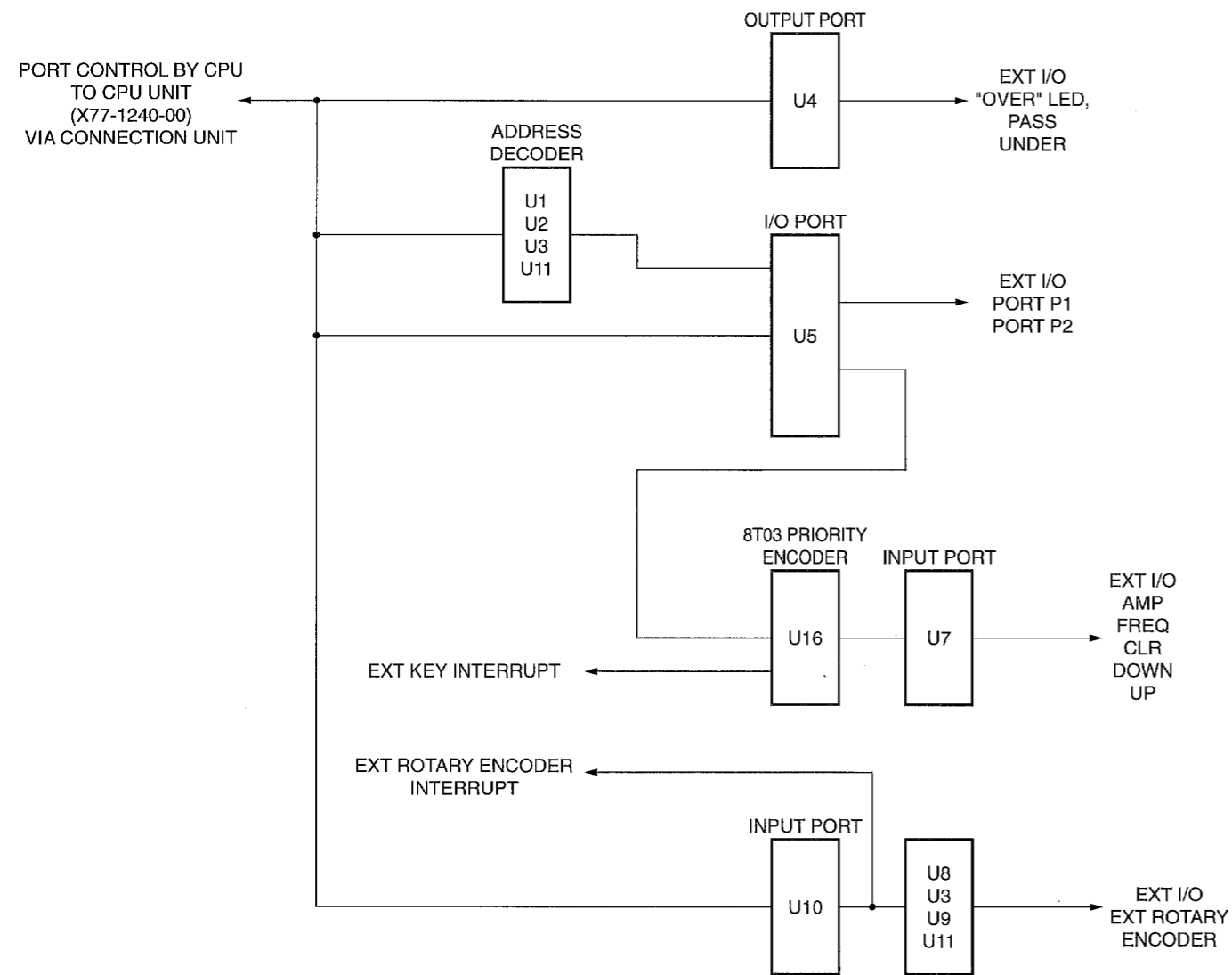
## OSCILLATOR UNIT (X71-1230-00)



# BLOCK DIAGRAM

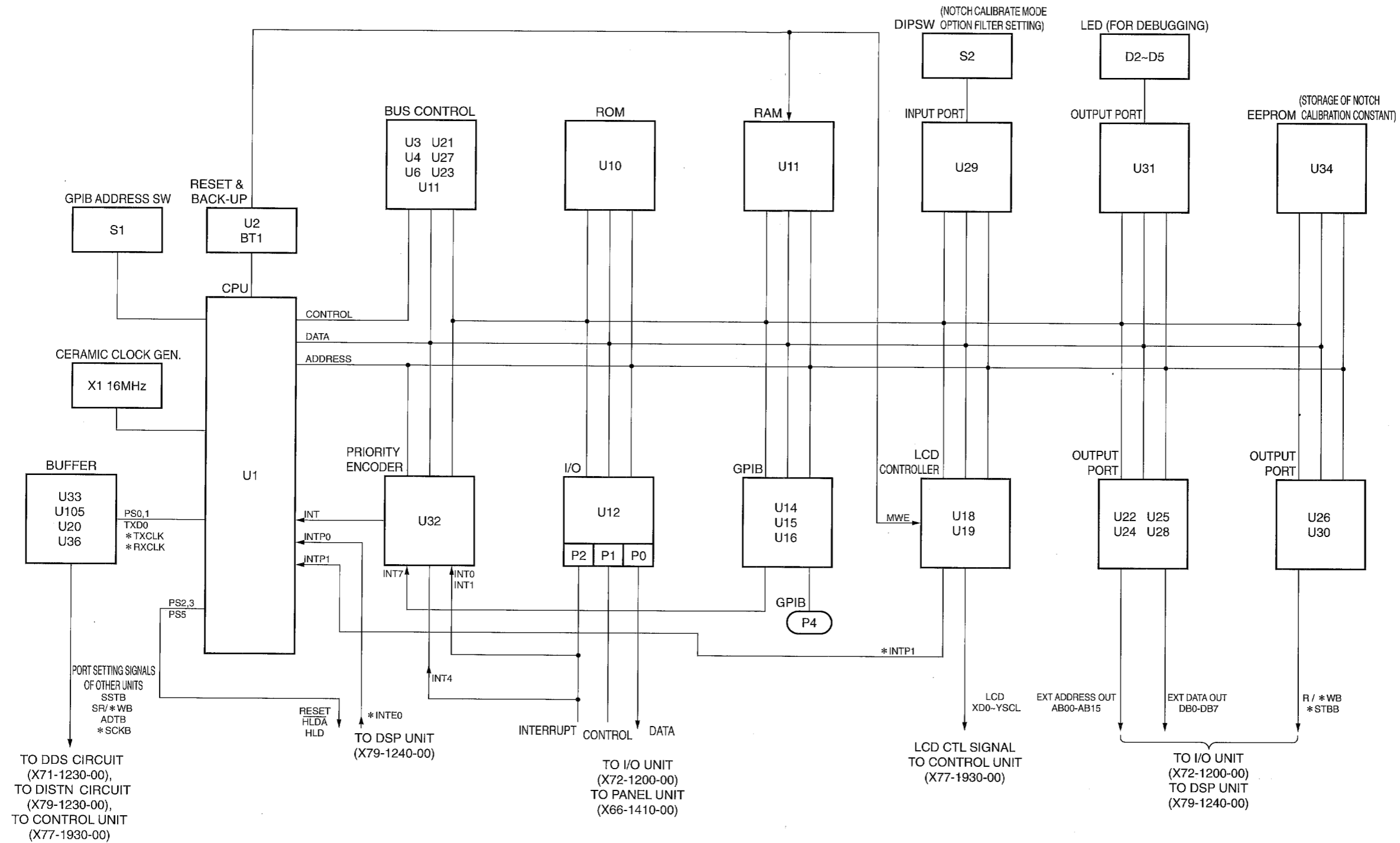
I/O UNIT (X72-1200-00)

CONTROL UNIT (X77-1930-00)



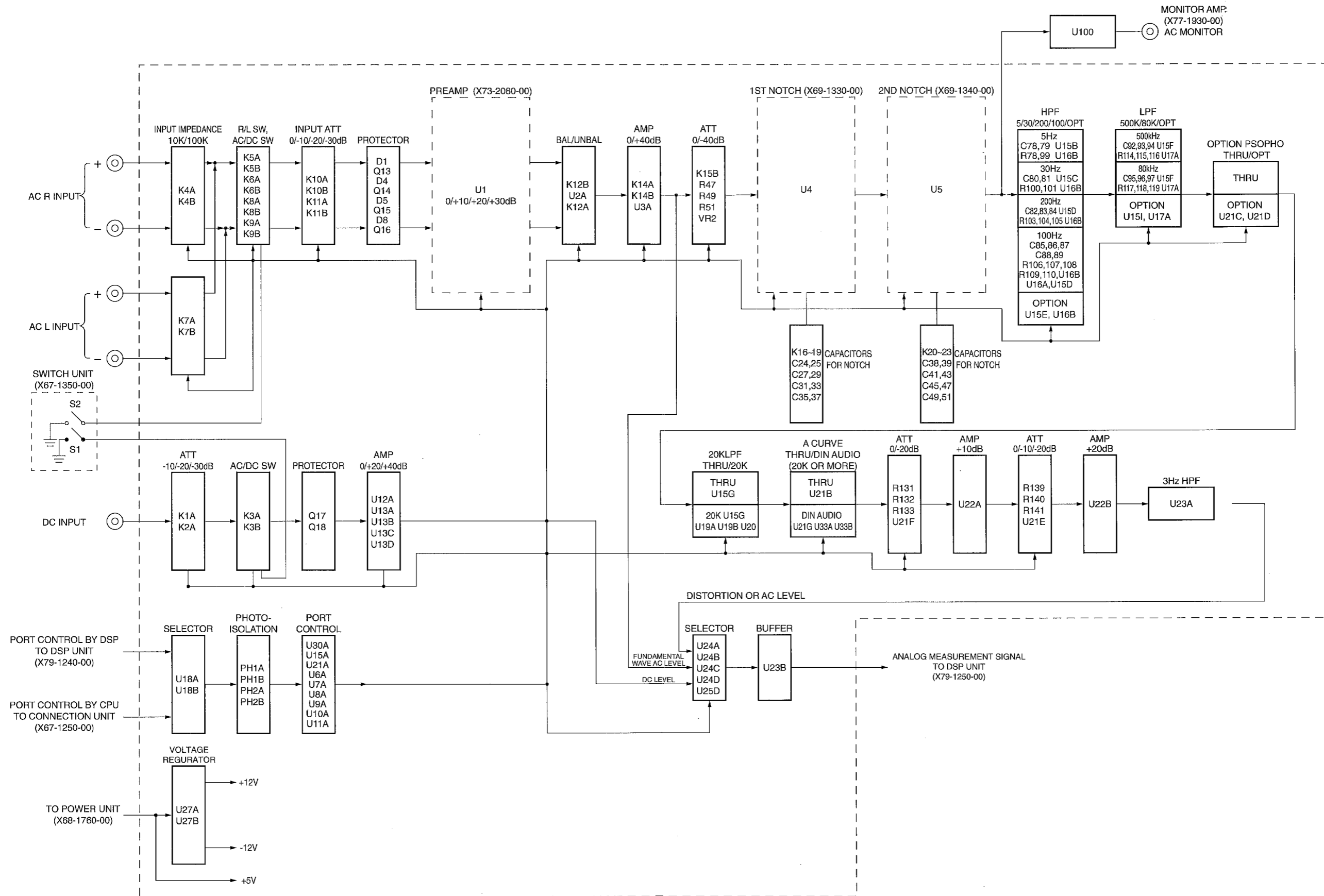
# BLOCK DIAGRAM

## CPU UNIT (X77-1940-00)



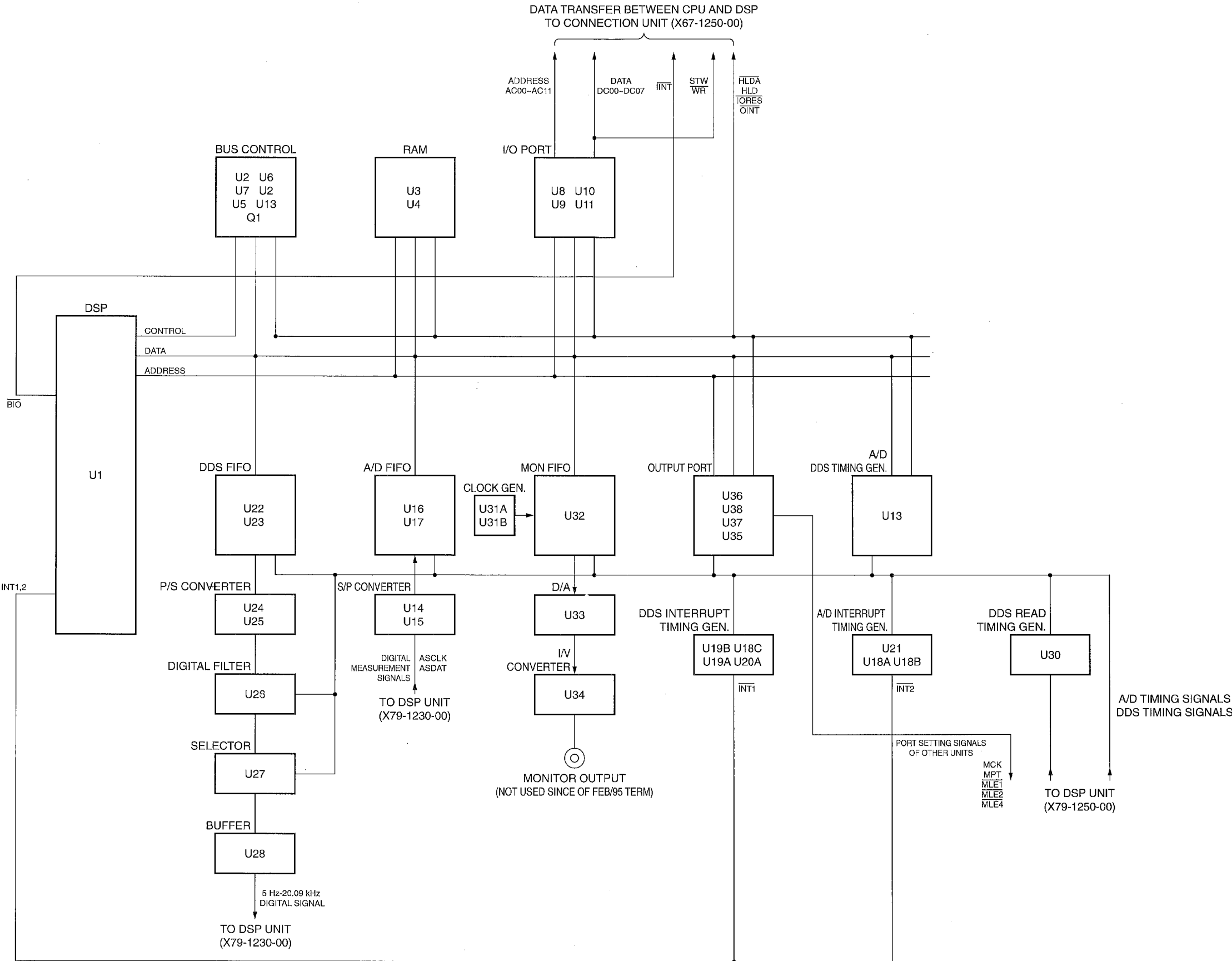
# BLOCK DIAGRAM

DPS UNIT (X79-1230-00)



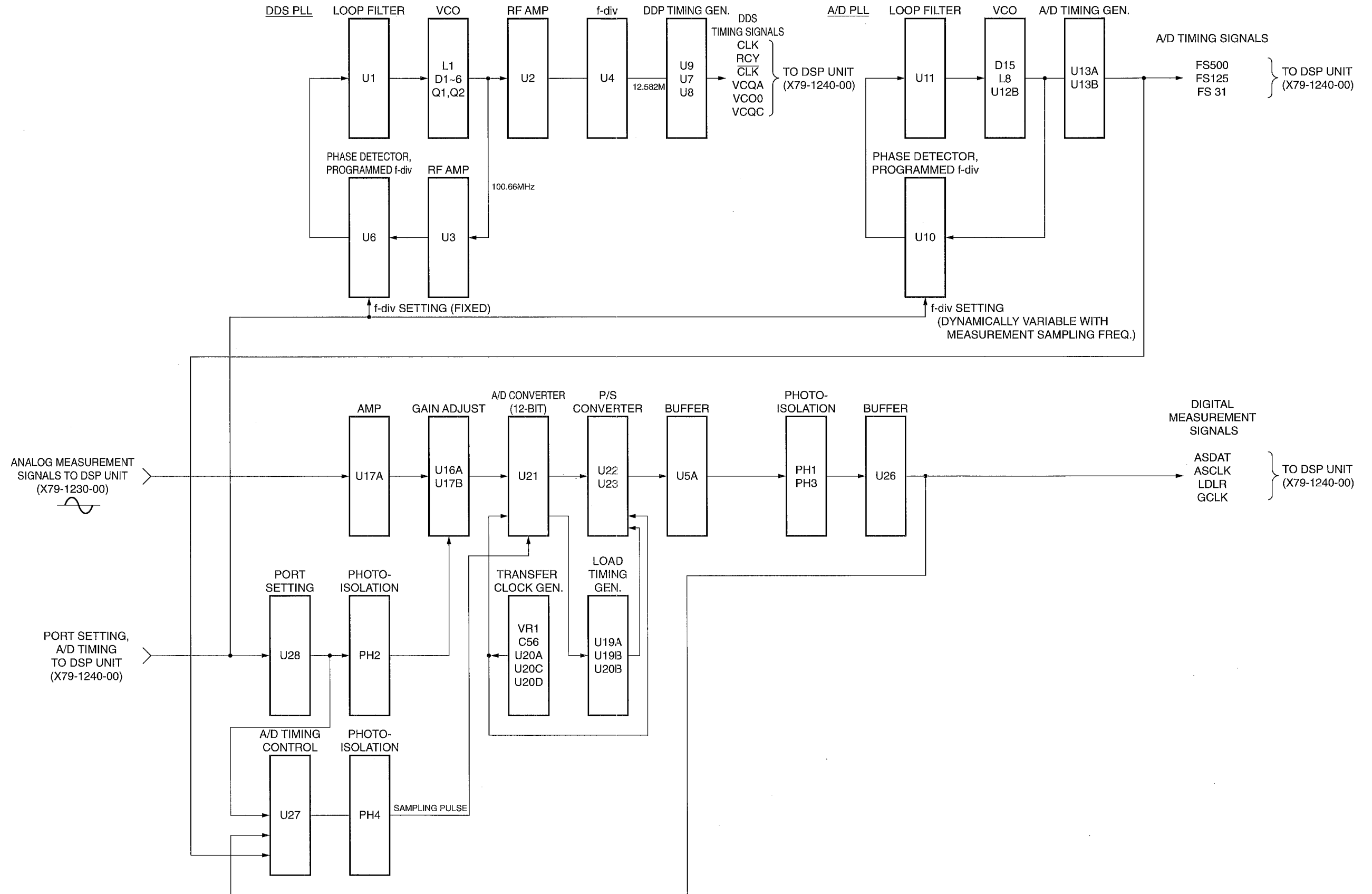
# BLOCK DIAGRAM

DPS UNIT (X79-1240-00)



# BLOCK DIAGRAM

DPS UNIT (X79-1250-00)





# ADJUSTMENT

To obtain the best performance, periodically calibrate the unit. Sometimes, only one mode need be calibrated, while at other times, all modes should be calibrated. When one mode is calibrated, it must be noted that the other modes may be affected. When calibrating all modes, perform the calibration in the specified sequence.

The following calibration required an accurate measuring instrument and an insulated adjusting flat blade screwdriver. If they are not available, contact your dealer. For optimum adjustment, turn the power on and warm up the scope sufficiently (more than 30 minutes) before starting. Before calibrating the scope, check the power supply voltage.

## TEST EQUIPMENT REQUIRED

The following instrument or their equivalent should be used for making adjustment.

Test Equipment	Model	Maker
Calibrator	5700A	Fluke
Thermal RMS Digital Multimeter	8506A	Fluke
Universal Counter	5335A	H.P.
Low Distortion Oscillator	AG15B	Shibasoku

## PREPARATION FOR ADJUSTMENT

Set the GEN, AC, DC and GND knobs located on the rear panel to the non-floating state before making adjustments.

# ADJUSTMENT

## Power voltage adjustment & check

Adjust V1 so that the voltage at TP1 of the Power Unit (X68-1760-00) is +15 V (between +14.95 and +15.05 V).

The supply voltage is adjusted only on the point specified above.

However, it is also necessary to check the voltages of the following units;

+5 V, +15 V and -15 V of Connection Unit (X67-1250-00), +5 V, +12.5 V, -12.5 V, +17.5 V and -17.5 V of Oscillator Unit (X71-1230-00).

+5 V, +12.5 V and -12.5 V of DSP Unit (X79-1230-00).

Voltage	Permissible Range
+5 V	+4.75 to +5.25 V
+15 V	+14.75 to +15.25 V
-15 V	-14.75 to -15.25 V
+12.5 V	+12.0 to +13.0 V
-12.5 V	-12.0 to -13.0 V
+17.5 V	+17.0 to +18.0 V
-17.5 V	-17.0 to -18.0 V

## LCD contrast adjustment

After setting the item system menu CTR5 to 5, adjust VR1 of the Control Unit (X77-1930-00) to a position making the contrast easiest to see (VR1 can be adjusted through the hold on the shield).

VR 1	LCD contrast adjustment	Use this VR for LCD contrast adjustment.
------	-------------------------	--

## A/D offset adjustment

The A/D offset adjustment sets the DC offset generated by the opamp inside the DSP Unit (X79-1250-00) to 0. The DSP Unit (X79-1250-00) can be adjusted from the rear of the shield case but, before the adjustment, it is first required to set slide switch S1 to the position with which pins ② and ③ are shorted (to the opposite side to TP7).

Set ITEM to DC-V measurement and the input range to 30 mV. After these settings, adjust V2 so that the displayed value is 0 V.

After adjustment, return S1 to the position with which pins ① and ② are shorted.

## Data transfer clock frequency adjustment

The clock for use in transfer of A/D converted data is adjusted by connecting a counter to TP5 and turning VR1 so that the clock frequency is 8.3 MHz.

For this adjustment, it is necessary to take the DSP Unit (X79-1250-00) out of the shield case.

VR 2	A/D offset adjustment	Use this VR for set DC offset to measurement A/D converter to 0.
VR 1	Data transfer clock frequency adjustment	Use this VR to adjust data transfer clock to 8.3 MHz.

## Oscillator adjustments

Set ITEM to GEN. The following adjustments are performed on the Oscillator Unit (X71-1230-00). Use the following procedures for the adjustments.

### (1) D/A converter 1 kHz output level adjustment

Set GEN to VARI of 1 kHz and level of 0 dBV, and adjust 78 so that voltage at V4 (PCM56P-K) pin ⑨ is 5.9 to 6.1 Vp-p.

### (2) Digital oscillator VARI output level adjustment

Set GEN to VARI of 1 kHz and level of 0 dBV, and adjust VR2 so that the GEN output read on the calibration voltmeter is 0 dBV  $\pm$ 0.1 dB when the GEN output is terminated with 600 ohms.

### (3) D/A converter distortion adjustment

Set GEN to VARI of 5 kHz and level of 0 dBV, and adjust VR1 so that the GEN output read on the calibration voltmeter is minimized (below 0.0048 V) when the GEN output is terminated with 600 ohms.

### (4) SPOT 1 output level adjustment

Switch GEN to SPOT 1, set level to 0 dBV and adjust VR5 so that the GEN output read on the calibration voltmeter is 0 dBV  $\pm$ 0.1 dB when the GEN output is terminated with 600 ohms.

### (5) SPOT 2 output level adjustment

Switch GEN to SPOT 2, set level to 0 dBV and adjust VR5 so that the GEN output read on the calibration voltmeter is 0 dBV  $\pm$ 0.1 dB when the GEN output is terminated with 600 ohms.

### (6) SPOT 3 output level adjustment

Switch GEN to SPOT 3, set level to 0 dBV and adjust VR4 so that the GEN output read on the calibration voltmeter is 0 dBV  $\pm$ 0.1 dB when the GEN output is terminated with 600 ohms.

### (7) CR oscillator output level adjustment

Set GEN to VARI of 25 kHz and level of 0 dBV, and adjust VR3 so that the GEN output read on the calibration voltmeter is 0 dBV  $\pm$ 0.1 dB when the GEN output is terminated with 600 ohms.

# ADJUSTMENT

**(8) DDS frequency response adjustment**

Set the GEN level to 0 dBV, and adjust TC1 so that the level read on the calibration voltmeter when the frequency is 20 kHz is identical to the level when the frequency is 1 kHz. Check that the level deviation in the range from 20 Hz to 20 kHz is within the specification of  $\pm 0.05$  dB (reference: 1 kHz).

**(9) CR oscillator frequency response check**

Set the GEN level to 0 dBV and, on the calibration voltmeter, check that the frequency level deviations in the range from 20.09 kHz to 110 kHz are within the specification of  $\pm 0.5$  dB (reference: 1 kHz).

**(10) Attenuator level check**

Set the GEN frequency to 1 kHz, vary the level as described below and check that the output accuracy is within the specification ( $\pm 0.5$  dB at no less than -40 dBV,  $\pm 0.8$  dB at below 40 dBV).

Between 14.0 and 11.0 dB, check at all levels in 0.1 dB steps.

Below 11.0 dB, check at levels of 10.9 dB, 7.9 dB, 1.9 dB, -10.1 dB, -34.1 dB and -81.2 dB.

**(11) Distortion check**

Set the GEN level to 14 dBV and, on the calibration voltmeter, check that distortions at different frequencies meet the specifications below.

Between 5 Hz and 110 kHz, THD+N should be no more than 0.01% with 600-ohm termination.

Between 20 Hz and 20 kHz, THD+N should be no more than 0.005% with 600-ohm termination and 80k LPF switched ON.

With SPOT 1 and SPOT 2, THD+N should be no more than 0.001% with 600-ohm termination and 80k LPF switched ON.

With SPOT 3, THD should be no more than 0.001% with 600-ohm termination and 80k LPF switched ON.

**Oscillator adjustment points**

R 78	D/A converter 1 kHz output level	Make output at U4 pin ⑨ stable (at $f = 1$ kHz). This influences distortion at $f = 110$ kHz, distortion of SPOT 3, etc. This influences all levels of digital oscillator.
VR 2	Digital oscillator VARI output level	Use this VR to adjust output level of digital oscillator VARI.
VR 1	D/A converter output distortion adjustment	Use this VR to minimize output distortion of D/A converter U4.
VR 5	SPOT 1 output level	Use this VR to adjust output level of SPOT 1. Re-adjustment is required after adjustments of R78 and/or VR2.
VR 6	SPOT 2 output level	Use this VR to adjust output level of SPOT 2. Re-adjustment is required after adjustments of R78 and/or VR2.
VR 4	SPOT 3 output level	Use this VR to adjust output level of SPOT 3. Re-adjustment is required after adjustments of R78 and/or VR2.
VR 3	CR oscillator output level	Use this V to adjust output level of CR oscillator. Re-adjustment is required after adjustment of VR2.
TC 1	DDS frequency response adjustment	Use this trimmer to adjust digital oscillator so that it meets frequency response specification in frequency range from 5 Hz to 20 kHz.

# ADJUSTMENT

## Adjustments for DC-V measurement

Switch ITEM to DC-V. The following adjustments are performed on the DSP Unit (X79-1230-00). Use the following procedures for the adjustments.

### (1) DC offset adjustment of output amp U23B

Set the range to 30 mV, connect TP3 to GND (TP20) and adjust V13 so that the displayed value is in the range of (between -0.01 and +0.01 mV).

### (2) DC offset adjustment of DC preamp U12A

Set the range to 30 mV and shot- the DC inputs on the rear panel. Then adjust VR14 so that the displayed value is in the range of (between -0.03 and +0.03 mV).

### (3) 3 V range adjustment (Gain adjustment of output amp U23B)

Set the range to 3 V, set the DC calibrator output to 3.00 V and apply it to the DC input. Then adjust VR6 so that the displayed value is between 2.997 and 3.003 V.

### (4) 300 mV range adjustment

Set the range to 300 mV, set the DC calibrator output to 300 mV and apply it to the DC input. Then adjust VR7 so that the displayed value is between 299.7 and 300.3 mV.

### (5) 30 mV range adjustment

Set the range to 30 mV, set the DC calibrator output to 30.0 mV and apply it to the DC input. Then adjust VR8 so that the displayed value is between 29.97 and 30.03 mV.

### (6) 30 V range adjustment

Set the range to 30 V, set the DC calibrator output to 30.0 V and apply it to the DC input. Then adjust VR3 so that the displayed value is between 29.97 and 30.03 V.

### (7) 100 V range adjustment

Set the range to 100 mV, set the DC calibrator output to 100.0 V and apply it to the DC input. Then adjust VR4 so that the displayed value is between 99.9 and 100.1 V.

## Adjustment points in DC-V measurement

VR 13	DC offset adjustment of output amp U23B	Use this VR to adjust DC offset produced in U23B to 0. This adjustment affects AC measurement as well as DC measurement.
VR 14	DC offset adjustment of DC preamp U12A	Use this VR to adjust DC offset produced in U12B to 0.
VR 6	3 V range adjustment (Gain adjustment of output amp U23B)	Use this VR to adjust gain of DC-V 3 V range. This adjustment affects other ranges in DC-V measurement as well as all gains in all ranges in AC-V measurement and distortion measurement.
VR 7	300 mV range adjustment	Use this VR to adjust gain of DC-V 300 mV range. After adjusting VR7, it is necessary to re-adjust 30 mV range gain using VR8.
VR 8	30 mV range adjustment	Use this VR to adjust gain of DC-V 30 mV range.
VR 3	30 V range adjustment	Use this VR to adjust -20 dB attenuator of DC-V 30 V range. After adjusting VR3, it is necessary to re-adjust 100 V range using VR4.
VR 4	100 V range adjustment	Use this VR to adjust -10 dB attenuator of DC-V 100 V range.

# ADJUSTMENT

## Adjustments for AC-V measurement

Switch ITEM to AC-V. The following adjustments are performed on the DSP Unit (X79-1230-00). (The adjustments for DC-V measurement should be completed before proceeding to the following.)

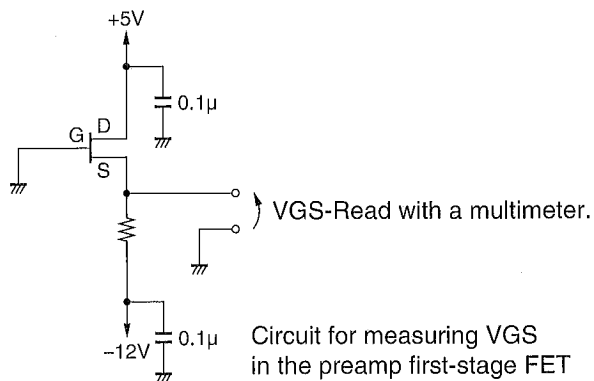
### (1) First, check the DC offset in the preamp output.

Set the input mode to L CH UNBAL and short-circuit the input.

Set the range to 100 mV fixed and check that the DC offset in the output from preamp U1 pin ② is no more than 0.3 V.

Then set input mode to BAL, short-circuit the BAL input and check that the DC offset in the output from preamp U1 pin ③ is no more than 0.3 V.

If the voltage is larger, hunting or delay in response may occur in AC measurements. In this case, adjust the pair of Q1 and Q2 (UNBAL input) or Q3 and Q4 (BAL input) in preamp U1 (X73-2080-00). (The difference in VGS of the pair should be no more than 3 mV.)



### (2) Set the input impedance to 100-kilohms, switch all filters OFF and switch the input mode to UNBAL, L CH.

### (3) AC-V gain adjustment

Set the range to 3 V, AC calibrator output to 3.0 Vrms and its frequency to 25 kHz, and apply the output to the L CH input. Then adjust VR5 so that the displayed value is between 2.998 and 3.002 V.

### (4) 1 kHz gain adjustment

Use the same 3 V range but switch the 20k LPF ON. Set the CA calibrator output to 3.0 Vrms and the frequency to 1 kHz. Then adjust VR10 so that the displayed value is between 2.998 and 3.002 V. After adjustment, switch the 20k LPF OFF.

### (5) 30 mV range adjustment

Set the range to 30 nV, AC calibrator output to 30.0 mV and its frequency to 1 kHz. Then adjust VR5 again so that the displayed value is between 29.98 and 30.02 mV.

### (6) -40 dB attenuator gain adjustment

Set the range to 3 V, AC calibrator output to 3.0 Vrms and its frequency to 1 kHz. Then adjust VR2 so that the displayed value is between 2.998 and 3.002 V.

### (7) AC amp DC offset adjustment

Short-circuit the L CH input, set the range to 300  $\mu$ V, and connect TP1 to TP18 (GND). Then, while observing TP10 with the oscilloscope, adjust VR9 so that the DC level becomes 0 V. After adjustment, disconnect TP1 from TP18.

Then, while observing TP12 with the oscilloscope, adjust V11 so that the DC level is 0 V.

Then, while observing TP13 with the oscilloscope, adjust VR12 so that the DC level is 0 V.

### (8) -10 dB attenuator frequency response adjustment

Set the range to 3 V, AC calibrator output to 3.0 Vrms and its frequency to 210 kHz, and record the level measured under this condition.

Next, switch the range to 10 V and adjust TC2 so that the current level is equal to the level measured above. Then vary the frequency between 5 Hz and 210 kHz and check that the frequency response at 110 kHz or more is within  $\pm 10\%$  of that at 1 kHz.

### (9) -20 dB attenuator frequency response adjustment

Set the range to 10 V, AC calibrator output to 10.0 Vrms and its frequency to 210 kHz, and record the level measured under this condition.

Next, switch the range to 30 V and adjust TC1 so that the current level is equal to the level measured above. Then vary the frequency between 5 Hz and 210 kHz and check that the frequency response at 110 kHz or more is within  $\pm 10\%$  of that at 1 kHz.

### (10) -30 dB attenuator frequency response check

Set the range to 100 V and AC calibrator output to 10.0 Vrms. Then vary the frequency between 5 Hz and 210 kHz and check that the frequency response at 110 kHz or more is within  $\pm 15\%$  of that at 1 kHz.

### (11) Adjustment with BAL input

Switch the input mode to BAL, set the range to 100 mV, and short-circuit the UNBAL input (upper connector). Apply the AC calibrator output with a level of 100 mVrms and frequency of 1 kHz to the BAL input (lower connector). Then adjust VR1 so that the displayed value is between 99.93 and 100.70 mV.

### (12) BAL attenuator frequency response adjustment

With the same input set-up as (11) above, adjust the -10 dB attenuator with TC4 and the -20 dB attenuator with -20 dB using the same procedures as (8) to (10) above.

# ADJUSTMENT

## (13) Range accuracy check

Set the input mode to UNBAL and check that the measuring accuracy at the full-scale input is within specification in each of the ranges from 300 mV to 100 V.

## (14) HPF frequency response check

Set the range to 1 V and the AC calibrator output to 1 Vrms. Then check the frequency response of each HPF based on REL measurement with respect to 1 kHz. (Check by referring to the specifications of the built-in filters in the test result report.)

## (15) LPF frequency response check

Set the range to 1 V and the AC calibrator output to 1 Vrms. Then check the frequency response of each LPF based on REL measurement with respect to 1 kHz. (Check by referring to the specifications of the built-in filters in the test result report.)

## (16) PSOPHO frequency response check

Set the range to 1 V and the AC calibrator output to 1 Vrms. Then check the frequency response of each PSOPHO based on REL measurement with respect to 1 kHz. (Check by referring to the specifications of the built-in filters in the test result report.)

## AC-V monitor output adjustment

Proceed to this adjustment after having completed all adjustments of the DSP Unit (X79-1230-00).

Set the ITEM to AC-V measurement, the range to 1 V fixed and the input mode to UNBAL. Set the AC calibrator output to 1 Vrms and 1 kHz and apply it to AC IN. Then adjust V100 of the Control Unit (X77-1930-00) from the rear of the PCB so that the monitor output on the rear panel outputs 1 Vrms (between 995 and 1005 mV) with 600-ohm termination.

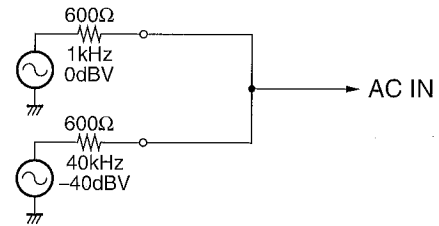
VR 100	Monitor output level	Use this VR to adjust monitor output level in AC-V measurements.
--------	----------------------	--

## (17) Residual distortion check

At each frequency in each frequency range, check that the residual distortions (THD+N, THD0 are within specifications.

## (18) Distortion check

Mix 1 kHz, 0 dB signal and 40 kHz, -40 dBV signal externally (use two oscillators. The distortions should be no more than 0.1%). Apply the mixed signal and check that the distortions are between -39 and -41 dB with both THD+N and THD.



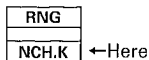
## Adjustment points for AC-V measurement

VR 5	AC-V gain adjustment	Use this VR to align screen display with actual input level by adjusting gain of amp U22B.
VR 10	1 kHz gain adjustment	Use this VR to align screen display when 20k LPF is ON with actual input level by adjusting gain of amp U19B.
VR 2	-40 dB attenuator gain adjustment	Use this VR to align screen display in ranges which use -40 dB attenuator with actual input level.
VR 9	DC offset adjustment	Use this VR to set DC offset of amp U16B to 0.
VR 11	DC offset adjustment	Use this VR to set DC offset of amp U22A to 0.
VR 12	DC offset adjustment	Use this VR to set DC offset of amp U22A to 0.
TC 2 TC 4	-10 dB attenuator frequency response adjustment	Use these trimmers to adjust frequency response of -10 dB input attenuator (10 V and 100 V ranges). TC2: UNBAL. TC4: BAL.
TC 1 TC 3	-20 dB attenuator frequency response adjustment	Use these trimmers to adjust frequency response of -20 dB input attenuator (30 V and 100 V ranges). TC1: UNBAL. TC3: BAL.
VR 1	BAL amp gain adjustment	Use this VR to align screen display with BAL input and that with UNBAL input.

# ADJUSTMENT

## Notch filter calibration

Set S1 bit ⑧ on the CPU Unit (X77-1940-00). When power is switched ON with this condition, the calibration menu will be displayed on AC-V measurement menu 7-3.



Fix the range at 1 V, then select the calibration menu. First select F4=1st to calibrate the frequency of the 1st notch filter, then select F3=100 Hz as the calibration frequency range. Apply 1 Vrms, 100 Hz sine wave externally and vary the frequency linearly in the range of 100 Hz  $\pm$ 10% to find the point where dipping (decrease in level) occurs. When F5=ENTER is pressed once at the point with most noticeable dipping, the frequency is displayed on the screen; press F5= ENTER again now. The frequency displayed on the screen is captured in the bottom left of the screen and the value is written in the EEPROM of the CPU Unit. (This value will not be cleared by reset operations.) Similarly to above, by selecting the calibration frequency range with F3 and notch filter with F4, calibrate frequencies in order of 100 Hz/2nd, 1 kHz/1st, 1 kHz/2nd, 10 Hz/1st, 10 kHz/2nd, 100 kHz/1st and 100 kHz/2nd. The 100 kHz calibrations are to be performed with 30 mV input level in the 30 mV range.

After the notch filter calibration, set S1 bit ⑧ back to ON and turn power ON again.

"NCH.K" will disappear from AC-V measurement menu 7-3.

## Reset operations

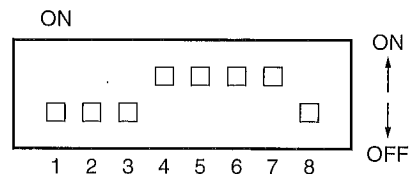
### Product reset

After setting bit ⑧ of S1 on the CPU Unit (X77-1940-00) to OFF, turn power ON while holding the SHIFT key depressed; the system will boot while changing screens. This is the product reset operation. Use care in this operation for it clears the sequence memory contents set by the user (but the notch filter calibration frequencies are not cleared). After servicing of a unit, etc., this reset operation should be performed always after obtaining approval of the user. Also do not forget to set S1 bit ⑧ back to ON again.

### SHIFT-reset

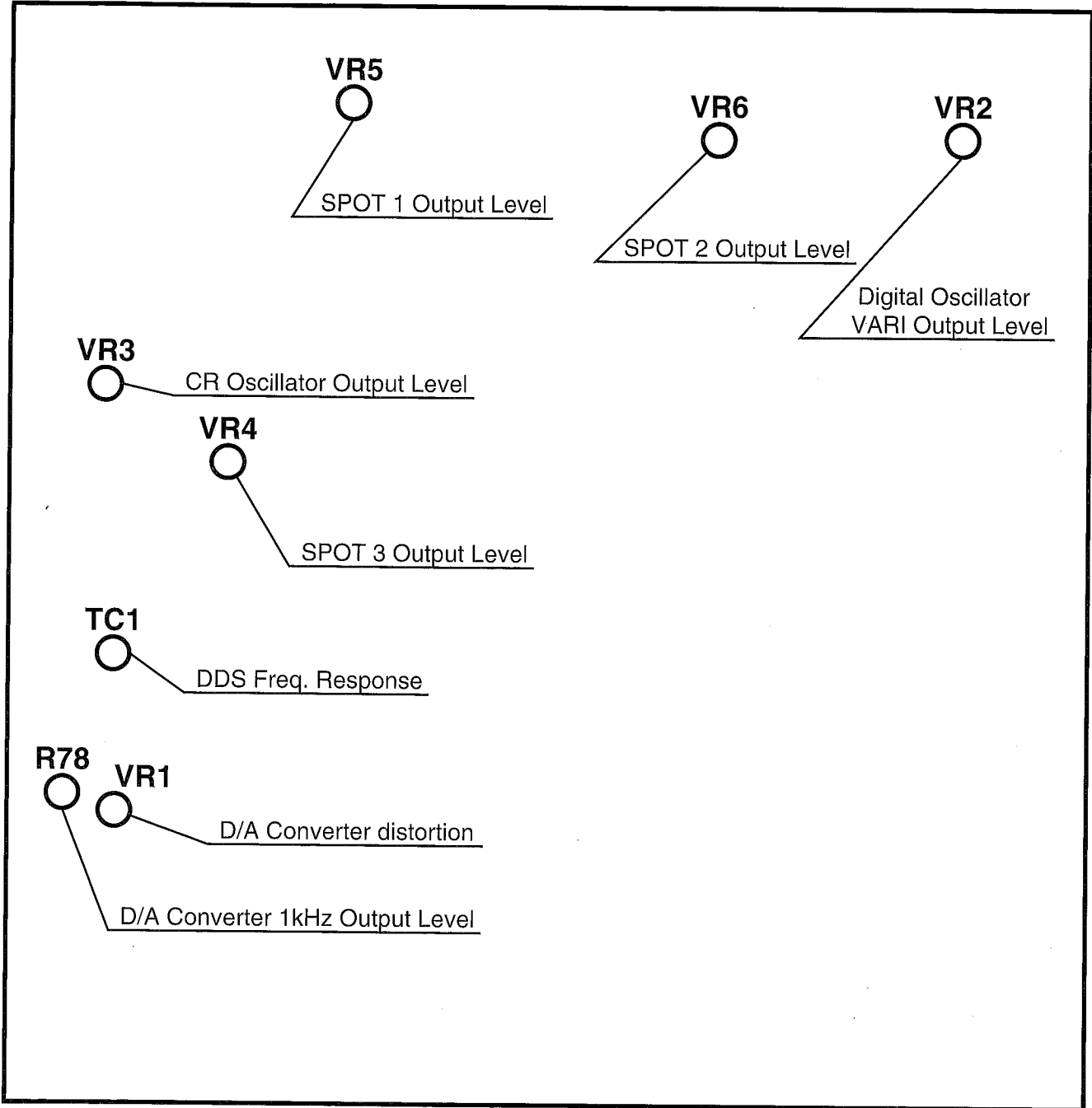
If power is turned on while holding the SHIFT key depressed by leaving bit ⑧ of S1 on the CPU Unit (X77-1940-00) to ON (factory setup before shipment), the system will also boot while changing screens. This operation is the SHIFT-reset operation. This operation does not clear the sequence memory contents but it simply resets the ITEM setup to the initial condition.

CPU S1



# ADJUSTMENT

Oscillator Unit (X71-1230-00)

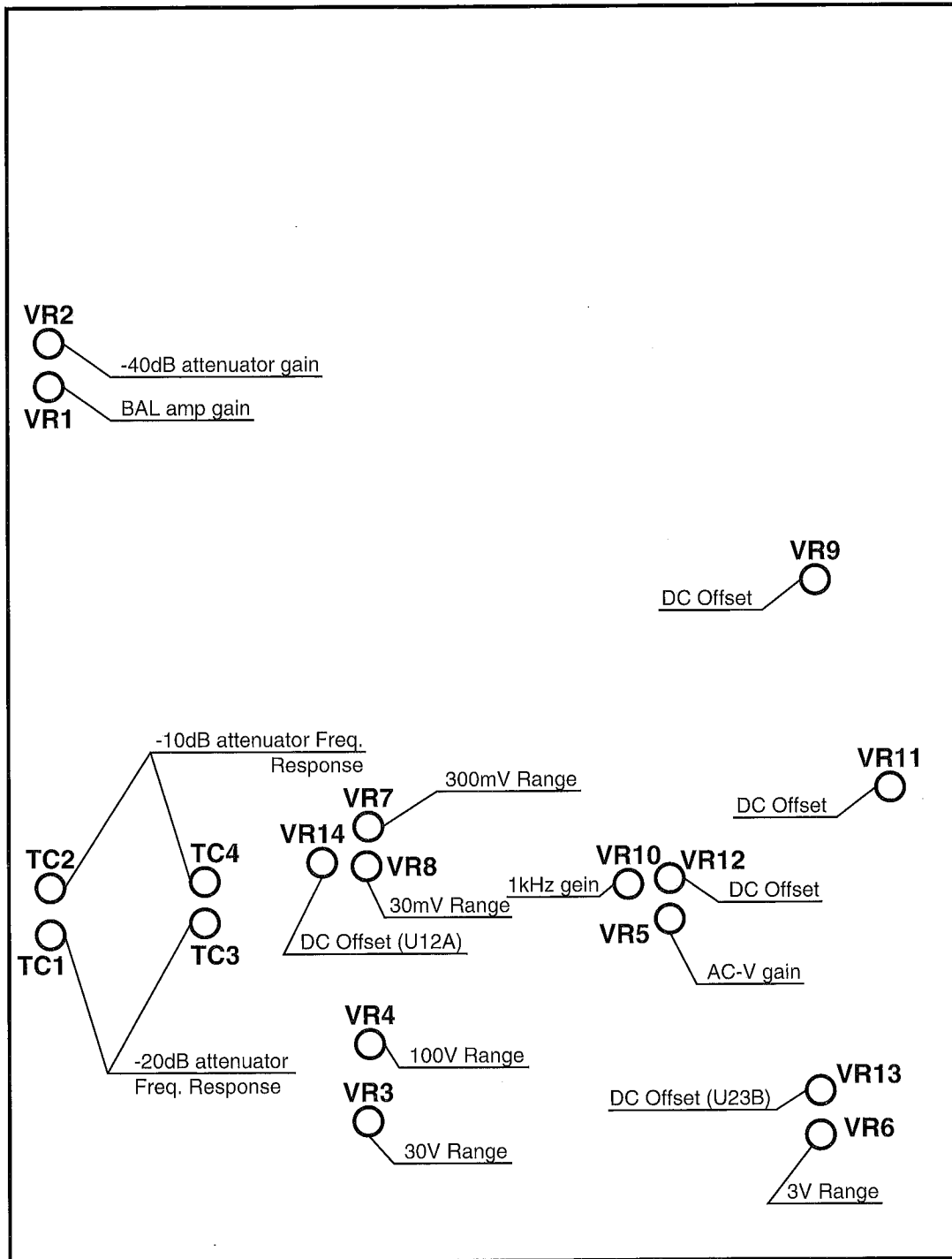


FRONT



# ADJUSTMENT

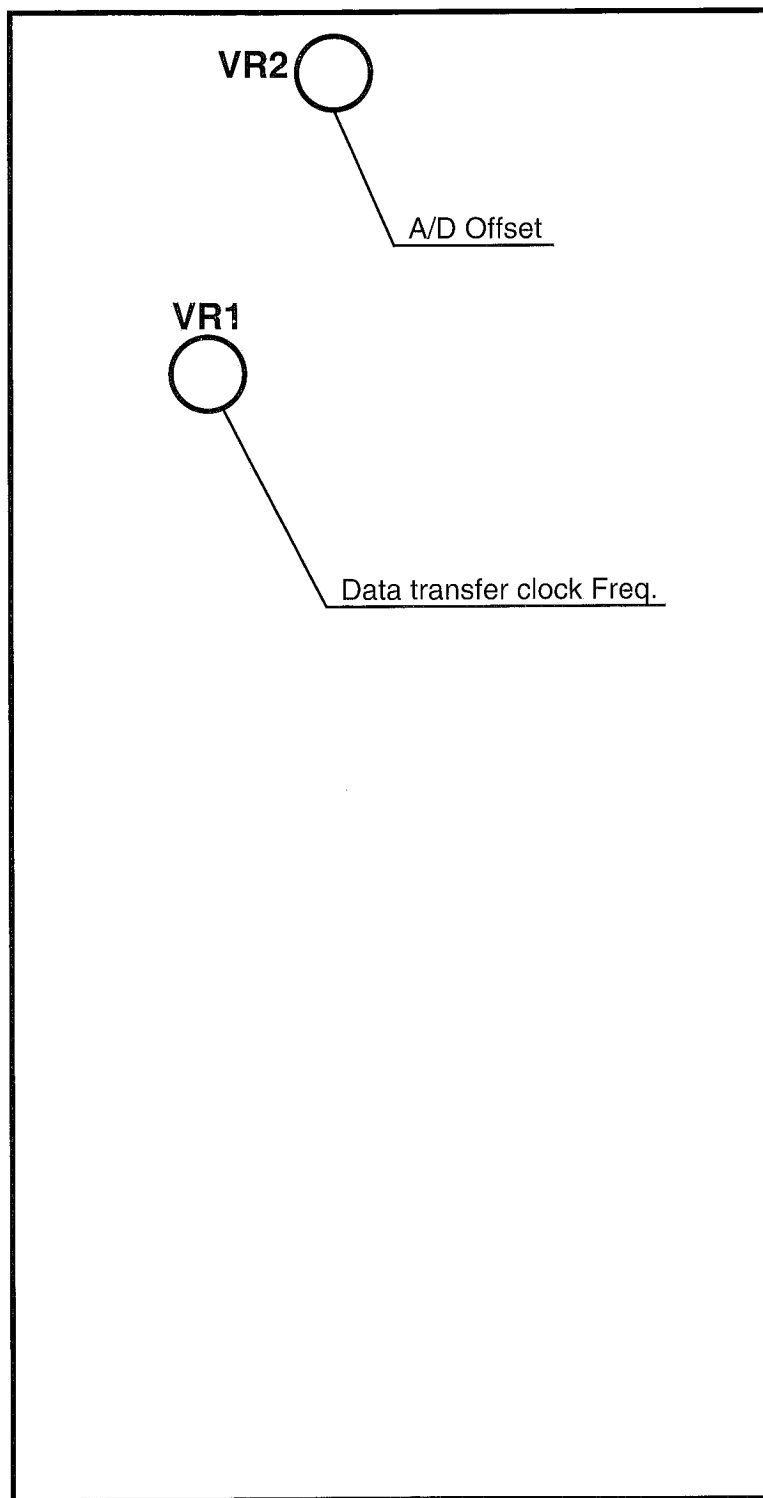
DPS Unit (X79-1230-00)



↓  
FRONT

# ADJUSTMENT

DPS Unit (X79-1250-00)

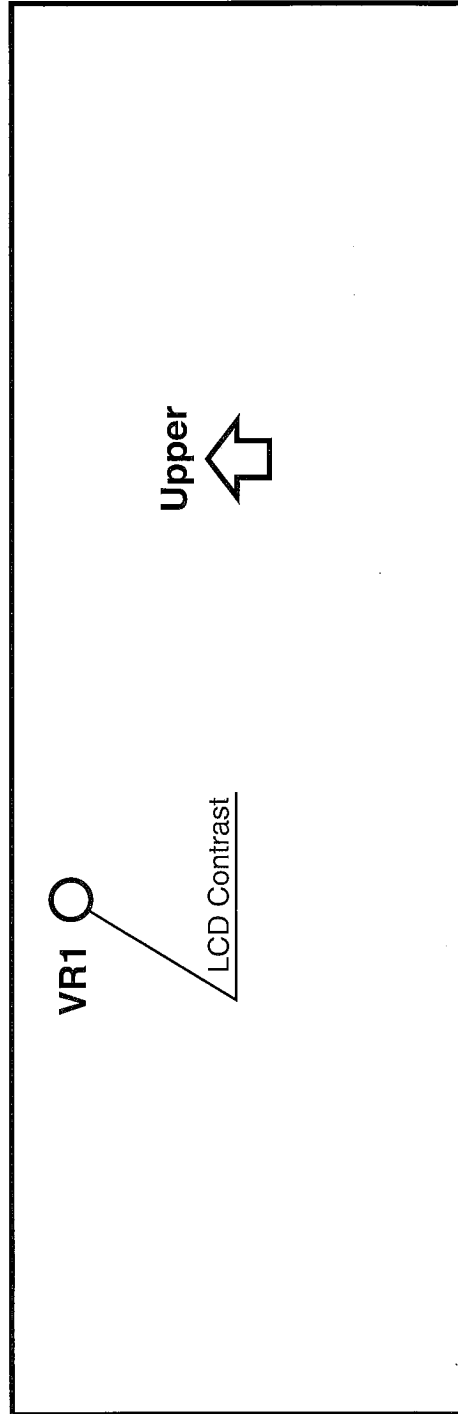
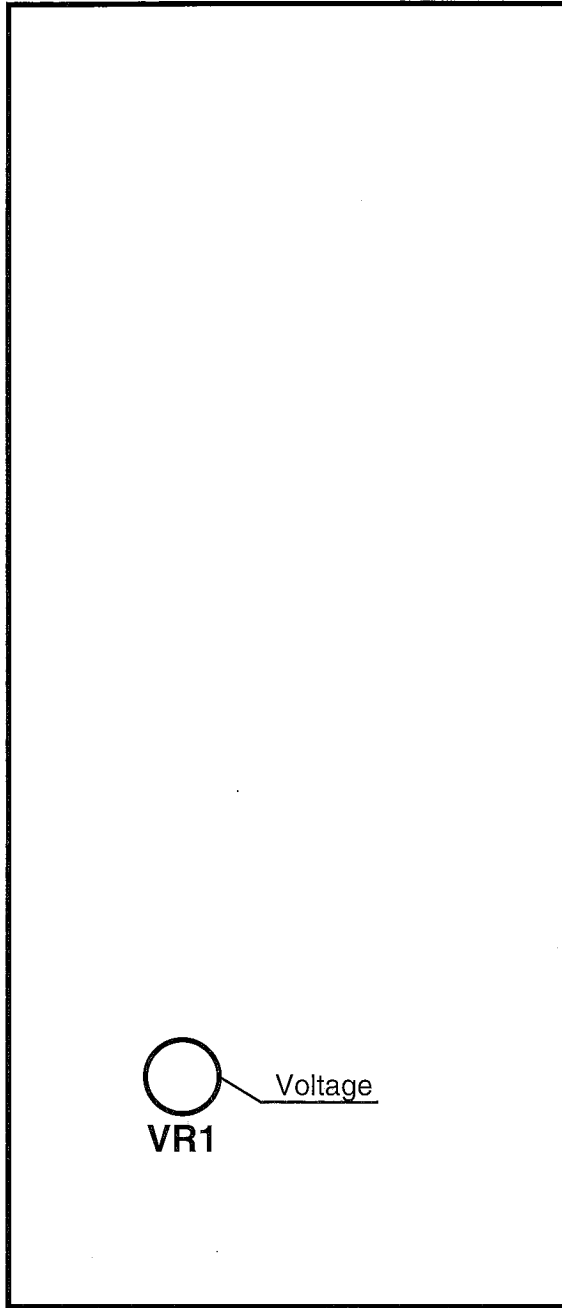


FRONT

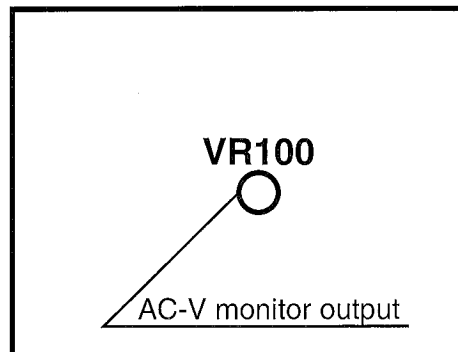
# ADJUSTMENT

Power Unit (X68-1760-00)

Control Unit (X77-1930-00)



  
FRONT



REAR PANEL

# TROUBLE SHOOTING

	Symptom	Possible Cause	Check Items
1	LCD display or LEDs do not light.	Fuse is blown. Transformer is abnormal. Power Unit failure.	<ul style="list-style-type: none"> <li>● Check fuse.</li> <li>● Check output from secondary side of transformer.</li> <li>● Check output voltage from Power Unit.</li> </ul>
2	After SHIFT-reset operation, LCD screen remains white or LEDs light abnormally, key operations do not occur and GEN output is not output.	CPU Unit (X77-1990-00) is abnormal.	<ul style="list-style-type: none"> <li>● CPU may not be functioning. (Check operations of CPU clock, data bus and address bus.)</li> </ul>
3	Key operations, LED lighting and GEN output are normal, but LCD screen is abnormal.	CPU Unit (X77-1990-00) is abnormal.	<ul style="list-style-type: none"> <li>● Check LCD controller V18 for abnormality.</li> </ul>
		Control Unit (X77-1930-00) is abnormal.	<ul style="list-style-type: none"> <li>● Perform SHIFT-reset and check voltages at D/A converter V3 pins ② or ① and after.</li> </ul>
4	After SHIFT-reset, LCD screen shows a menu but key operations are not accepted. GEN output is also abnormal.	DSP Unit (X79-1240-00) is abnormal.	<ul style="list-style-type: none"> <li>● DSP may not be functioning. (Check operations of DSP clock, data bus and address bus.)</li> <li>● Check if data transfer between CPU ↔ DSP is normal. (Check operations of <math>\overline{\text{TORES}}</math>, <math>\overline{\text{HLDA}}</math>, <math>\overline{\text{HLD}}</math>, <math>\overline{\text{OINT}}</math> and <math>\overline{\text{IINT}}</math> as well as those of V3, V4 and U8 to U12.)</li> <li>● If U1, U2, U13 and U30 use sockets, check contact failure.</li> </ul>
		DSP Unit (X79-1250-00)	<ul style="list-style-type: none"> <li>● If U27 uses a socket, check contact failure.</li> </ul>
		Panel Unit (X66-1410-00) is abnormal.	<ul style="list-style-type: none"> <li>● Check if V16A pin ② outputs key interrupt signal.</li> </ul>
5	LCD display shows a menu, but responses to key operations such as ITEM switching are poor. Measurement value display does not show neither "----" or figures.	DSP Unit (X79-1250-00) is abnormal.	<ul style="list-style-type: none"> <li>● Same as 4 above.</li> <li>● Check if A/D PLL is normal. (In Fast mode of AC-V measurement, check voltage at output TP4 of U11 with oscilloscope. Check 10 MHz clock at U10 pin ①. Check operations of MCK, MLE3 and MDT.)</li> <li>● Check signals ADSTART at U27 pin ⑱ → U14C → PH4 → U21 pin ⑳ and <math>\overline{\text{CONSVST}}</math>. (Specially, I level of pulse input to U21 pin ㉑ should be 0 V. Also check sampling frequency.)</li> <li>● Check 8.3 MHz clock at TP5 of U20D pin ①.</li> <li>● Check <math>\overline{\text{LOAD}}</math> signals from V21 pin ㉒ → V19 → V20B pin ⑥. (Check operations of A/D converter U21.)</li> <li>● Check signals from U22 and U23 → U5A → PH1 and PH3 → V26, as well as ASDAT, ASCLK, LDL and GCLK. (Check serial transfer.)</li> </ul>
		DSP Unit (X79-1240-00)	<ul style="list-style-type: none"> <li>● Same as 4 above.</li> <li>● Check ASDAT, ASCLK and <math>\overline{\text{ADWCK}}</math> to U14 and U15. (Check loading in FIFO memory U16 and U17.)</li> <li>● Check ADRCK. (Check data read from DSP FIFO memory U16 and U17.)</li> <li>● Check <math>\overline{\text{INT2}}</math> at V18A pin ③. (Check fetch instruction and interrupt in data for DSP.)</li> </ul>

# TROUBLE SHOOTING

6	"---" is displayed on measurement value display position.	DSP Unit (X79-1250-00) is abnormal.	<ul style="list-style-type: none"> <li>● Set S1 of DSP Unit (X79-1250-00) to DC offset adjustment position, and check if V17B pin ⑦ is 0 V and if DC-V measurement value display also 0 V. If it is not 0 V, check U18 and U17B. If it is 0 V, U17A of DSP Unit (X79-1230-00) is abnormal.</li> </ul>
		DSP Unit (X79-1230-00) is abnormal.	<ul style="list-style-type: none"> <li>● Identify whether AC-V measurement or DC-V measurement is abnormal. If both are abnormal, supply voltage or port set-up may be abnormal; check ports in order of U18A, U18B, PH1 then PH2 to see that DSP outputs signals SST, SDT and SCK. (Note that SST, SDT and SCK from CPU are valid only during ITEM switching.)</li> <li>● If port set-up does not seem to be abnormal, refer to gain distribution table, fix the range and check signals at each node in order of input → output.</li> </ul>
7	In distortion measurement, residual distortion is not within the spec.	DSP Unit (X79-1250-00) is abnormal.	<ul style="list-style-type: none"> <li>● 9.4 MHz data transfer clock (TP5) is below 9 MHz. In such a case, distortion increases as measured frequency is increased.</li> </ul>
		DSP Unit (X79-1230-00) is abnormal.	<ul style="list-style-type: none"> <li>● Check operation of opamps used in 1st notch and 2 notch filters (X69-1330-00, X69-1340-00).</li> <li>● Check operation of preamp (X73-2080-00). Specifically, switch range to 100 mV, 300 mV, 1 V and 3 V to find which range is abnormal. Also check gain switching FET.</li> </ul>
		CPU Unit (X77-1940-00) is abnormal.	<ul style="list-style-type: none"> <li>● Check if there is any problem with notch calibration coefficient of EEPROM U34. (Notch calibration is always required after replacement of CPU BD.)</li> </ul>
8	In auto measurement of AC-V, it takes long time until measurement result is focused. (Similar phenomena also occurs in L/R measurement.)	DSP Unit (X79-1230-00)	<ul style="list-style-type: none"> <li>● Check DC offset error between U16B, U22A, U22B and U23B. (Shot AC IN, adjust preamps below then adjust DC offset with VR9, VR11, VR12 and VR13 in this order.)</li> </ul>
		Preamp Unit (X73-2080-00)	<ul style="list-style-type: none"> <li>● Check if the pair of first-stage FET Q1 and Q2 or that of Q3 and Q4 (in case of BAL input) is matched. (Short AC IN and measure DC offset at pin ⑫ or pin ⑬ (in case of BAL input) in AC V measurement; DC offset should be within 300 mV.)</li> </ul>
		DSP Unit (X79-1250-00)	<ul style="list-style-type: none"> <li>● Check DC offset error in V17B. (Switch S1 to offset adjustment and adjust VR2 in DC 30 mV range.)</li> </ul>
9	Digital oscillator oscillation is abnormal or oscillation frequency is unstable.	DSP Unit (X79-1250-00) is abnormal.	<ul style="list-style-type: none"> <li>● DDS PLL is abnormal. (+12.582 MHz, +5 V clock is not output at TP6.) If PLL is not locked, the balance of U1 pin ⑫ = U1 pin ⑬ = approx. 2.5 V is lost and output frequency becomes unstable.</li> </ul>
		DSP Unit (X79-1240-00) is abnormal.	<ul style="list-style-type: none"> <li>● Check operations of DDS FIFO U22 and U23 (DDSCK timing).</li> <li>● Check operations of parallel/serial converter U24 and U25 (SLD and CLK timings).</li> <li>● Check operations of digital filter U26 (BCK0, WCK0 and DOL timings).</li> </ul>
		DSP Unit (X79-1230-00) is abnormal.	<ul style="list-style-type: none"> <li>● Check operations of U27 and U28 output signals.</li> <li>● Check clock, data and latch operations from P1 up to D/A converter U4. (Particularly, if duty of 12.582 MHz clock to U4 pin ⑤ is spoiled, it becomes impossible to assure L level. Adjust this with 113.)</li> <li>● If sine wave is output at D/A converter U4 pin ⑨, check signals of U6A, U6B then U7A in this order.</li> </ul>

# TROUBLE SHOOTING

10	CR oscillator oscillates abnormally.	Oscillator Unit (X71-1230-00) is abnormal.	<ul style="list-style-type: none"> <li>● Check operations of U8A, U5 and U8B.</li> </ul>
		Oscillator Unit (X71-1220-00) is abnormal.	<ul style="list-style-type: none"> <li>● Check operations of U1A, U1B, U2A and U2B.</li> </ul>
11	Attenuator operation is abnormal.	Oscillator Unit (X71-1230-00) is abnormal.	<ul style="list-style-type: none"> <li>● If 0.1 dB step is abnormal, check operations of U25, U26 and U27.</li> <li>● If 1 dB or higher step is abnormal, check relay operations of K1 to K8.</li> </ul>
12	Both digital oscillator and CR oscillator are abnormal.	Oscillator Unit (X71-1230-00) is abnormal.	<ul style="list-style-type: none"> <li>● In order of U13 to U13D then PH3 and PH4, check if serial data, clock, latch pulse and selector signals for port-setup are supplied from CPU Unit through P2 by varying GEN frequency level.</li> </ul>
		CPU Unit (X77-1940-00) is abnormal.	<ul style="list-style-type: none"> <li>● Check if serial data (SDTB9, clock (*SCKB), latch (SR/*WB) and selector (SSTB) signals for set-up above are output.</li> </ul>

# PARTS LIST

VA-2230

Y88-1800-00

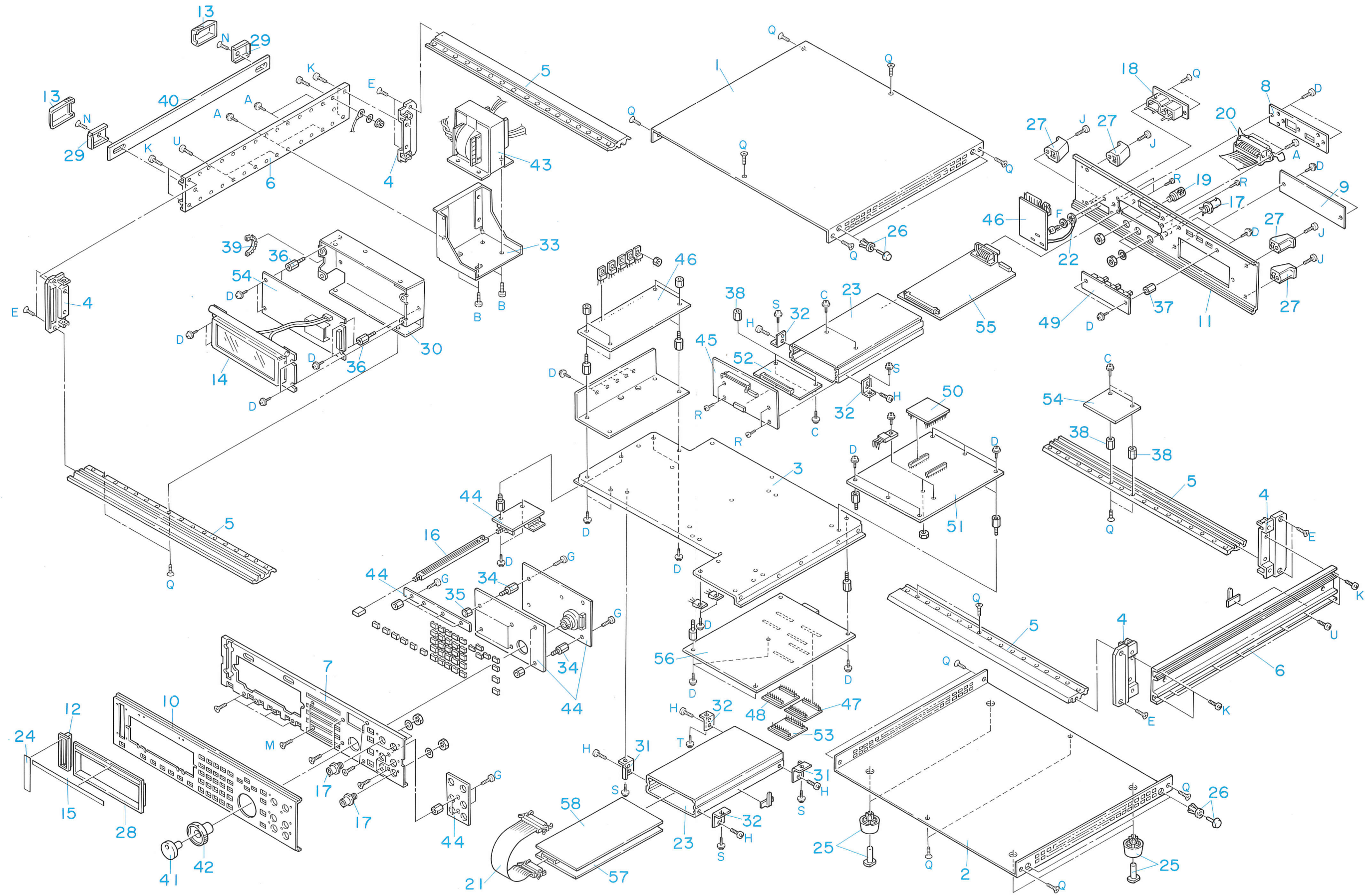
REF. NO	PARTS NO	NAME & DESCRIPTION
	B42-3819-05	SERIAL NO. PLATE
	B42-3820-05	LABEL; CARTON BOX
	B63-0232-10	INSTRUCTION MANUAL; JAPANESE
	B63-0233-10	INSTRUCTION MANUAL; ENGLISH
	D4SBS6	DIODE; BRIDGE
	E30-1929-05	BS POWER CORD
	E30-1950-05	JIS POWER CORD
	E30-1951-05	UL/CSA POWER CORD
	E30-1952-05	CEE POWER CORD
	E30-1953-05	SAA POWER CORD
	E38-1097-05	WIRE ASS'Y; SUB P TO CONNECTION
	E38-1099-05	WIRE ASS'Y; CONNE. TO LCD. CN
	E38-1100-05	WIRE ASS'Y; CONNE. TO LCD. CN
	E38-1102-15	WIRE ASS'Y; P. SW TO P. AC
	E38-1103-05	WIRE ASS'Y; CONNECTION TO DDS
	E38-1104-05	WIRE ASS'Y; LSD. CN TO LCD
	E38-1105-05	WIRE ASS'Y; CONNECTION TO DISTN
	E38-1106-15	WIRE ASS'Y; PW TO CONNE. DSP
	E38-1107-05	WIRE ASS'Y; SUB P TO PANEL F
	E38-1108-15	WIRE ASS'Y; DDS TO DSP MAIN
	E38-1109-05	WIRE ASS'Y; DSP SUB TO DISTN
	E38-1110-15	WIRE ASS'Y; DSP SUB TO DISTN
	E38-1111-15	WIRE ASS'Y; PW TO DDS. DISTN
	E38-1112-15	WIRE ASS'Y; DDS TO BNC
	E38-1113-05	WIRE ASS'Y; SUB P TO PANEL F
	E38-1114-05	WIRE ASS'Y; SW TO DDS. BNC
	E38-1115-05	WIRE ASS'Y; DISTN TO SW
	E38-1116-05	WIRE ASS'Y; BNC TO SUB P
	E38-1117-05	WIRE ASS'Y; BNC
	E38-1118-15	WIRE ASS'Y; BNC TO DISTN
	E38-1119-05	WIRE ASS'Y; DSP MAIN TO MONITOR
	E38-1120-15	WIRE ASS'Y; DISTN TO BNC
	E38-1122-05	WIRE ASS'Y; DSP MAIN TO DSP SUB
	E38-1149-05	WIRE ASS'Y; POWER TO STACK
	E38-1150-15	WIRE ASS'Y; DISTN TO Q11
	E38-1151-15	WIRE ASS'Y; DISTN TO Q12
	E38-1196-05	WIRE ASS'Y; MONITOR TO DISTN
	F05-3112-05	FUSE (5X20MM) T315MA/250V
	F05-6313-05	FUSE (5X20MM) T630MA/250V
	H10-2885-12	FOAMED STYRENE PAD
	H20-1721-04	VINYL COVER
	H53-0167-04	CARTON BOX
	K27-0567-04	BUTTON; WITHOUT WINDOW, WHITE
	K27-0584-04	BUTTON; F1J
	K27-0595-04	BUTTON; F2J
	K27-0596-04	BUTTON; F3J
	K27-0597-04	BUTTON; F4J
	K27-0598-04	BUTTON; F5J
	K27-3619-04	BUTTON; WHITE/HOLE
	K27-3620-04	BUTTON; BLUE/HOLE
	K27-3622-04	BUTTON; FENTJ, WHITE
	K27-3623-04	BUTTON; FCLRJ, WHITE
	K27-3628-04	BUTTON; GRAY/HOLE
	K27-3629-04	BUTTON; FPOWERJ, GRAY
	K27-3630-04	BUTTON; F1J, WHITE
	K27-3631-04	BUTTON; F2J, WHITE
	K27-3632-04	BUTTON; F3J, WHITE
	K27-3633-04	BUTTON; F4J, WHITE
	K27-3634-04	BUTTON; F5J, WHITE
	K27-3635-04	BUTTON; F6J, WHITE
	K27-3636-04	BUTTON; F7J, WHITE
	K27-3637-04	BUTTON; F8J, WHITE
	K27-3638-04	BUTTON; F9J, WHITE
	K27-3639-04	BUTTON; F0J, WHITE
	K27-3640-04	BUTTON; F. J, WHITE
	K27-3643-04	BUTTON SET
	K27-3644-04	BUTTON; FRCLJ, WHITE
	K27-3645-04	BUTTON; F-J, WHITE
	K27-3646-04	BUTTON; FCHJ, GRAY
	K27-3647-04	BUTTON; F^J, WHITE
	K27-3648-04	BUTTON; F^J, BLUE
	K27-3649-04	BUTTON; F^J, BLUE
	L92-0213-05	FERRITE CORE
	L92-0214-05	FERRITE CORE
	N14-0404-04	FLANGE NUT M3
	2SB1015(Y)	TR. SI, PNP
	2SD1406(Y)	TR. SI, NPN
	A01-4024-02	CASE; TOP
	A01-4025-02	CASE; BOTTOM
	A10-1499-22	CHASSIS
	A13-2204-05	FRAME; SIDE ESCUTCHEON
	A13-2208-02	FRAME; TOP, BOTTOM
	A13-2209-02	FRAME; SIDE
	A22-1312-22	SUB PANEL
	A29-0515-04	PANEL; GP-1B
	A29-0516-04	BLIND PANEL
	A63-0191-02	FRONT PANEL
	A83-0074-12	REAR PANEL
	B01-0707-03	ESCUTCHEON
	B09-0409-03	CAP; HANDLE
	B38-0417-05	LCD
	B73-0110-04	NAME PLATE; MODEL NO.
	D21-0935-04	EXTENSION SHAFT

REF. NO	PARTS NO	NAME & DESCRIPTION
17	E04-0253-15	BNC RECEPTACLE
18	E18-0366-25	AC INLET
19	E21-0667-05	METAL TERMINAL
20	E38-1098-05	WIRE ASS'Y; EXT I/O TO CONNE.
21	E38-1101-05	WIRE ASS'Y; CONNE. TO DSP MAIN
22	E38-1121-05	WIRE ASS'Y; SW TO GND
23	F11-1279-03	SHIELD CASE
24	F15-0768-04	BLIND PLATE
25	J02-0089-05	RUBBER FOOT
26	J02-0519-05	RUBBER FOOT; UNDER
27	J02-0535-03	RUBBER FOOT; REAR
28	J10-0434-02	BEZEL
29	J21-4843-04	BRACKET; HANDLE
30	J21-4815-02	BRACKET; LCD
31	J21-4916-04	BRACKET; SHIELD CASE
32	J21-4917-04	BRACKET; SHIELD CASE
33	J21-4926-23	BRACKET; POWER TRANSFORMER
34	J32-1220-04	BOSS L=10MM/M2.6
35	J32-1221-14	BOSS L=6MM/M3
36	J32-1230-04	BOSS L=6MM/M3
37	J32-1232-04	BOSS L=12MM/M3
38	J32-1237-04	BOSS L=7MM/M3
39	J42-0557-04	BUSHING
40	K01-0558-05	HANDLE
41	K29-0879-03	DIAL; JOG
42	K29-0880-03	DIAL; SHUTTLE
43	L07-1530-05	POWER TRANSFORMER
44	X66-1410-00	PANEL UNIT
45	X67-1250-00	CONNECTION UNIT
46	X68-1760-00	POWER UNIT
47	X69-1330-00	FILTER UNIT
48	X69-1340-00	FILTER UNIT
49	X69-1350-00	SWITCH UNIT
50	X71-1220-00	OSCILLATOR UNIT
51	X71-1230-00	OSCILLATOR UNIT
52	X72-1200-00	I/O UNIT
53	X73-2080-00	PRE AMP UNIT
54	X77-1930-00	CONTROL UNIT
55	X77-1940-00	CPU UNIT
56	X79-1230-00	DSP UNIT
57	X79-1240-00	DSP UNIT
58	X79-1250-00	DSP UNIT

## SCREWS

	Parts No.	Parts Name	Figure
A	N09-0628-04	SCREW, SEMS PAN HD	(M3 x 10)
B	N09-0654-05	SCREW, SEMS PAN HD	(M4 x 8)
C	N09-0741-04	SCREW, SEMS PAN HD	(M3 x 6)
D	N09-0742-04	SCREW, SEMS PAN HD	(M3 x 8)
E	N09-4515-05	SCREW, FLAT HD	(M4 x 12)
F	N14-0622-05	NUT, WITH TOOTH	(M4)
G	N30-2604-41	SCREW, PAN HD	(M2.6 x 4)
H	N30-3004-41	SCREW, PAN HD	(M3 x 4)
J	N30-3012-41	SCREW, PAN HD	(M3 x 12)
K	N30-4008-41	SCREW, PAN HD	(M4 x 8)
L	N30-4010-41	SCREW, PAN HD	(M4 x 10)
M	N32-2604-41	SCREW, FLAT HD	(M2.6 x 4)
N	N32-4012-41	SCREW, FLAT HD	(M4 x 12)
P	N33-3006-41	SCREW, OVAL HD	(M3 x 6)
Q	N33-3008-41	SCREW, OVAL HD	(M3 x 8)
R	N67-2608-41	SCREW, SEMS PAN HD	(M2.6 x 8)
S	N67-3008-41	SCREW, SEMS PAN HD	(M3 x 8)
T	N67-3014-41	SCREW, SEMS PAN HD	(M3 x 14)
J	N89-3008-41	SCREW, BINDING TAPITITE	(3 x 8)

# DISASSEMBLY FOR REPAIR





# PARTS LIST

## PANEL UNIT

### X66-1410-00

REF. NO	PARTS NO	NAME & DESCRIPTION
	E02-0143-05	IC SOCKET 28P
	J39-0544-05	SPACER
	J73-0354-02	PCB (UNMOUNTED)
C1	CQ92FM1H104K	CAP. PLASTIC 0.1 10% 50V
C2	CQ92FM1H104K	CAP. PLASTIC 0.1 10% 50V
C3	CQ92FM1H473K	CAP. PLASTIC 0.047 10% 50V
C4	CE04EW1H2R2M	CAP. ELECTRO 2.2 20% 50V
C5	C91-1315-05	CAP. CERAMIC 0.1 80/-20% 50V
C6		NO USE
C7	C91-1315-05	CAP. CERAMIC 0.1 80/-20% 50V
C8	C91-1315-05	CAP. CERAMIC 0.1 80/-20% 50V
C9	CE04EW1C470M	CAP. ELECTRO 47 20% 16V
C10	C91-1315-05	CAP. CERAMIC 0.1 80/-20% 50V
C11	C91-1315-05	CAP. CERAMIC 0.1 80/-20% 50V
C12	C91-1315-05	CAP. CERAMIC 0.1 80/-20% 50V
C13	C91-1315-05	CAP. CERAMIC 0.1 80/-20% 50V
C14	C91-1315-05	CAP. CERAMIC 0.1 80/-20% 50V
C15	C91-1315-05	CAP. CERAMIC 0.1 80/-20% 50V
C16	C91-1315-05	CAP. CERAMIC 0.1 80/-20% 50V
C17	C91-1315-05	CAP. CERAMIC 0.1 80/-20% 50V
D9	B30-3017-05	LED
D10	B30-3017-05	LED
D11	B30-3017-05	LED
D12	B30-3017-05	LED
D13	B30-3017-05	LED
D14	B30-3017-05	LED
D15	B30-3017-05	LED
D16	B30-3017-05	LED
L1	L33-0828-05	CHOKO COIL
P1	E40-7471-05	PIN CONNECTOR 50P
P2	E40-3250-05	PIN CONNECTOR 6P
P3	E40-3249-05	PIN CONNECTOR 5P
P4	E40-7472-05	PIN CONNECTOR 14P
P5	E40-3254-05	PIN CONNECTOR 10P
P6	E40-3254-05	PIN CONNECTOR 10P
P7	E40-3250-05	PIN CONNECTOR 6P
P8	E40-3249-05	PIN CONNECTOR 5P
P9		NO USE
P10	E40-3247-05	PIN CONNECTOR 3P
P14	E40-7470-05	PIN CONNECTOR 50P
P100	E40-7420-05	PIN CONNECTOR 4P
Q9	2SA1346	TR. SI, PNP
Q10	2SA1346	TR. SI, PNP
Q11	2SA1346	TR. SI, PNP
Q12	2SA1346	TR. SI, PNP
Q13	2SA1346	TR. SI, PNP
Q14	2SA1346	TR. SI, PNP
Q15	2SA1346	TR. SI, PNP
Q16	2SA1346	TR. SI, PNP
R1	RN14BK2C1001F	RES. METAL FILM 1K 1% 1/6W
R2	RN14BK2C1001F	RES. METAL FILM 1K 1% 1/6W
R3	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W
R4	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W
R5	RN14BK2C3600F	RES. METAL FILM 360 1% 1/6W
R9	RN14BK2C3600F	RES. METAL FILM 360 1% 1/6W
R10	RN14BK2C3600F	RES. METAL FILM 360 1% 1/6W
R11		NO USE
R12	RN14BK2C3600F	RES. METAL FILM 360 1% 1/6W
R13	RN14BK2C3600F	RES. METAL FILM 360 1% 1/6W
R14	RN14BK2C3600F	RES. METAL FILM 360 1% 1/6W
R15		NO USE
R16	RN14BK2C3600F	RES. METAL FILM 360 1% 1/6W
R17	RN14BK2C3600F	RES. METAL FILM 360 1% 1/6W
R18		NO USE
R19	RN14BK2C3600F	RES. METAL FILM 360 1% 1/6W
R20	RN14BK2C3600F	RES. METAL FILM 360 1% 1/6W
R21	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W
R22	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W
R40	RN14BK2C10R0F	RES. METAL FILM 10.0 1% 1/6W
R100	R90-0628-05	RES. NETWORK 8X10K
R101	R90-0628-05	RES. NETWORK 8X10K
R102	R90-1186-05	RES. NETWORK 22K
S1	S79-0608-05	JOG SHUTTLE
S2	S70-0601-05	SWITCH
S3	S70-0601-05	SWITCH
S4	S70-0601-05	SWITCH
S5	S70-0601-05	SWITCH
S6	S70-0601-05	SWITCH
S7	S70-0602-05	SWITCH
S8	S70-0601-05	SWITCH
S9	S70-0602-05	SWITCH
S10	S70-0601-05	SWITCH

REF. NO	PARTS NO	NAME & DESCRIPTION
S11	S70-0601-05	SWITCH
S12	S70-0601-05	SWITCH
S13	S70-0601-05	SWITCH
S14	S70-0601-05	SWITCH
S15	S70-0601-05	SWITCH
S16	S70-0601-05	SWITCH
S17	S70-0601-05	SWITCH
S18	S70-0601-05	SWITCH
S19	S70-0601-05	SWITCH
S20	S70-0601-05	SWITCH
S21	S70-0601-05	SWITCH
S22	S70-0601-05	SWITCH
S23	S70-0601-05	SWITCH
S24	S70-0601-05	SWITCH
S25	S70-0601-05	SWITCH
S26	S70-0601-05	SWITCH
S27	S70-0601-05	SWITCH
S28	S70-0601-05	SWITCH
S29	S70-0601-05	SWITCH
S30	S70-0601-05	SWITCH
S31	S70-0601-05	SWITCH
S32	S70-0601-05	SWITCH
S33	S70-0601-05	SWITCH
S34	S70-0601-05	SWITCH
S35	S70-0601-05	SWITCH
S36	S70-0601-05	SWITCH
S37	S70-0602-05	SWITCH
S38	S70-0601-05	SWITCH
S39	S70-0601-05	SWITCH
S40		NO USE
S41	S70-0602-05	SWITCH
S100	S68-0628-05	PUSH SWITCH
U1	TC4053BP	IC, TRIPLE 2-CH MPX/DE-MPX
U2	TC74HC259AP	IC, 8-BIT ADDRESSABLE LATCH
U8	TC74HC138AP	IC, 3 TO 8 LINE DECODER
U9		NO USE
U10	TC74HC259AP	IC, 8-BIT ADDRESSABLE LATCH
U11	TC74HC74AP	IC, DUAL D-FLIP FLOP
U12		NO USE
U13	TC74HC541AP	IC, OCTAL BUS BUFFER (3-STATE)
U14	T93-0866-04	IC, PROGRAMMED ROM
U15	TC74HC30AP	IC, 8-INPUT NAND GATE
U16	TC74HC14AP	IC, HEX SCHMITT INVERTER
U17	TC74HC00AP	IC, QUAD 2-INPUT NAND GATE

## CONNECTION UNIT

### X67-1250-00

REF. NO	PARTS NO	NAME & DESCRIPTION
	J73-0351-12	PCB (UNMOUNTED)
P1	E40-7472-05	PIN CONNECTOR 14P
P2	E40-7472-05	PIN CONNECTOR 14P
P3	E40-3255-05	PIN CONNECTOR 11P
P4	E40-7475-05	PIN CONNECTOR 50P
P5	E40-7472-05	PIN CONNECTOR 14P
P6	E40-3257-05	PIN CONNECTOR 13P
P7	E40-7474-05	PIN CONNECTOR 40P
P8	E40-3256-05	PIN CONNECTOR 12P
P9	E40-3254-05	PIN CONNECTOR 10P
P10	E40-3250-05	PIN CONNECTOR 6P
P11	E40-7476-05	PIN CONNECTOR 100P
P12	E40-7476-05	PIN CONNECTOR 100P





# PARTS LIST

REF. NO	PARTS NO	NAME & DESCRIPTION
R44	RN73FH2A182D	RES. METAL FILM 1.8K 0.5% 1/10W
R45	RN73FH2A272D	RES. METAL FILM 2.7K 0.5% 1/10W
R46	RN73FH2A272D	RES. METAL FILM 2.7K 0.5% 1/10W
R47	RN73FH2A303D	RES. METAL FILM 30K 0.5% 1/10W
R48	RN73FH2A272D	RES. METAL FILM 2.7K 0.5% 1/10W
R49	RN73FH2A272D	RES. METAL FILM 2.7K 0.5% 1/10W
R50	RN73FH2A303D	RES. METAL FILM 30K 0.5% 1/10W
R100	R90-1189-05	RES. NETWORK 10K,20K
R101	R90-1189-05	RES. NETWORK 10K,20K
U1	NJM5532M	IC,DUAL LOW NOISE OP-AMP
U2	NJM5532M	IC,DUAL LOW NOISE OP-AMP
U3	NJM5532M	IC,DUAL LOW NOISE OP-AMP
U4	TC4053BFS	IC,TRIPLE 2-CH MPX/DE-MPX
U5	TC4053BFS	IC,TRIPLE 2-CH MPX/DE-MPX
U6	TC4053BFS	IC,TRIPLE 2-CH MPX/DE-MPX
U7	TC4053BFS	IC,TRIPLE 2-CH MPX/DE-MPX
U8	TC4053BFS	IC,TRIPLE 2-CH MPX/DE-MPX
U9	TC4053BFS	IC,TRIPLE 2-CH MPX/DE-MPX
U10	TC4053BFS	IC,TRIPLE 2-CH MPX/DE-MPX
U11	TC4053BFS	IC,TRIPLE 2-CH MPX/DE-MPX

## OSCILLATOR UNIT

### X71-1230-00

REF. NO	PARTS NO	NAME & DESCRIPTION
	J73-0351-12	PCB (UNMOUNTED)
	M09-0743-04	SCREW,SENS PAN HD M3X10
	N14-0404-04	FLANGE NUT M3
C1	C91-1315-05	CAP. CERAMIC 0.1 80/-20% 50V
C2	C91-1315-05	CAP. CERAMIC 0.1 80/-20% 50V
C3	CE04EW1C101M	CAP. ELECTRO 100 20% 16V
C4	CE04EW1C470M	CAP. ELECTRO 47 20% 16V
C5	CE04EW1C470M	CAP. ELECTRO 47 20% 16V
C6	CE04HW1V100M	CAP. ELECTRO 10 20% 35V
C7	CQ92HP2A471F	CAP. PLASTIC 470P 1% 100V
C8	CQ92HP2A101F	CAP. PLASTIC 100P 1% 100V
C9	CQ93HPG1H333F	CAP. PLASTIC 0.033 1% 50V
C10	CQ92HP2A101F	CAP. PLASTIC 100P 1% 100V
C11	CE04EW1C100M	CAP. ELECTRO 10 20% 16V
C12	CE04EW1C100M	CAP. ELECTRO 10 20% 16V
C13	NO USE	
C14	CC45FSL1H121J	CAP. CERAMIC 120P 5% 50V
C15	CE04EW1C100M	CAP. ELECTRO 10 20% 16V
C16	CE04EW1C100M	CAP. ELECTRO 10 20% 16V
C19	CE04EW1C470M	CAP. ELECTRO 47 20% 16V
C20	CE04EW1C470M	CAP. ELECTRO 47 20% 16V
C21	NO USE	
C22	CQ92FM1H104J	CAP. PLASTIC 0.1 5% 50V
C26	CK45FB1H221K	CAP. CERAMIC 220P 10% 50V
C34	C91-1315-05	CAP. CERAMIC 0.1 80/-20% 50V
C35	CE04BW1C221M	CAP. ELECTRO 220 20% 16V
C36	CE04EW1C221M	CAP. ELECTRO 220 20% 16V
C37	CE04EW1H101M	CAP. ELECTRO 100 20% 50V
C49	CE04EW1H100M	CAP. ELECTRO 10 20% 50V
C50	NO USE	
C51	CE04EW1C100M	CAP. ELECTRO 10 20% 16V
C52	CE04EW1C100M	CAP. ELECTRO 10 20% 16V
C53	CE04EW1C100M	CAP. ELECTRO 10 20% 16V
C54	CE04EW1C100M	CAP. ELECTRO 10 20% 16V
C55	CE04EW1C100M	CAP. ELECTRO 10 20% 16V
C56	CE04EW1C100M	CAP. ELECTRO 10 20% 16V
C64	C91-1315-05	CAP. CERAMIC 0.1 80/-20% 50V
C65	NO USE	
C66	CE04EW1C101M	CAP. ELECTRO 100 20% 16V
C67	CE04EW1C470M	CAP. ELECTRO 47 20% 16V
C68	CE04EW1C470M	CAP. ELECTRO 47 20% 16V
C72	C91-1315-05	CAP. CERAMIC 0.1 80/-20% 50V
C73	CQ93HPG1H122F	CAP. PLASTIC 1200P 1% 50V
C74	CQ93HPG1H122F	CAP. PLASTIC 1200P 1% 50V
C75	CQ92HP2A221F	CAP. PLASTIC 220P 1% 100V
C76	NO USE	
C77	CQ93HPG1H102F	CAP. PLASTIC 1000P 1% 50V
C78	CQ92HP2A331F	CAP. PLASTIC 330P 1% 100V
C79	NO USE	
C80	CC45FCH1H330J	CAP. CERAMIC 33P 5% 50V
C81	CQ93HPG1H392F	CAP. PLASTIC 3900P 1% 50V
C85	CC45FCH1H220J	CAP. CERAMIC 22P 5% 50V
C90	CQ93HPG1H223F	CAP. PLASTIC 0.022 1% 50V
C91	CQ93HPG1H102F	CAP. PLASTIC 1000P 1% 50V
C92	CQ92HP2A391F	CAP. PLASTIC 390P 1% 100V
C93	CQ92HP2A471F	CAP. PLASTIC 470P 1% 100V
C94	CQ92HP2A471F	CAP. PLASTIC 470P 1% 100V
C95	C91-2645-05	CAP. FILM 0.82 2% 50V
C96	C91-2644-05	CAP. FILM 0.22 2% 50V
C97	CK45FB1H102K	CAP. CERAMIC 1000P 10% 50V

REF. NO	PARTS NO	NAME & DESCRIPTION
C98	CK45FB1H102K	CAP. CERAMIC 1000P 10% 50V
C99	CC45FCH1H330J	CAP. CERAMIC 33P 5% 50V
C202	CE04EW1H100M	CAP. ELECTRO 10 20% 50V
C205	CK45B2H222K	CAP. CERAMIC 2200P 10% 500V
D1	1SS132	DIODE
D2	1SS132	DIODE
D3	RD5.6JS	DIODE,ZENER
D4	RD5.6JS	DIODE,ZENER
D5	1SS132	DIODE
D6	1SS132	DIODE
D7	1SS132	DIODE
D8	1SS132	DIODE
D9	1SS132	DIODE
D10	1SS132	DIODE
D11	1SS132	DIODE
D12	1SS132	DIODE
D15	RD5.6F	DIODE,ZENER
D16	MTZ16JA	DIODE,ZENER
D17	MTZ16JA	DIODE,ZENER
D18	1SS132	DIODE
D19	RD5.6JS	DIODE,ZENER
D20	1SS132	DIODE
D21	RD5.6JS	DIODE,ZENER
D22	1SS132	DIODE
D23	RD5.6JS	DIODE,ZENER
D24	1SS132	DIODE
D25	1SS132	DIODE
D26	RD5.6F	DIODE,ZENER
D50	1SS132	DIODE
K1	S76-0627-05	RELAY
K2	S76-0627-05	RELAY
K3	S76-0627-05	RELAY
K4	S76-0627-05	RELAY
K5	S76-0627-05	RELAY
K6	S76-0627-05	RELAY
K7	S76-0627-05	RELAY
K8	S76-0627-05	RELAY
L1	L33-0832-05	CHOKE COIL
P1	E40-3259-05	PIN CONNECTOR 15P
P2	E40-3254-05	PIN CONNECTOR 10P
P3	E40-3249-05	PIN CONNECTOR 5P
P4	E40-3250-05	PIN CONNECTOR 6P
P5	E40-7486-05	PIN CONNECTOR 20P
P6	E40-7486-05	PIN CONNECTOR 20P
P7	E40-7486-05	PIN CONNECTOR 20P
P8	E40-7486-05	PIN CONNECTOR 20P
P9	E40-3246-05	PIN CONNECTOR 2P
PH1	TLP2630	IC,PHOTO COUPLER
PH2	TLP2630	IC,PHOTO COUPLER
PH3	TLP2630	IC,PHOTO COUPLER
PH4	TLP2630	IC,PHOTO COUPLER
Q1	2SK190H	FET, N-CHANNEL
Q2	2SK190H	FET, N-CHANNEL
Q3	2SD1406(Y)	TR. SI, NPN
Q4	2SB1015(Y)	TR. SI, PNP
R1	RN14BK2C2200F	RES. METAL FILM 220 1% 1/6W
R2	RN14BK2C2200F	RES. METAL FILM 220 1% 1/6W
R3	RN14BK2C2200F	RES. METAL FILM 220 1% 1/6W
R4	RN14BK2C2200F	RES. METAL FILM 220 1% 1/6W
R5	RN14BK2C2200F	RES. METAL FILM 220 1% 1/6W
R6	RN14BK2C2200F	RES. METAL FILM 220 1% 1/6W
R7	RN14BK2C2200F	RES. METAL FILM 220 1% 1/6W
R8	RN14BK2C2200F	RES. METAL FILM 220 1% 1/6W
R9	RN14BK2C3300F	RES. METAL FILM 330 1% 1/6W
R10	RN14BK2C3300F	RES. METAL FILM 330 1% 1/6W
R11	RN14BK2C3300F	RES. METAL FILM 330 1% 1/6W
R12	RN14BK2C3300F	RES. METAL FILM 330 1% 1/6W
R13	RN14BK2C3300F	RES. METAL FILM 330 1% 1/6W
R14	RN14BK2C1001F	RES. METAL FILM 1K 1% 1/6W
R15	RN14BK2C1001F	RES. METAL FILM 1K 1% 1/6W
R16	RN14BK2C1001F	RES. METAL FILM 1K 1% 1/6W
R17	RN14BK2C5602F	RES. METAL FILM 56K 1% 1/6W
R18	RN14BK2C5602F	RES. METAL FILM 56K 1% 1/6W
R19	RN14BK2C4302F	RES. METAL FILM 43K 1% 1/6W
R20	RN14BK2C2201F	RES. METAL FILM 2.2K 1% 1/6W
R21	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W
R22	RN14BK2C05101F	RES. METAL FILM 5.1K 1% 1/6W
R23	RN14BK2C2201F	RES. METAL FILM 2.2K 1% 1/6W
R24	RN14BK2C4703F	RES. METAL FILM 470K 1% 1/6W
R25	RN14BK2C2003F	RES. METAL FILM 200K 1% 1/6W
R26	RN14BK2C10R0F	RES. METAL FILM 10.0 1% 1/6W
R27	RN14BK2C2002F	RES. METAL FILM 20K 1% 1/6W
R28	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W
R29	RN14BK2C2000F	RES. METAL FILM 200 1% 1/6W
R30	RN14BK2C05101F	RES. METAL FILM 5.1K 1% 1/6W
R31	RN14BK2C4701F	RES. METAL FILM 4.7K 1% 1/6W
R32	RN14BK2C4301F	RES. METAL FILM 4.3K 1% 1/6W



# PARTS LIST

REF. NO	PARTS NO	NAME & DESCRIPTION
R26	RN73FH2A473D	RES. METAL FILM 47K 0.5% 1/10W
R27	RN73FH2A473D	RES. METAL FILM 47K 0.5% 1/10W
R28	RN73FH2A473D	RES. METAL FILM 47K 0.5% 1/10W
R29	RN73FH2A473D	RES. METAL FILM 47K 0.5% 1/10W
R30	RN73FH2A473D	RES. METAL FILM 47K 0.5% 1/10W
R31	RN73FH2A473D	RES. METAL FILM 47K 0.5% 1/10W
R32	RN73FH2A473D	RES. METAL FILM 47K 0.5% 1/10W
R33	RN73FH2A473D	RES. METAL FILM 47K 0.5% 1/10W
R34	RN73FH2A473D	RES. METAL FILM 47K 0.5% 1/10W
R35	RN73FH2A473D	RES. METAL FILM 47K 0.5% 1/10W
R36	RN73FH2A473D	RES. METAL FILM 47K 0.5% 1/10W
R37	RN73FH2A473D	RES. METAL FILM 47K 0.5% 1/10W
R38	RN73FH2A473D	RES. METAL FILM 47K 0.5% 1/10W
R39	RN73FH2A471D	RES. METAL FILM 470 0.5% 1/10W
R40	RN73FH2A471D	RES. METAL FILM 470 0.5% 1/10W
R41	RN73FH2A471D	RES. METAL FILM 470 0.5% 1/10W
R42	RN73FH2A471D	RES. METAL FILM 470 0.5% 1/10W
R43	RN73FH2A471D	RES. METAL FILM 470 0.5% 1/10W
R44	RN73FH2A471D	RES. METAL FILM 470 0.5% 1/10W
R45	RN73FH2A471D	RES. METAL FILM 470 0.5% 1/10W
R46	RN73FH2A471D	RES. METAL FILM 470 0.5% 1/10W
R47	RN73FH2A470D	RES. METAL FILM 47 0.5% 1/10W
R48	RN73FH2A470D	RES. METAL FILM 47 0.5% 1/10W
R49	RN73FH2A470D	RES. METAL FILM 47 0.5% 1/10W
R50	RN73FH2A470D	RES. METAL FILM 47 0.5% 1/10W
R51	RN73FH2A470D	RES. METAL FILM 47 0.5% 1/10W
R52	RN73FH2A470D	RES. METAL FILM 47 0.5% 1/10W
R53	RN73FH2A470D	RES. METAL FILM 47 0.5% 1/10W
R54	RN73FH2A470D	RES. METAL FILM 47 0.5% 1/10W
R55	RN73FH2A470D	RES. METAL FILM 47 0.5% 1/10W
R56	RN73FH2A470D	RES. METAL FILM 47 0.5% 1/10W
R57	NO USE	
R58	RN73FH2A472D	RES. METAL FILM 4.7K 0.5% 1/10W
R59	RN73FH2A101D	RES. METAL FILM 100 0.5% 1/10W
R60	RN73FH2A101D	RES. METAL FILM 100 0.5% 1/10W
R61	RN73FH2A333D	RES. METAL FILM 33K 0.5% 1/10W
R62	RN73FH2A103D	RES. METAL FILM 10K 0.5% 1/10W
R63	RN73FH2A104D	RES. METAL FILM 100K 0.5% 1/10W
R64	RN73FH2A104D	RES. METAL FILM 100K 0.5% 1/10W
R65	RN73FH2A103D	RES. METAL FILM 10K 0.5% 1/10W
R66	RN73FH2A103D	RES. METAL FILM 10K 0.5% 1/10W
R67	RN73FH2A103D	RES. METAL FILM 10K 0.5% 1/10W

U1	TC74HC30AF	IC, 8-INPUT NAND GATE
U2	TC74HC138AF	IC, 3 TO 8 LINE DECODER
U3	TC74HC00AF	IC, QUAD 2-INPUT NAND GATE
U4	TC74HC574AF	IC, OCTAL D-TYPE F.F.
U5	UPD71055L	IC, PRO. PERIPHERAL INTERFACE
U6	TC74HC148AF	IC, 8 TO 3 LINE PRIORITY ENCO.
U7	TC74HC541AF	IC, OCTAL BUS BUFFER (3-STATE)
U8	TC4093BF	IC, QUAD 2-INPUT SCHMITT TRIG.
U9	UPD4528BG	IC, DUAL MONO. MULTIVIBRATOR
U10	TC74HC541AF	IC, OCTAL BUS BUFFER (3-STATE)
U11	TC74HC32AF	IC, QUAD 2-INPUT OR GATE

## PREAMP UNIT

### X73-2080-00

REF. NO	PARTS NO	NAME & DESCRIPTION
C3	J73-0349-12	PCB (UNNOUNDED)
C4	CC73FCH1H050C	CAP. CERAMIC 5P 0.25P 50V
C5	CC73FCH1H050C	CAP. CERAMIC 5P 0.25P 50V
C6	CK73FR1H103K	CAP. CERAMIC 0.01 10% 50V
C7	CK73FR1H103K	CAP. CERAMIC 0.01 10% 50V
C8	CK73FR1H103K	CAP. CERAMIC 0.01 10% 50V
C9	CK73FR1H103K	CAP. CERAMIC 0.01 10% 50V
C10	CC73FCH1H050C	CAP. CERAMIC 5P 0.25P 50V
C13	CC73FCH1H150J	CAP. CERAMIC 15P 5% 50V
C14	CK73FF1H104Z	CAP. CERAMIC 0.1 20/-80% 50V
C15	CK73FF1H104Z	CAP. CERAMIC 0.1 20/-80% 50V
C16	CC73FCH1H150J	CAP. CERAMIC 15P 5% 50V
C17	CC73FCH1H050C	CAP. CERAMIC 5P 0.25P 50V
C20	CC73FCH1H050C	CAP. CERAMIC 5P 0.25P 50V
C21	CC73FCH1H050C	CAP. CERAMIC 5P 0.25P 50V
D1	1SS184	DIODE
D2	1SS184	DIODE
D3	1SS184	DIODE
J1	E40-7480-05	PIN CONNECTOR 20P
J2	E40-7480-05	PIN CONNECTOR 20P
J17	E40-7480-05	PIN CONNECTOR 20P
J32	E40-7480-05	PIN CONNECTOR 20P
Q1	2SK426(X26)	FET, N-CHANNEL
Q2	2SK426(X26)	FET, N-CHANNEL
Q3	2SK426(X26)	FET, N-CHANNEL
Q4	2SK426(X26)	FET, N-CHANNEL
Q5	2SK1577(P2)	FET, N-CHANNEL
Q6	2SK426(X26)	FET, N-CHANNEL

REF. NO	PARTS NO	NAME & DESCRIPTION
Q7	2SK426(X26)	FET, N-CHANNEL
Q8	2SK1577(P2)	FET, N-CHANNEL
Q9	2SK426(X26)	FET, N-CHANNEL
Q10	2SK1577(P2)	FET, N-CHANNEL
Q11	2SK1577(P2)	FET, N-CHANNEL
Q12	2SK426(X26)	FET, N-CHANNEL
Q13	2SK426(X26)	FET, N-CHANNEL
Q14	2SK1577(P2)	FET, N-CHANNEL
Q15	2SK426(X26)	FET, N-CHANNEL
Q16	2SK1577(P2)	FET, N-CHANNEL
Q17	2SC2712	TR. SI, NPN
Q18	2SC2712	TR. SI, NPN
Q19	2SC2712	TR. SI, NPN
Q20	2SC2712	TR. SI, NPN
Q21	2SC2712	TR. SI, NPN
Q22	2SC2712	TR. SI, NPN
Q23	2SC3356	TR. SI, NPN
Q24	2SC3356	TR. SI, NPN
Q25	2SC3356	TR. SI, NPN
Q26	2SC3356	TR. SI, NPN
Q27	2SC3356	TR. SI, NPN
Q28	2SC3356	TR. SI, NPN
Q901	2SK1577(P1)	FET, N-CHANNEL
Q902	2SK1577(P1)	FET, N-CHANNEL
Q903	2SK1577(P1)	FET, N-CHANNEL
Q904	2SK1577(P1)	FET, N-CHANNEL

R1	RN73FH2A332D	RES. METAL FILM 3.3K 0.5% 1/10W
R2	RN73FH2A332D	RES. METAL FILM 3.3K 0.5% 1/10W
R3	RN73FH2A153D	RES. METAL FILM 15K 0.5% 1/10W
R4	RN73FH2A223D	RES. METAL FILM 22K 0.5% 1/10W
R5	RN73FH2A273D	RES. METAL FILM 27K 0.5% 1/10W
R6	RN73FH2A332D	RES. METAL FILM 3.3K 0.5% 1/10W
R7	RN73FH2A332D	RES. METAL FILM 3.3K 0.5% 1/10W
R8	RN73FH2A332D	RES. METAL FILM 3.3K 0.5% 1/10W
R9	RN73FH2A153D	RES. METAL FILM 15K 0.5% 1/10W
R10	RN73FH2A223D	RES. METAL FILM 22K 0.5% 1/10W
R11	RN73FH2A273D	RES. METAL FILM 27K 0.5% 1/10W
R12	RN73FH2A332D	RES. METAL FILM 3.3K 0.5% 1/10W
R13	RN73FH2A152D	RES. METAL FILM 1.5K 0.5% 1/10W
R14	RN73FH2A471D	RES. METAL FILM 470 0.5% 1/10W
R15	RN73FH2A622D	RES. METAL FILM 6.2K 0.5% 1/10W
R16	RN73FH2A101D	RES. METAL FILM 100 0.5% 1/10W
R17	RN73FH2A203D	RES. METAL FILM 20K 0.5% 1/10W
R18	RN73FH2A433D	RES. METAL FILM 43K 0.5% 1/10W
R19	RN73FH2A471D	RES. METAL FILM 470 0.5% 1/10W
R20	RN73FH2A152D	RES. METAL FILM 1.5K 0.5% 1/10W
R21	RN73FH2A622D	RES. METAL FILM 6.2K 0.5% 1/10W
R22	RN73FH2A101D	RES. METAL FILM 100 0.5% 1/10W
R23	RN73FH2A203D	RES. METAL FILM 20K 0.5% 1/10W
R24	RN73FH2A433D	RES. METAL FILM 43K 0.5% 1/10W
R25	RN73FH2A105D	RES. METAL FILM 1M 0.5% 1/10W
R26	R92-1647-05	RES. CHIP 2.2M 10% 1/10W
R27	R92-1647-05	RES. CHIP 2.2M 10% 1/10W
R28	RN73FH2A105D	RES. METAL FILM 1M 0.5% 1/10W
R29	RN73FH2A105D	RES. METAL FILM 1M 0.5% 1/10W
R30	RN73FH2A105D	RES. METAL FILM 1M 0.5% 1/10W
R31	RN73FH2A105D	RES. METAL FILM 1M 0.5% 1/10W
R32	RN73FH2A105D	RES. METAL FILM 1M 0.5% 1/10W
R33	RN73FH2A105D	RES. METAL FILM 1M 0.5% 1/10W
R34	RN73FH2A105D	RES. METAL FILM 1M 0.5% 1/10W
R35	RN73FH2A105D	RES. METAL FILM 1M 0.5% 1/10W
R36	RN73FH2A105D	RES. METAL FILM 1M 0.5% 1/10W
R37	RN73FH2A104D	RES. METAL FILM 100K 0.5% 1/10W
R38	RN73FH2A104D	RES. METAL FILM 100K 0.5% 1/10W
R39	RN73FH2A104D	RES. METAL FILM 100K 0.5% 1/10W
R40	RN73FH2A104D	RES. METAL FILM 100K 0.5% 1/10W
R41	RN73FH2A104D	RES. METAL FILM 100K 0.5% 1/10W
R42	RN73FH2A104D	RES. METAL FILM 100K 0.5% 1/10W
R43	RN73FH2A104D	RES. METAL FILM 100K 0.5% 1/10W
R44	RN73FH2A104D	RES. METAL FILM 100K 0.5% 1/10W
R45	RN73FH2A104D	RES. METAL FILM 100K 0.5% 1/10W
R46	RN73FH2A104D	RES. METAL FILM 100K 0.5% 1/10W
R47	RN73FH2A104D	RES. METAL FILM 100K 0.5% 1/10W
R48	RN73FH2A104D	RES. METAL FILM 100K 0.5% 1/10W
R49	RN73FH2A104D	RES. METAL FILM 100K 0.5% 1/10W
R50	RN73FH2A104D	RES. METAL FILM 100K 0.5% 1/10W
R51	R92-0670-05	JUMPING RES. ZERO OHM
R52	RN73FH2A271D	RES. METAL FILM 270 0.5% 1/10W
R53	R92-0670-05	JUMPING RES. ZERO OHM
R54	RN73FH2A271D	RES. METAL FILM 270 0.5% 1/10W

U1	NJM5532M	IC, DUAL LOW NOISE OP-AMP
U2	NJM5532M	IC, DUAL LOW NOISE OP-AMP
U3	TC4053BF	IC, TRIPLE 2-CH MPX/DE-MPX









# PARTS LIST

REF. NO	PARTS NO	NAME & DESCRIPTION	REF. NO	PARTS NO	NAME & DESCRIPTION
D31	1SS132	DIODE	R18	RN14BK2C1000F	RES. METAL FILM 100 1% 1/6W
D32	1SS132	DIODE	R19	RN14BK2C1112D	RES. METAL FILM 11.1K0.5% 1/6W
D33	1SS132	DIODE	R20	RN14BK2C8982D	RES. METAL FILM 89.8K 0.5% 1/6W
JPI	R92-1061-05	JUMPING RES. ZERO OHM (5MM)	R21	RN14BK2C4642D	RES. METAL FILM 46.4K 0.5% 1/6W
K1	S76-0627-05	RELAY	R22	RN14BK2C6812D	RES. METAL FILM 68.1K 0.5% 1/6W
K2	S76-0627-05	RELAY	R23	RN14BK2C1003D	RES. METAL FILM 100K 0.5% 1/6W
K3	S76-0627-05	RELAY	R24	RN14BK2C1003F	RES. METAL FILM 100K 1% 1/6W
K4	S76-0627-05	RELAY	R25	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W
K5	S76-0627-05	RELAY	R26	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W
K6	S76-0627-05	RELAY	R27	RN14BK2C1212F	RES. METAL FILM 12.1K 1% 1/6W
K7	S76-0627-05	RELAY	R28	RN14BK2C1212F	RES. METAL FILM 12.1K 1% 1/6W
K8	S76-0627-05	RELAY	R29	RN14BK2C1003F	RES. METAL FILM 100K 1% 1/6W
K9	S76-0627-05	RELAY	R30	RN14BK2C1212F	RES. METAL FILM 12.1K 1% 1/6W
K10	S76-0627-05	RELAY	R31	RN14BK2C1212F	RES. METAL FILM 12.1K 1% 1/6W
K11	S76-0627-05	RELAY	R32	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W
K12	S76-0627-05	RELAY	R33	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W
K13	NO USE		R34	RN14BK2C1003F	RES. METAL FILM 100K 1% 1/6W
K14	S76-0627-05	RELAY	R35	RN14BK2C2201F	RES. METAL FILM 2.2K 1% 1/6W
K15	S76-0627-05	RELAY	R36	RN14BK2C2201F	RES. METAL FILM 2.2K 1% 1/6W
K16	S76-0627-05	RELAY	R37	RN14BK2C2001F	RES. METAL FILM 2K 1% 1/6W
K17	S76-0627-05	RELAY	R38	NO USE	
K18	S76-0627-05	RELAY	R39	RN14BK2C2201F	RES. METAL FILM 2.2K 1% 1/6W
K19	S76-0627-05	RELAY	R40	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W
K20	S76-0627-05	RELAY	R41	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W
K21	S76-0627-05	RELAY	R42	RN14BK2C1003D	RES. METAL FILM 100K 0.5% 1/6W
K22	S76-0627-05	RELAY	R43	RN14BK2C1001D	RES. METAL FILM 1K 0.5% 1/6W
K23	S76-0627-05	RELAY	R44	RN14BK2C10R0F	RES. METAL FILM 10.0 1% 1/6W
P1	E40-3249-05	PIN CONNECTOR 5P	R45	RN14BK2C1001F	RES. METAL FILM 1K 1% 1/6W
P2	E40-3248-05	PIN CONNECTOR 4P	R46	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W
P3	E40-3249-05	PIN CONNECTOR 5P	R47	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W
P4	E40-3246-05	PIN CONNECTOR 2P	R48	RN14BK2C1000F	RES. METAL FILM 100 1% 1/6W
P5	NO USE		R49	RN14BK2C1001F	RES. METAL FILM 1K 1% 1/6W
P6	E40-3254-05	PIN CONNECTOR 10P	R50	NO USE	
P7	E40-3254-05	PIN CONNECTOR 10P	R51	RN14BK2C1100D	RES. METAL FILM 110 0.5% 1/6W
P8	E40-7487-05	PIN CONNECTOR 20P	R52	RN14BK2C1000F	RES. METAL FILM 100 1% 1/6W
P9	NO USE		R53	RN14BK2C8983D	RES. METAL FILM 898K 0.5% 1/6W
P10	E40-3249-05	PIN CONNECTOR 5P	R54	RN14BK2C1003D	RES. METAL FILM 100K 0.5% 1/6W
P11	NO USE		R55	NO USE	
P12	E40-3246-05	PIN CONNECTOR 2P	R56	RN14BK2C6813D	RES. METAL FILM 681K 0.5% 1/6W
P13	E40-7486-05	PIN CONNECTOR 20P	R57	RN14BK2C4323D	RES. METAL FILM 432K 0.5% 1/6W
P14	E40-7486-05	PIN CONNECTOR 20P	R58	NO USE	
P15	E40-7486-05	PIN CONNECTOR 20P	R59	RN14BK2C1004F	RES. METAL FILM 1M 1% 1/6W
P16	E40-7486-05	PIN CONNECTOR 20P	R60	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W
P17	E40-7486-05	PIN CONNECTOR 20P	R61	RN14BK2C1003F	RES. METAL FILM 100K 1% 1/6W
P18	E40-7486-05	PIN CONNECTOR 20P	R62	RN14BK2C9093D	RES. METAL FILM 909K 0.5% 1/6W
P19	E40-7486-05	PIN CONNECTOR 20P	R63	RN14BK2C1004F	RES. METAL FILM 1M 1% 1/6W
P20	E40-7486-05	PIN CONNECTOR 20P	R64	RN14BK2C1103D	RES. METAL FILM 110K 0.5% 1/6W
P21	E40-7486-05	PIN CONNECTOR 20P	R65	RN14BK2C1001F	RES. METAL FILM 1K 1% 1/6W
P22	E40-7486-05	PIN CONNECTOR 20P	R66	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W
P23	E40-7486-05	PIN CONNECTOR 20P	R67	RN14BK2C4701F	RES. METAL FILM 4.7K 1% 1/6W
P24	E40-7486-05	PIN CONNECTOR 20P	R68	RN14BK2C4701F	RES. METAL FILM 4.7K 1% 1/6W
P100	E40-3249-05	PIN CONNECTOR 5P	R69	RN14BK2C4701F	RES. METAL FILM 4.7K 1% 1/6W
P111	E40-3238-05	PIN CONNECTOR 3P	R72	RN14BK2C1001F	RES. METAL FILM 1K 1% 1/6W
P112	E40-3238-05	PIN CONNECTOR 3P	R73	RN14BK2C1001F	RES. METAL FILM 1K 1% 1/6W
PH1	TLP2630	IC, PHOTO COUPLER	R74	RN14BK2C1001F	RES. METAL FILM 1K 1% 1/6W
PH2	TLP2630	IC, PHOTO COUPLER	R75	RN14BK2C1001D	RES. METAL FILM 1K 0.5% 1/6W
Q1	RN1006	TR. S1, NPN	R76	RN14BK2C1000F	RES. METAL FILM 100 1% 1/6W
Q2	RN1006	TR. S1, NPN	R77	RN14BK2C1004F	RES. METAL FILM 1M 1% 1/6W
Q3	RN1006	TR. S1, NPN	R78	NO USE	
Q4	2SC3402	TR. S1, NPN	R79	RN14BK2C1501F	RES. METAL FILM 1.5K 1% 1/6W
Q5	2SC3402	TR. S1, NPN	R80	RN14BK2C1001F	RES. METAL FILM 1K 1% 1/6W
Q6	2SC3402	TR. S1, NPN	R81	RN14BK2C1001F	RES. METAL FILM 1K 1% 1/6W
Q7	RN1006	TR. S1, NPN	R82	RN14BK2C1001F	RES. METAL FILM 1K 1% 1/6W
Q8	RN1006	TR. S1, NPN	R85	RN14BK2C1001F	RES. METAL FILM 1K 1% 1/6W
Q9	RN1006	TR. S1, NPN	R91	RN14BK2C4700F	RES. METAL FILM 470 1% 1/6W
Q10	RN1006	TR. S1, NPN	R92	RN14BK2C4700F	RES. METAL FILM 470 1% 1/6W
Q13	2SC1907	TR. S1, NPN	R93	RN14BK2C4700F	RES. METAL FILM 470 1% 1/6W
Q14	2SC1907	TR. S1, NPN	R98	RN14BK2C2003F	RES. METAL FILM 200K 1% 1/6W
Q15	2SC1907	TR. S1, NPN	R99	RN14BK2C8203F	RES. METAL FILM 820K 1% 1/6W
Q16	2SC1907	TR. S1, NPN	R100	RN14BK2C1003F	RES. METAL FILM 100K 1% 1/6W
Q17	2SC1907	TR. S1, NPN	R101	RN14BK2C2003F	RES. METAL FILM 200K 1% 1/6W
Q18	2SC1907	TR. S1, NPN	R102	RN14BK2C1500F	RES. METAL FILM 150 1% 1/6W
R1	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W	R103	RN14BK2C6342F	RES. METAL FILM 63.4K 1% 1/6W
R2	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W	R104	RN14BK2C2482F	RES. METAL FILM 24.8K 1% 1/6W
R3	RN14BK2C1212F	RES. METAL FILM 12.1K 1% 1/6W	R105	RN14BK2C4323D	RES. METAL FILM 432K 0.5% 1/6W
R4	RN14BK2C1212F	RES. METAL FILM 12.1K 1% 1/6W	R106	RN14BK2C1402F	RES. METAL FILM 14K 1% 1/6W
R5	RN14BK2C1003F	RES. METAL FILM 100K 1% 1/6W	R107	RN14BK2C1473F	RES. METAL FILM 147K 1% 1/6W
R6	RN14BK2C1000F	RES. METAL FILM 100 1% 1/6W	R108	RN14BK2C3322F	RES. METAL FILM 33.2K 1% 1/6W
R7	RN14BK2C8982D	RES. METAL FILM 89.8K 0.5% 1/6W	R109	RN14BK2C2552F	RES. METAL FILM 25.5K 1% 1/6W
R8	RN14BK2C1112D	RES. METAL FILM 11.1K0.5% 1/6W	R110	RN14BK2C1073F	RES. METAL FILM 107K 1% 1/6W
R9	RN14BK2C6812D	RES. METAL FILM 68.1K 0.5% 1/6W	R111	NO USE	
R10	RN14BK2C4642D	RES. METAL FILM 46.4K 0.5% 1/6W	R112	RN14BK2C1003F	RES. METAL FILM 100K 1% 1/6W
R11	RN14BK2C1003D	RES. METAL FILM 100K 0.5% 1/6W	R113	RN14BK2C1000F	RES. METAL FILM 100 1% 1/6W
R12	RN14BK2C1003F	RES. METAL FILM 100K 1% 1/6W	R114	RN14BK2C6800F	RES. METAL FILM 680 1% 1/6W
R13	RN14BK2C1212F	RES. METAL FILM 12.1K 1% 1/6W	R115	RN14BK2C6800F	RES. METAL FILM 680 1% 1/6W
R14	RN14BK2C1212F	RES. METAL FILM 12.1K 1% 1/6W	R116	RN14BK2C6800F	RES. METAL FILM 680 1% 1/6W
R15	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W	R117	RN14BK2C4991F	RES. METAL FILM 4.99K 1% 1/6W
R16	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W	R118	RN14BK2C4991F	RES. METAL FILM 4.99K 1% 1/6W
R17	RN14BK2C1003F	RES. METAL FILM 100K 1% 1/6W	R119	RN14BK2C4991F	RES. METAL FILM 4.99K 1% 1/6W
			R125	RN14BK2C3901F	RES. METAL FILM 3.9K 1% 1/6W
			R126	RN14BK2C2402F	RES. METAL FILM 24K 1% 1/6W
			R127	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W
			R128	RN14BK2C2002F	RES. METAL FILM 20K 1% 1/6W

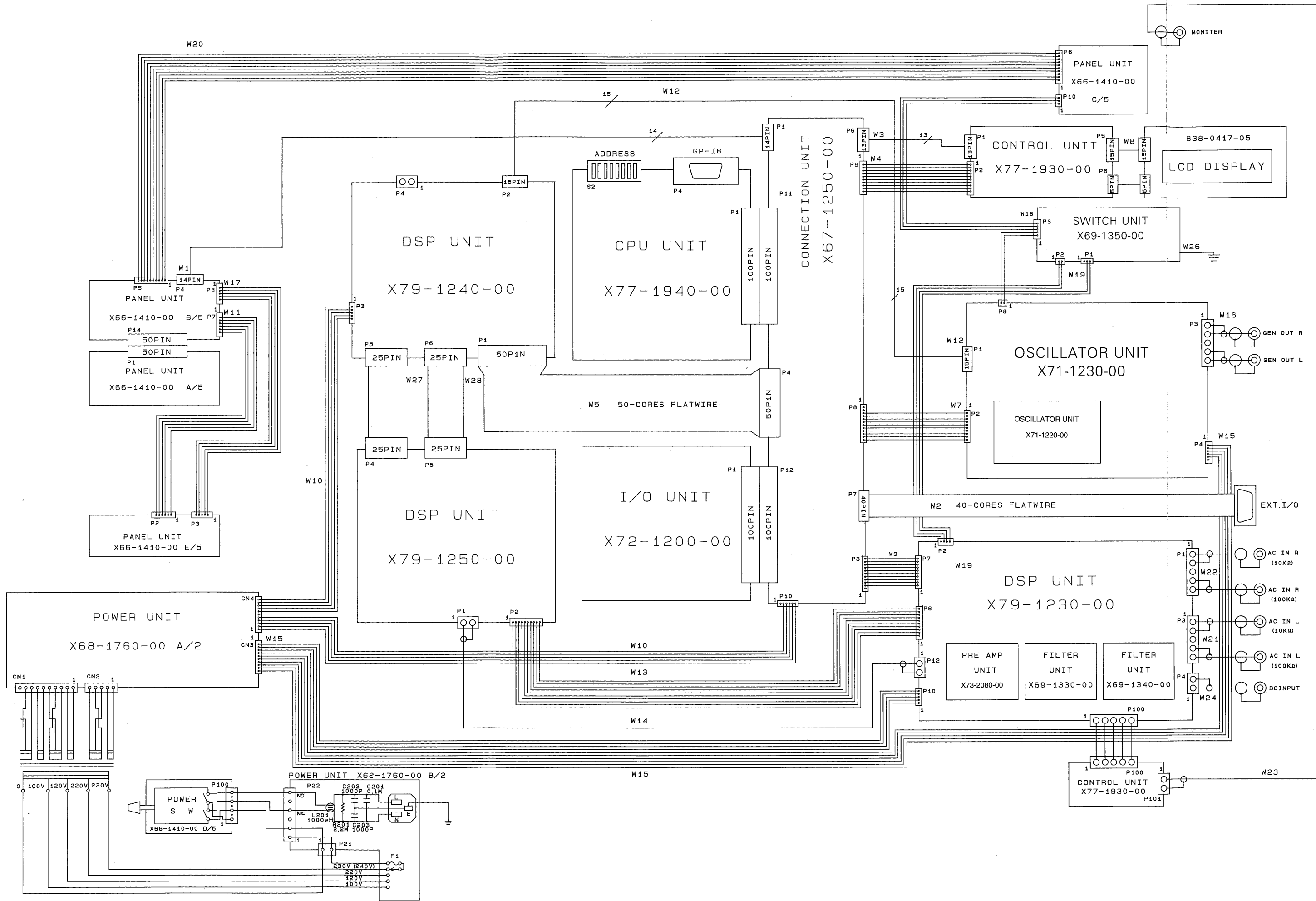




# PARTS LIST

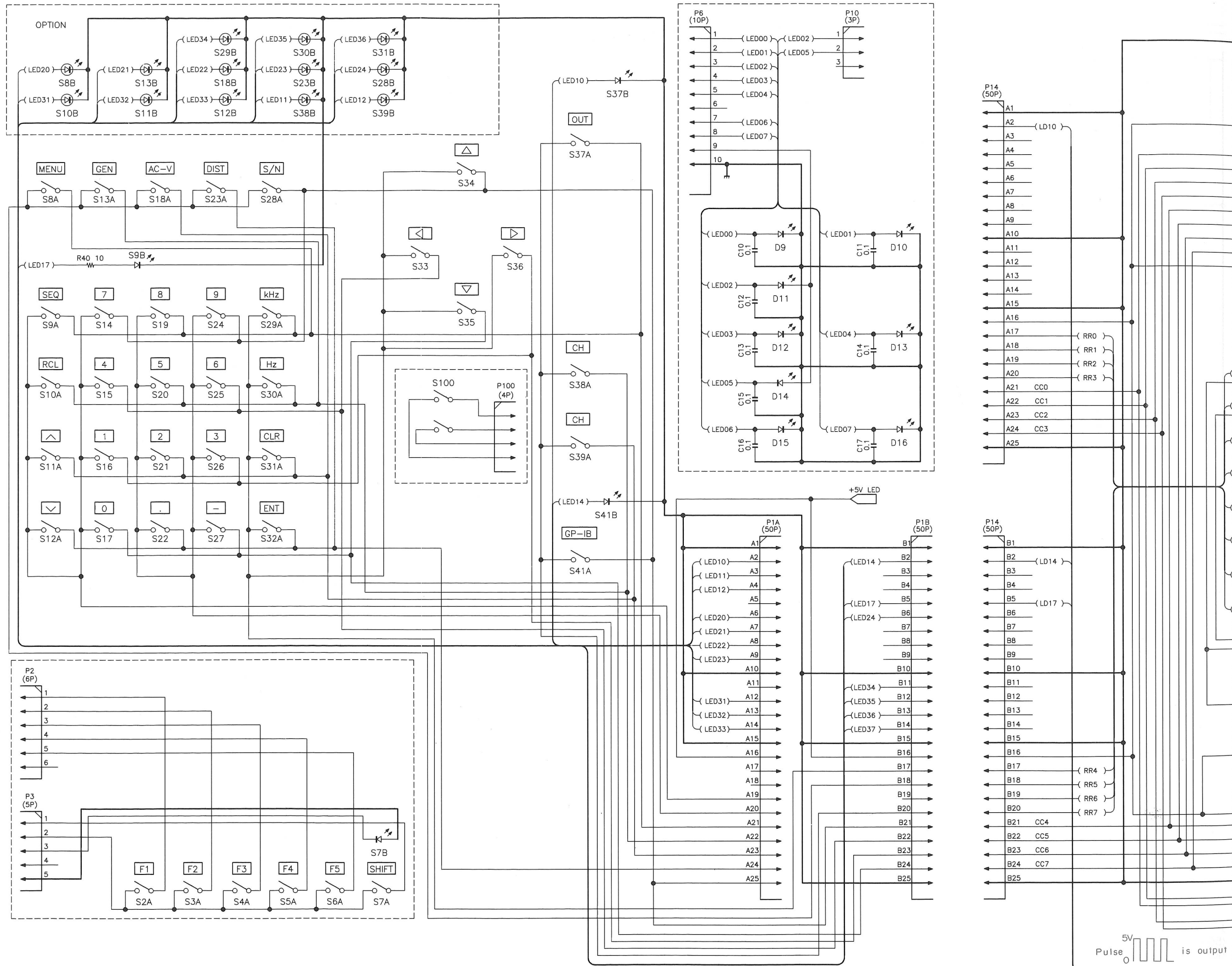
REF. NO	PARTS NO	NAME & DESCRIPTION
D7	NO USE	
D8	1SS226	DIODE
D9	RDS. 1M	DIODE, ZENER
D15	HVR100	DIODE
D801	MTZ5.1JB	DIODE, ZENER
L1	L40-1281-80	FERRI INDUCTOR 120NH 10%
L2	L40-1001-81	FERRI INDUCTOR 10UH 10%
L3	L40-1011-81	FERRI INDUCTOR 100UH 10%
L4	L33-0839-05	FERRI INDUCTOR 10UH 20%
L5	L33-0839-05	FERRI INDUCTOR 10UH 20%
L6	L40-1011-81	FERRI INDUCTOR 100UH 10%
L7	NO USE	
L8	L40-1011-81	FERRI INDUCTOR 100UH 10%
P1	E40-3260-05	PIN CONNECTOR 2P
P2	E40-3268-05	PIN CONNECTOR 10P
P3	NO USE	
P4	E40-3853-05	PIN CONNECTOR 25P
P5	E40-3853-05	PIN CONNECTOR 25P
PH1	TLP2630	IC, PHOTO COUPLER
PH2	TLP521-4	IC, PHOTO COUPLER
PH3	TLP2630	IC, PHOTO COUPLER
PH4	TLP2630	IC, PHOTO COUPLER
Q1	2SC3099	TR. SI, NPN
Q2	2SC3099	TR. SI, NPN
R1	RN73FH2A101D	RES. METAL FILM 100 0.5% 1/10W
R2	RN73FH2A513D	RES. METAL FILM 51K 0.5% 1/10W
R3	RN73FH2A103D	RES. METAL FILM 10K 0.5% 1/10W
R4	RN73FH2A302D	RES. METAL FILM 3K 0.5% 1/10W
R5	RN73FH2A101D	RES. METAL FILM 100 0.5% 1/10W
R6	RN73FH2A302D	RES. METAL FILM 3K 0.5% 1/10W
R7	RN73FH2A330D	RES. METAL FILM 33 0.5% 1/10W
R8	RN73FH2A330D	RES. METAL FILM 33 0.5% 1/10W
R9	RN73FH2A101D	RES. METAL FILM 100 0.5% 1/10W
R10	RN73FH2A682D	RES. METAL FILM 6.8K 0.5% 1/10W
R11	RN73FH2A682D	RES. METAL FILM 6.8K 0.5% 1/10W
R12	RN73FH2A101D	RES. METAL FILM 100 0.5% 1/10W
R13	RN73FH2A472D	RES. METAL FILM 4.7K 0.5% 1/10W
R14	RN73FH2A392D	RES. METAL FILM 3.9K 0.5% 1/10W
R15	RN73FH2A102D	RES. METAL FILM 1K 0.5% 1/10W
R16	RN73FH2A470D	RES. METAL FILM 47 0.5% 1/10W
R17	RN73FH2A470D	RES. METAL FILM 47 0.5% 1/10W
R18	RN73FH2A101D	RES. METAL FILM 100 0.5% 1/10W
R19	R92-0670-05	JUMPING RES. ZERO OHM
R20	R92-0670-05	JUMPING RES. ZERO OHM
R21	NO USE	
R22	RN73FH2A101D	RES. METAL FILM 100 0.5% 1/10W
R23	RN73FH2A102D	RES. METAL FILM 1K 0.5% 1/10W
R24	NO USE	
R25	RN73FH2A101D	RES. METAL FILM 100 0.5% 1/10W
R26	RN73FH2A513D	RES. METAL FILM 51K 0.5% 1/10W
R27	RN73FH2A472D	RES. METAL FILM 4.7K 0.5% 1/10W
R28	RN73FH2A103D	RES. METAL FILM 10K 0.5% 1/10W
R29	RN73FH2A103D	RES. METAL FILM 10K 0.5% 1/10W
R30	RN73FH2A472D	RES. METAL FILM 4.7K 0.5% 1/10W
R31	RN73FH2A101D	RES. METAL FILM 100 0.5% 1/10W
R32	RN73FH2A101D	RES. METAL FILM 100 0.5% 1/10W
R33	RN73FH2A102D	RES. METAL FILM 1K 0.5% 1/10W
R34	RN73FH2A102D	RES. METAL FILM 1K 0.5% 1/10W
R35	RN73FH2A102D	RES. METAL FILM 1K 0.5% 1/10W
R36	RN73FH2A222D	RES. METAL FILM 2.2K 0.5% 1/10W
R37	RN73FH2A331D	RES. METAL FILM 330 0.5% 1/10W
R38	RN73FH2A102D	RES. METAL FILM 1K 0.5% 1/10W
R39	RN73FH2A102D	RES. METAL FILM 1K 0.5% 1/10W
R40	RN73FH2A102D	RES. METAL FILM 1K 0.5% 1/10W
R41	RN73FH2A102D	RES. METAL FILM 1K 0.5% 1/10W
R42	RN73FH2A222D	RES. METAL FILM 2.2K 0.5% 1/10W
R43	RN73FH2A102D	RES. METAL FILM 1K 0.5% 1/10W
R44	RN73FH2A102D	RES. METAL FILM 1K 0.5% 1/10W
R45	RN73FH2A102D	RES. METAL FILM 1K 0.5% 1/10W
R46	RN73FH2A101D	RES. METAL FILM 100 0.5% 1/10W
R47	RN73FH2A201D	RES. METAL FILM 200 0.5% 1/10W
R48	RN73FH2A202D	RES. METAL FILM 2K 0.5% 1/10W
R49	RN73FH2A101D	RES. METAL FILM 100 0.5% 1/10W
R50	RN73FH2A101D	RES. METAL FILM 100 0.5% 1/10W
R51	RN73FH2A564D	RES. METAL FILM 560K 0.5% 1/10W
R52	RN73FH2A433D	RES. METAL FILM 43K 0.5% 1/10W
R53	RN73FH2A203D	RES. METAL FILM 20K 0.5% 1/10W
R54	RN73FH2A331D	RES. METAL FILM 330 0.5% 1/10W
R55	RN73FH2A103D	RES. METAL FILM 10K 0.5% 1/10W
R56	RN73FH2A103D	RES. METAL FILM 10K 0.5% 1/10W
R57	RN73FH2A152D	RES. METAL FILM 1.5K 0.5% 1/10W
R58	NO USE	
R59	RN73FH2A123D	RES. METAL FILM 12K 0.5% 1/10W
R60	RN73FH2A221D	RES. METAL FILM 220 0.5% 1/10W
R61	RN73FH2A221D	RES. METAL FILM 220 0.5% 1/10W
R62	RN73FH2A221D	RES. METAL FILM 220 0.5% 1/10W
R63	RN73FH2A221D	RES. METAL FILM 220 0.5% 1/10W
R64	RN73FH2A221D	RES. METAL FILM 220 0.5% 1/10W
R65	RN73FH2A221D	RES. METAL FILM 220 0.5% 1/10W
R66	RN73FH2A331D	RES. METAL FILM 330 0.5% 1/10W
R67	RN73FH2A331D	RES. METAL FILM 330 0.5% 1/10W

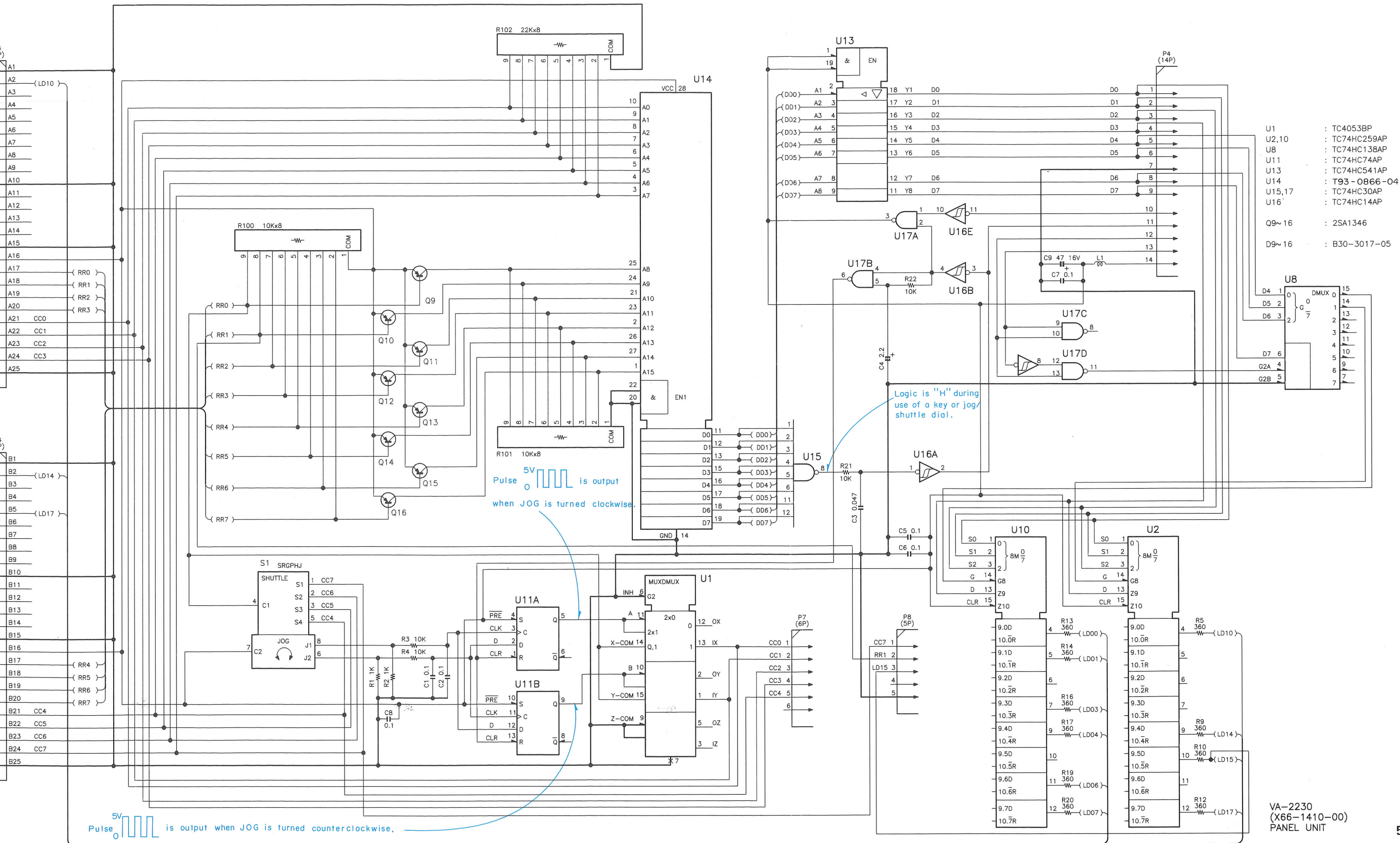
REF. NO	PARTS NO	NAME & DESCRIPTION
R68	RN73FH2A331D	RES. METAL FILM 330 0.5% 1/10W
R69	RN73FH2A202D	RES. METAL FILM 2K 0.5% 1/10W
R70	NO USE	
R71	RN73FH2A202D	RES. METAL FILM 2K 0.5% 1/10W
R72	RN73FH2A202D	RES. METAL FILM 2K 0.5% 1/10W
R73	RN73FH2A331D	RES. METAL FILM 330 0.5% 1/10W
R74	RN73FH2A331D	RES. METAL FILM 330 0.5% 1/10W
R75	RN73FH2A331D	RES. METAL FILM 330 0.5% 1/10W
R76	RN73FH2A331D	RES. METAL FILM 330 0.5% 1/10W
R77	RN73FH2A102D	RES. METAL FILM 1K 0.5% 1/10W
R78	RN73FH2A222D	RES. METAL FILM 2.2K 0.5% 1/10W
R79	RN73FH2A105D	RES. METAL FILM 1M 0.5% 1/10W
R80	NO USE	
R81	RN73FH2A273D	RES. METAL FILM 27K 0.5% 1/10W
R82	RN73FH2A511D	RES. METAL FILM 510 0.5% 1/10W
R83	RN73FH2A472D	RES. METAL FILM 4.7K 0.5% 1/10W
R84	RN73FH2A103D	RES. METAL FILM 10K 0.5% 1/10W
R85	RN73FH2A101D	RES. METAL FILM 100 0.5% 1/10W
R86	RN73FH2A132D	RES. METAL FILM 1.3K 0.5% 1/10W
R87	RN73FH2A331D	RES. METAL FILM 330 0.5% 1/10W
R88	RN73FH2A331D	RES. METAL FILM 330 0.5% 1/10W
R89	RN73FH2A331D	RES. METAL FILM 330 0.5% 1/10W
R90	RN73FH2A331D	RES. METAL FILM 330 0.5% 1/10W
R801	R92-0150-05	JUMPING RES. ZERO OHM (10NM)
R802	RN14BK2C1002F	RES. METAL FILM 10K 1% 1/6W
S1	S31-1503-05	SLIDE SWITCH
TP1	E23-0401-05	PIN TERMINAL
TP2	E23-0401-05	PIN TERMINAL
TP3	E23-0401-05	PIN TERMINAL
TP4	E23-0401-05	PIN TERMINAL
TP5	E23-0401-05	PIN TERMINAL
TP6	E23-0401-05	PIN TERMINAL
TP7	E23-0401-05	PIN TERMINAL
TP8	E23-0401-05	PIN TERMINAL
U1	UPC4071G2	IC, LOW NOISE JFET INPUT OP-AMP
U2	UPC2711T-E3	IC, LOW NOISE AMP
U3	UPC2711T-E3	IC, LOW NOISE AMP
U4	TD7104F	IC, RECEPTION PRESCALER
U5	SN75ALS192NS	IC, QUAD DIFFERENTIAL LINE DRI.
U6	NB1507PF	IC, PLL FREQUENCY SYNTHESIZER
U7	TC74HC74AF	IC, DUAL D-F.F. PR & CLR
U8	TC74HC393AF	IC, DUAL BINARY COUNTER
U9	TC74HC00AF	IC, QUAD 2-INPUT NAND GATE
U10	NB87086APF	IC, CMOS FREQUENCY SYNTHESIZER
U11	UPC4071G2	IC, LOW NOISE JFET INPUT OP-AMP
U12	TC74HC14AF	IC, HEX SCHMITT INVERTER
U13	TC74HC393AF	IC, DUAL BINARY COUNTER
U14	TC74HC04AF	IC, HEX INVERTER
U17	UPC4072G2	IC, LOW NOISE JFET INPUT OP-AMP
U18	TC4053BF	IC, TRIPLE 2-CH MPX/DE-MPX
U19	TC74HC74AF	IC, DUAL D-F.F. PR & CLR
U20	TC74HC00AF	IC, QUAD 2-INPUT NAND GATE
U21	MAX120CWG	IC, 12BIT SAMPLING A/D CONVERT.
U22	TC74HC166AF	IC, 8-BIT SIPO SHIFT REGISTER
U23	TC74HC166AF	IC, 8-BIT SIPO SHIFT REGISTER
U26	TC74HC257AF	IC, QUAD 2-CH MULTIPLEXER
U27	CTM6080	IC, GATE ARRAY
U28	TC74HC259AF	IC, 8-BIT ADDRESSABLE LATCH
VR1	R32-0839-05	RES. SEMI FIXED 5K
VR2	R32-0848-05	RES. SEMI FIXED 20K



# SCHEMATIC DIAGRAM

PANEL UNIT (X66-1410-00)





- U1 : TC4053BP
- U2,10 : TC74HC259AP
- U8 : TC74HC138AP
- U11 : TC74HC74AP
- U13 : TC74HC541AP
- U14 : T93-0866-04
- U15,17 : TC74HC30AP
- U16 : TC74HC14AP
- Q9~16 : 2SA1346
- D9~16 : B30-3017-05

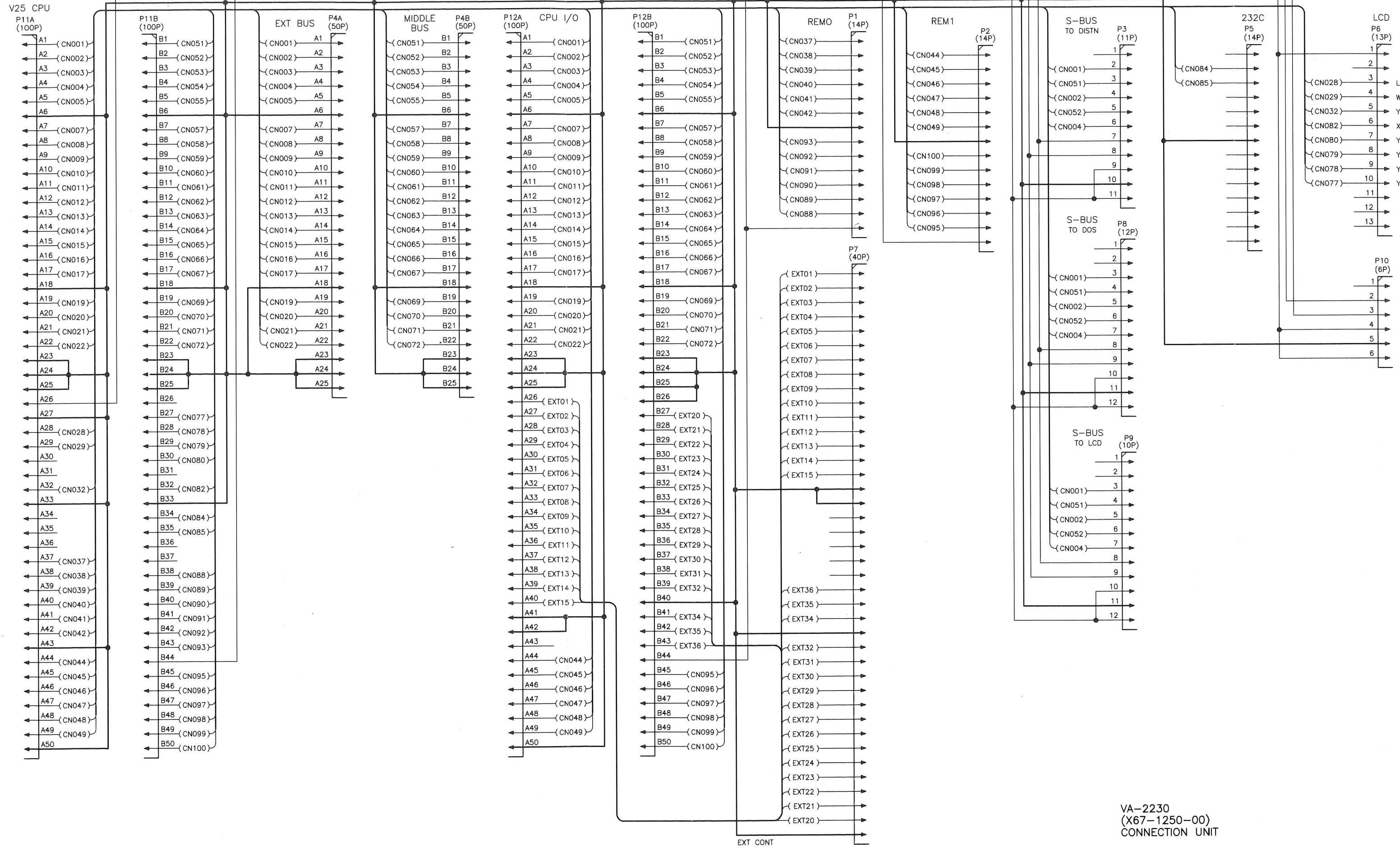
5V Pulse is output when JOG is turned clockwise.

Logic is "H" during use of a key or jog/shuttle dial.

5V Pulse is output when JOG is turned counterclockwise.

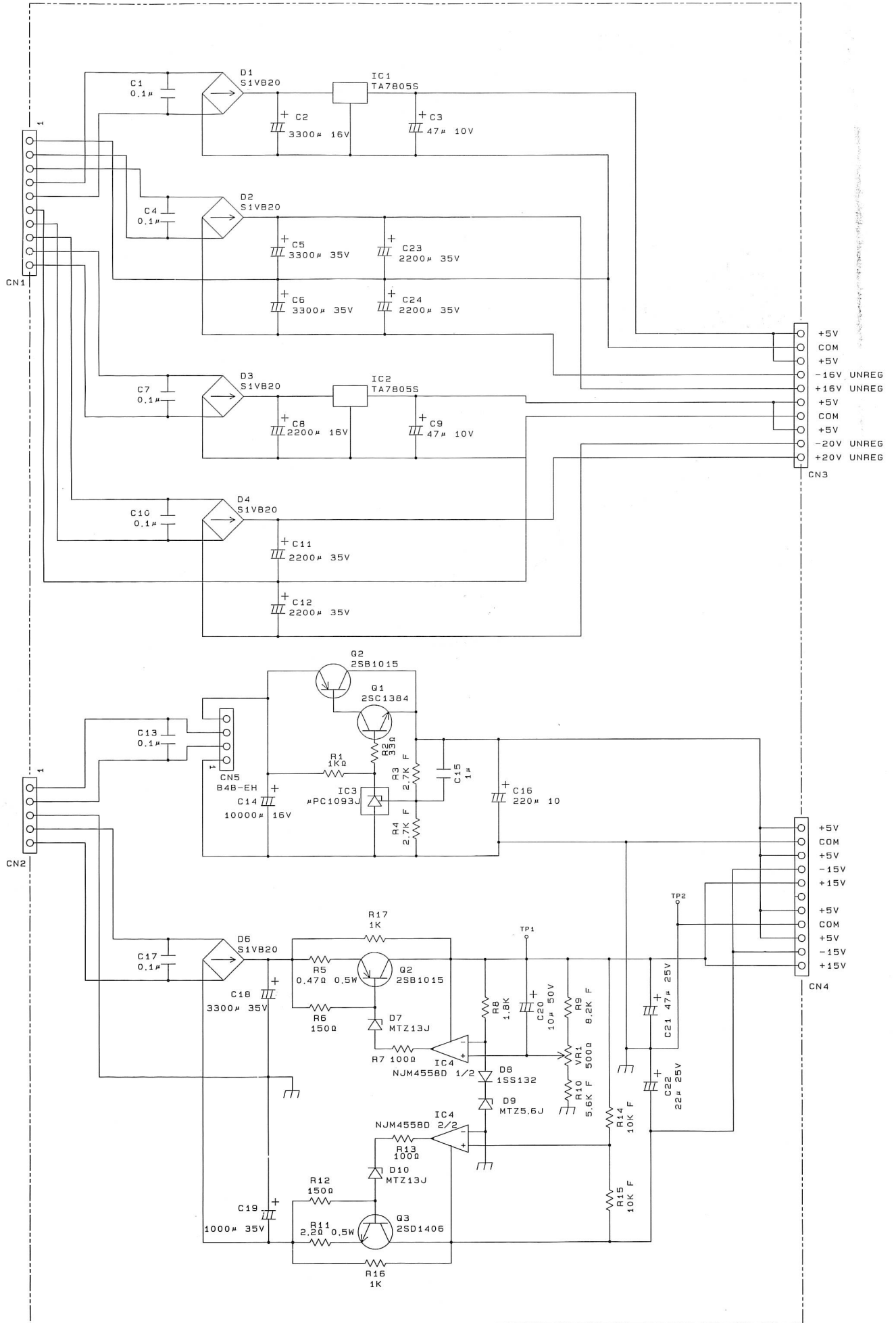


CONNECTION UNIT (X67-1250-00)



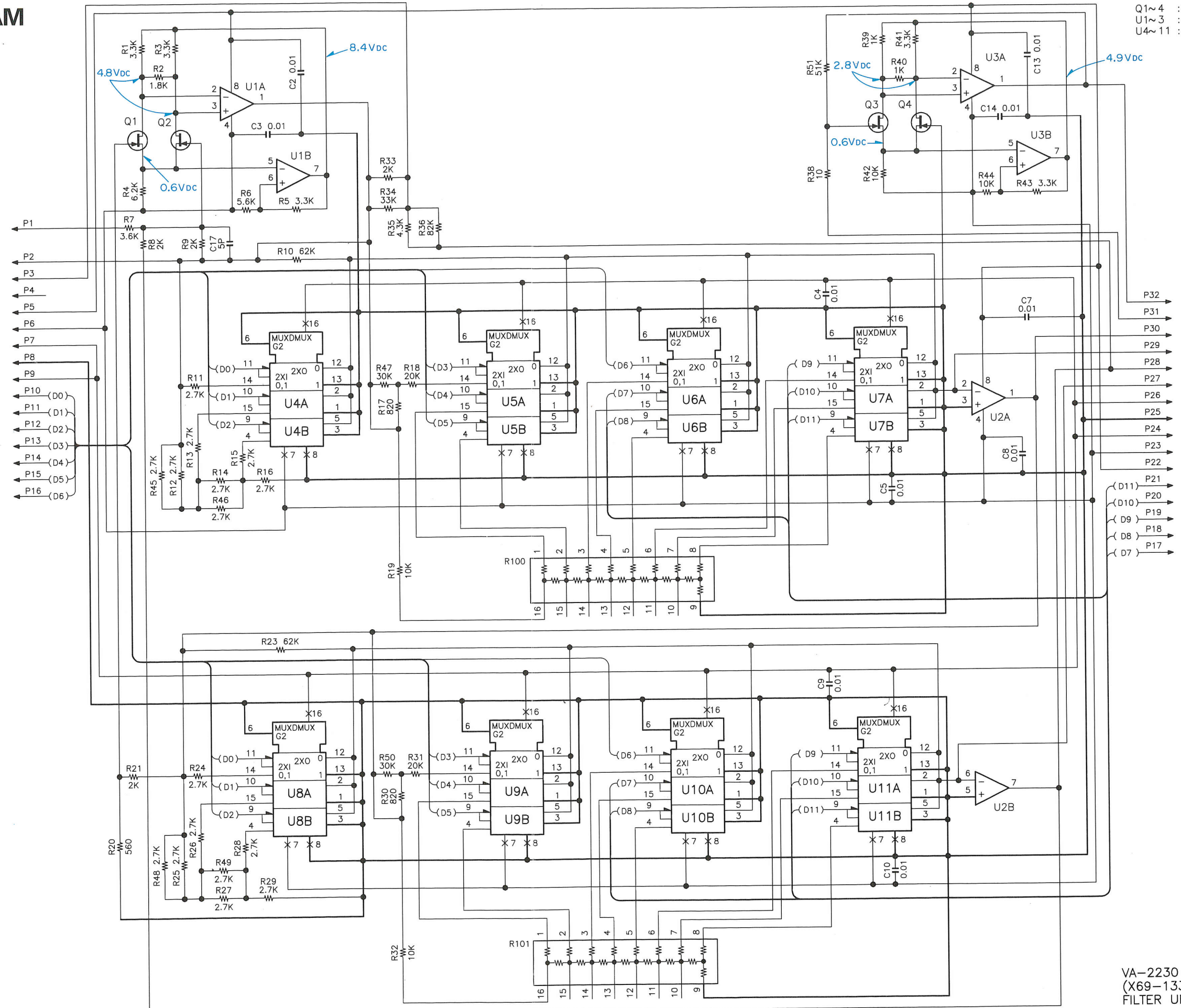
VA-2230  
(X67-1250-00)  
CONNECTION UNIT

POWER UNIT (X68-1760-00)



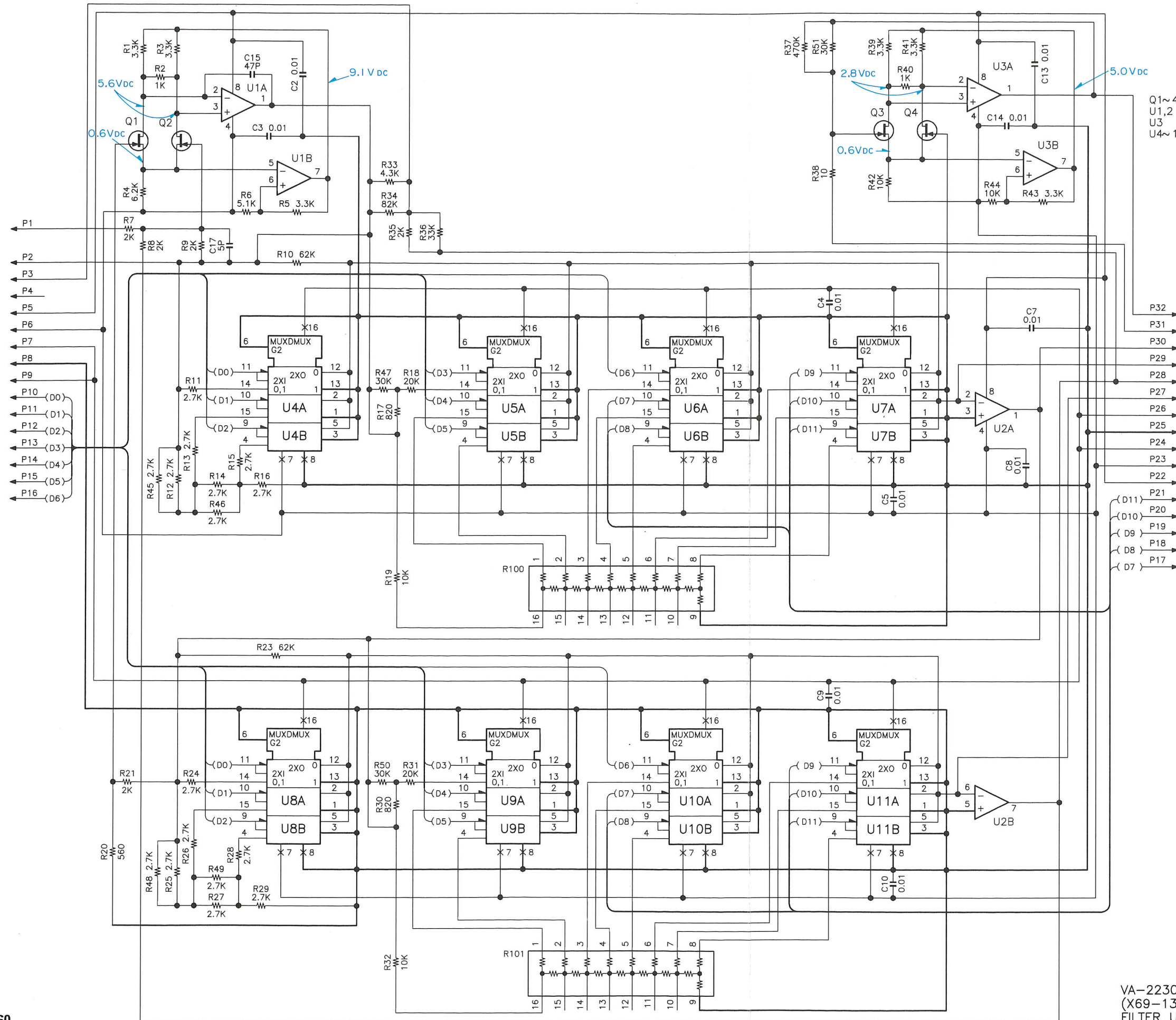
# SCHEMATIC DIAGRAM

FILTER UNIT (X69-1330-00)



Q1~4 : 2SK426 (X26)  
 U1~3 : NJM5532M  
 U4~11 : TC4053BFS

VA-2230  
 (X69-1330-00)  
 FILTER UNIT

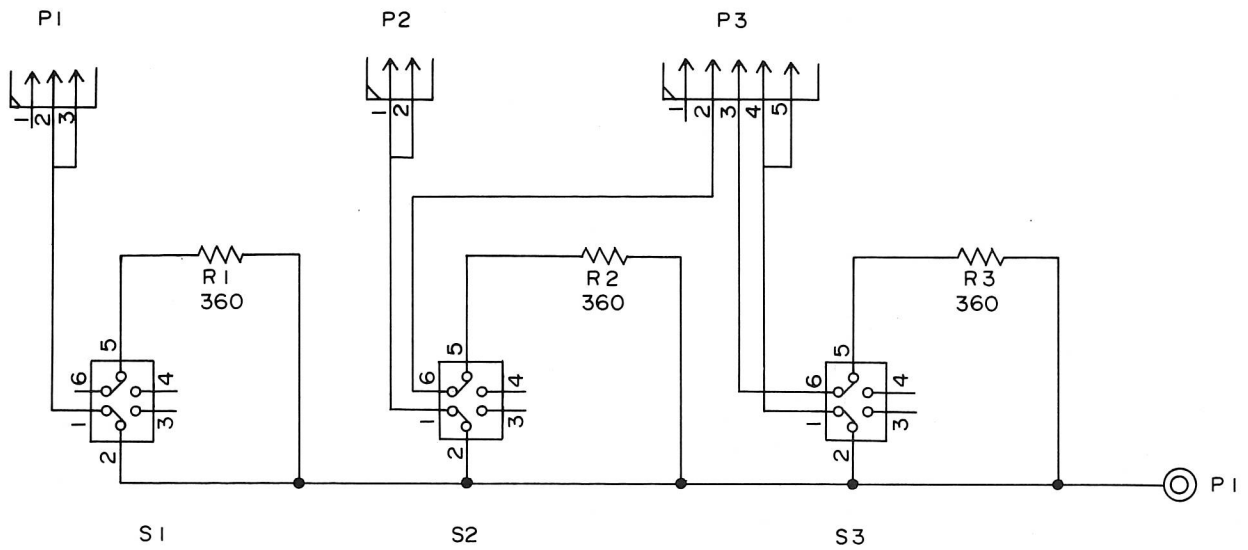


- Q1~4 : 2SK426 (X26)
- U1,2 : NJM4580E
- U3 : NJM5532M
- U4~11 : TC4053BFS

VA-2230  
(X69-1340-00)  
FILTER UNIT

# SCHEMATIC DIAGRAM

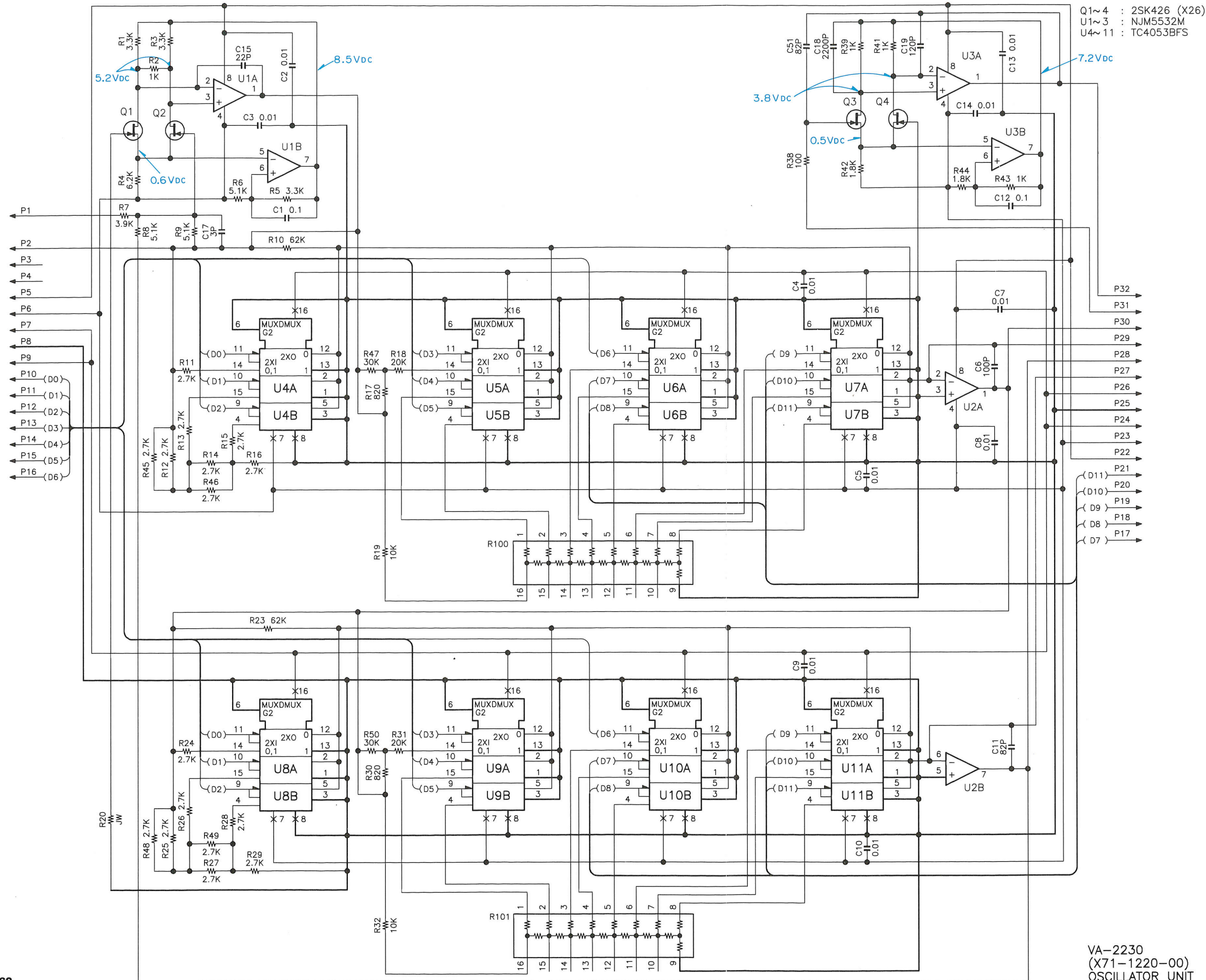
SWITCH UNIT (X69-1350-00)



VA - 2230  
(X69-1350-00)  
Switch unit

OSCILLATOR UNIT (X71-1220-00)

SCHEMATIC DIAGRAM

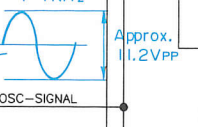
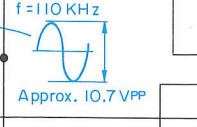
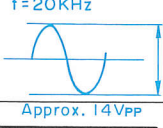
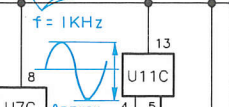
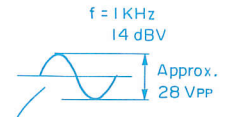
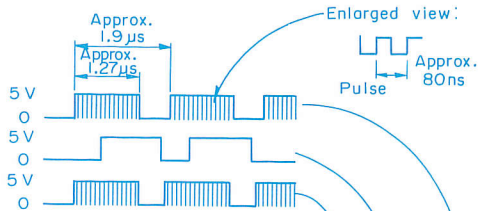
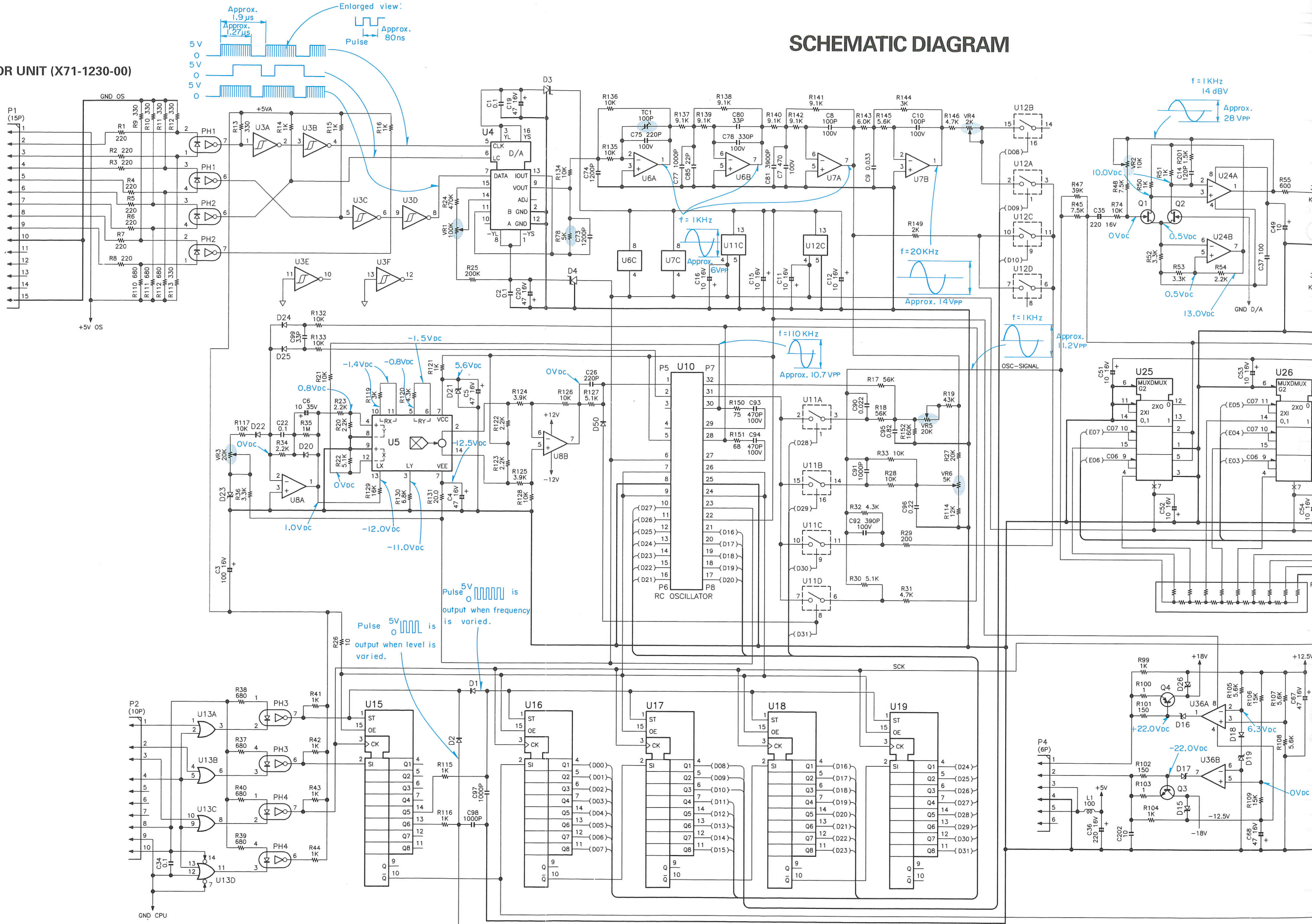


Q1~4 : 2SK426 (X26)  
 U1~3 : NJM5532M  
 U4~11 : TC4053BFS

VA-2230  
 (X71-1220-00)  
 OSCILLATOR UNIT

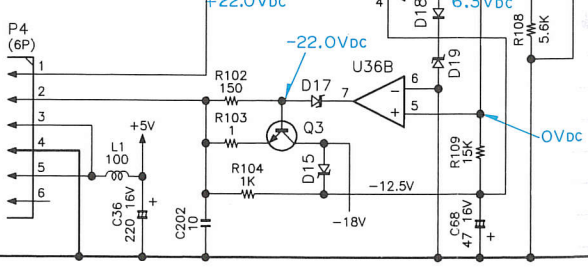
OSCILLATOR UNIT (X71-1230-00)

SCHEMATIC DIAGRAM

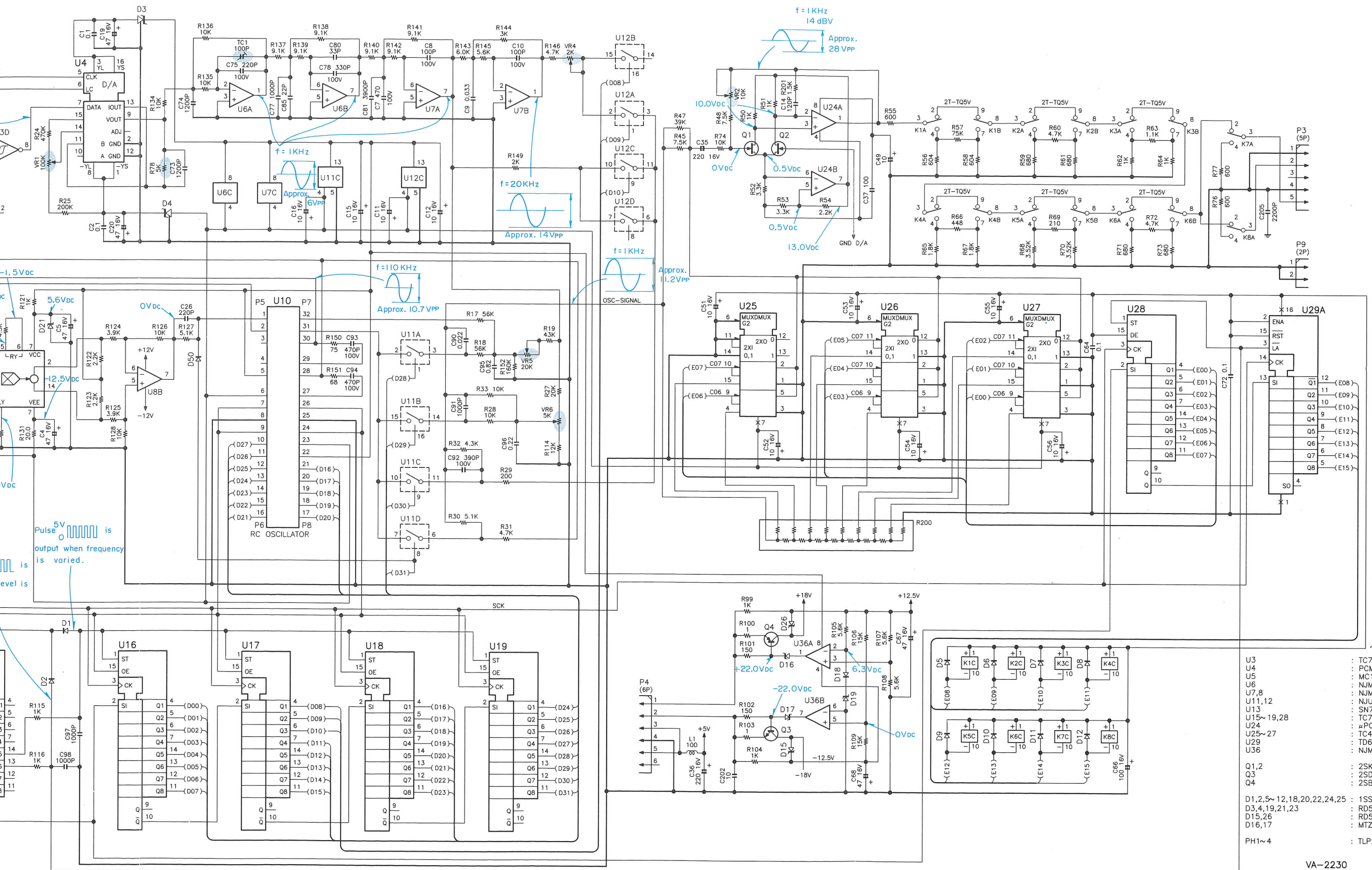


Pulse 5V is output when frequency is varied.

Pulse 5V is output when level is varied.



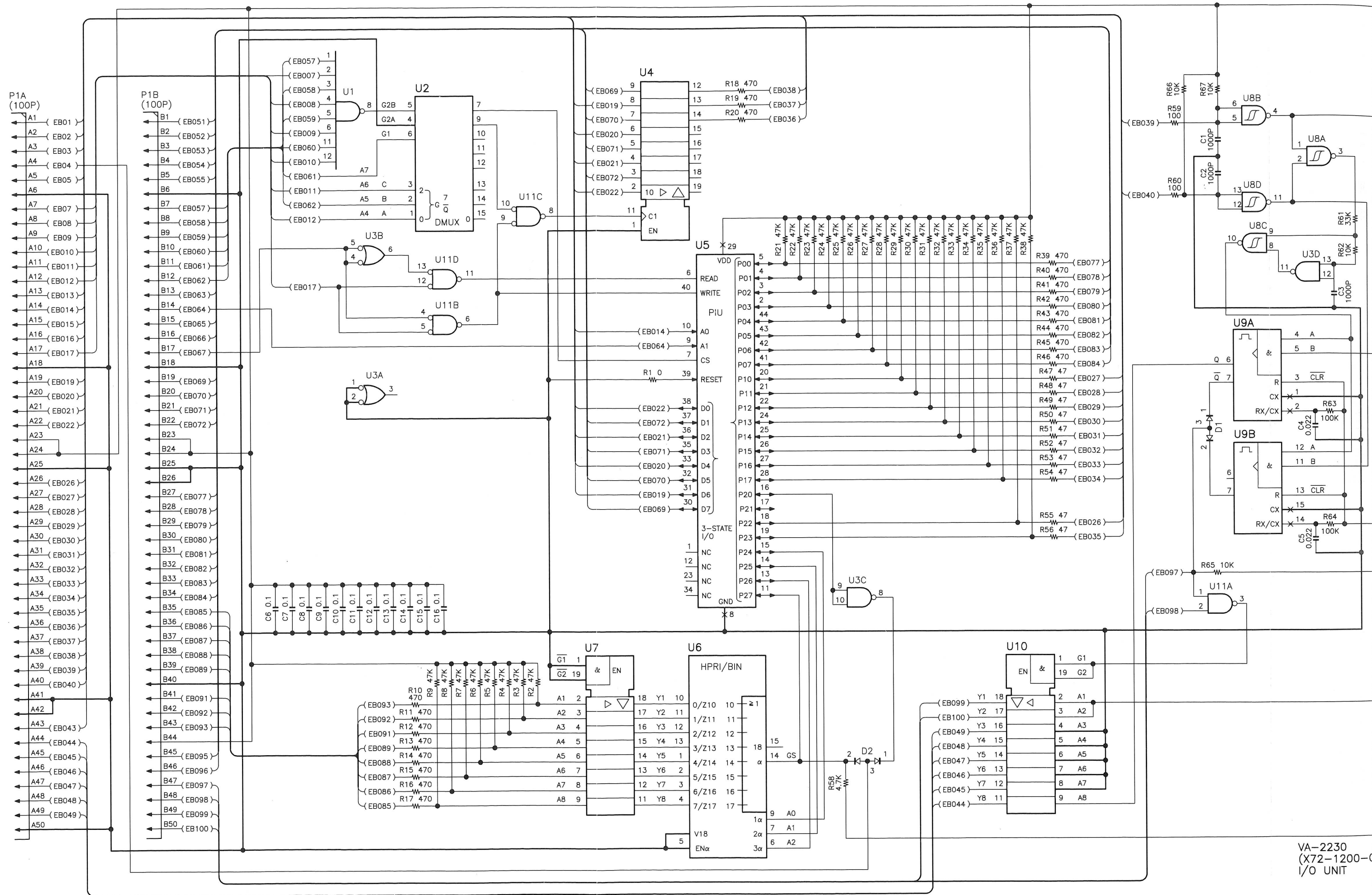
# SCHEMATIC DIAGRAM



- U3 : TC74HC14AP
- U4 : PCM56P-K
- U5 : MC1495M
- U6 : NJM4580D
- U7,8 : NJM5532DD
- U11,12 : NJU201AD
- U13 : SN74LS32N
- U15~19,28 : TC74HC4094AP
- U24 : μPC814C
- U25~27 : TC4053BP
- U29 : TD62801P
- U36 : NJM4558D
- Q1,2 : 2SK190H
- Q3 : 2SD1406(Y)
- Q4 : 2SB1015(Y)
- D1,2,5~12,18,20,22,24,25 : 1SS132
- D3,4,19,21,23 : RD5.6JS
- D15,26 : RD5.6F
- D16,17 : MTZ16JA
- PH1~4 : TLP2630

VA-2230  
(X71-1230-00)  
OSCILLATOR UNIT



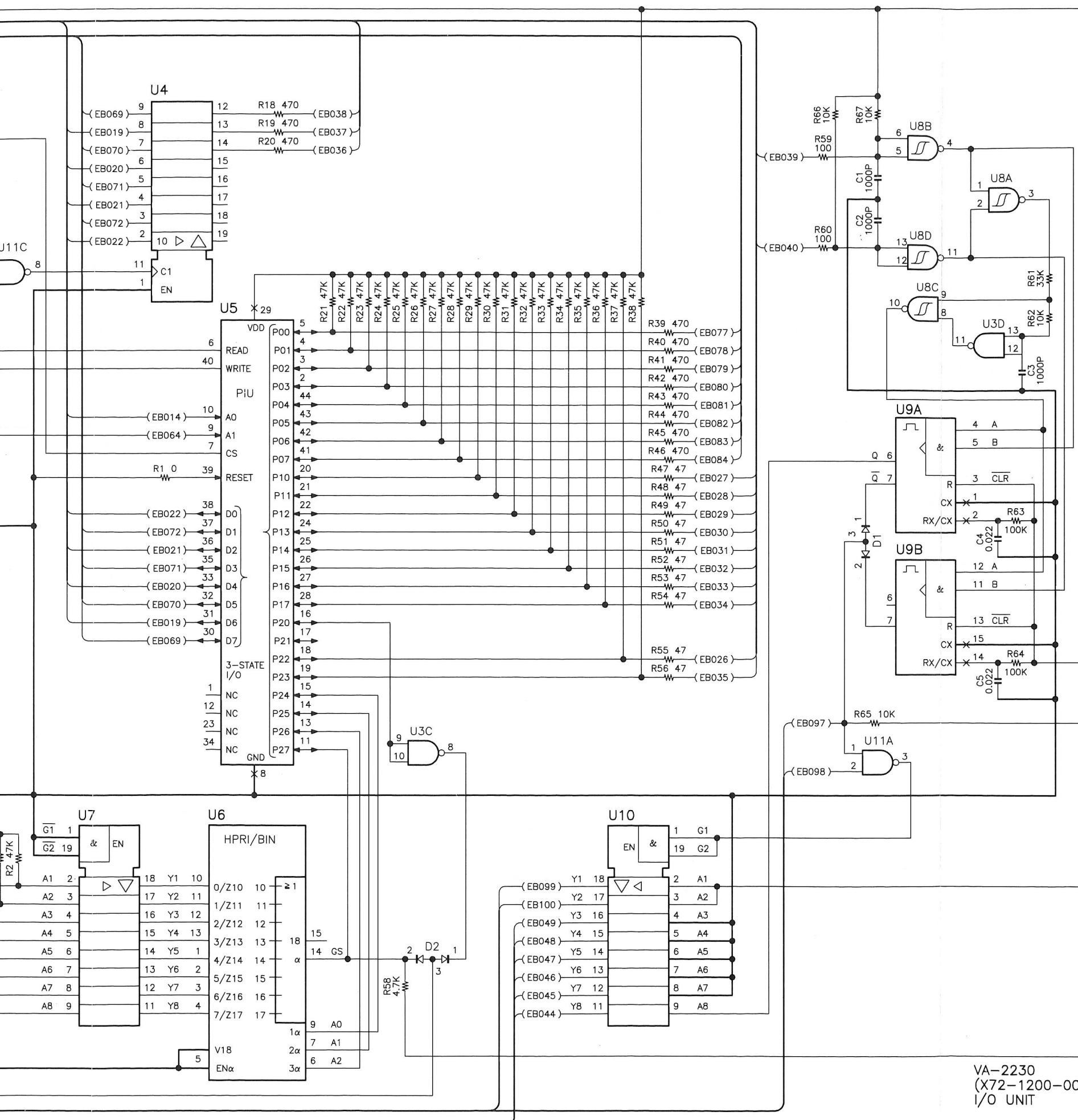


VA-2230  
(X72-1200-0  
I/O UNIT

# SCHEMATIC DIAGRAM

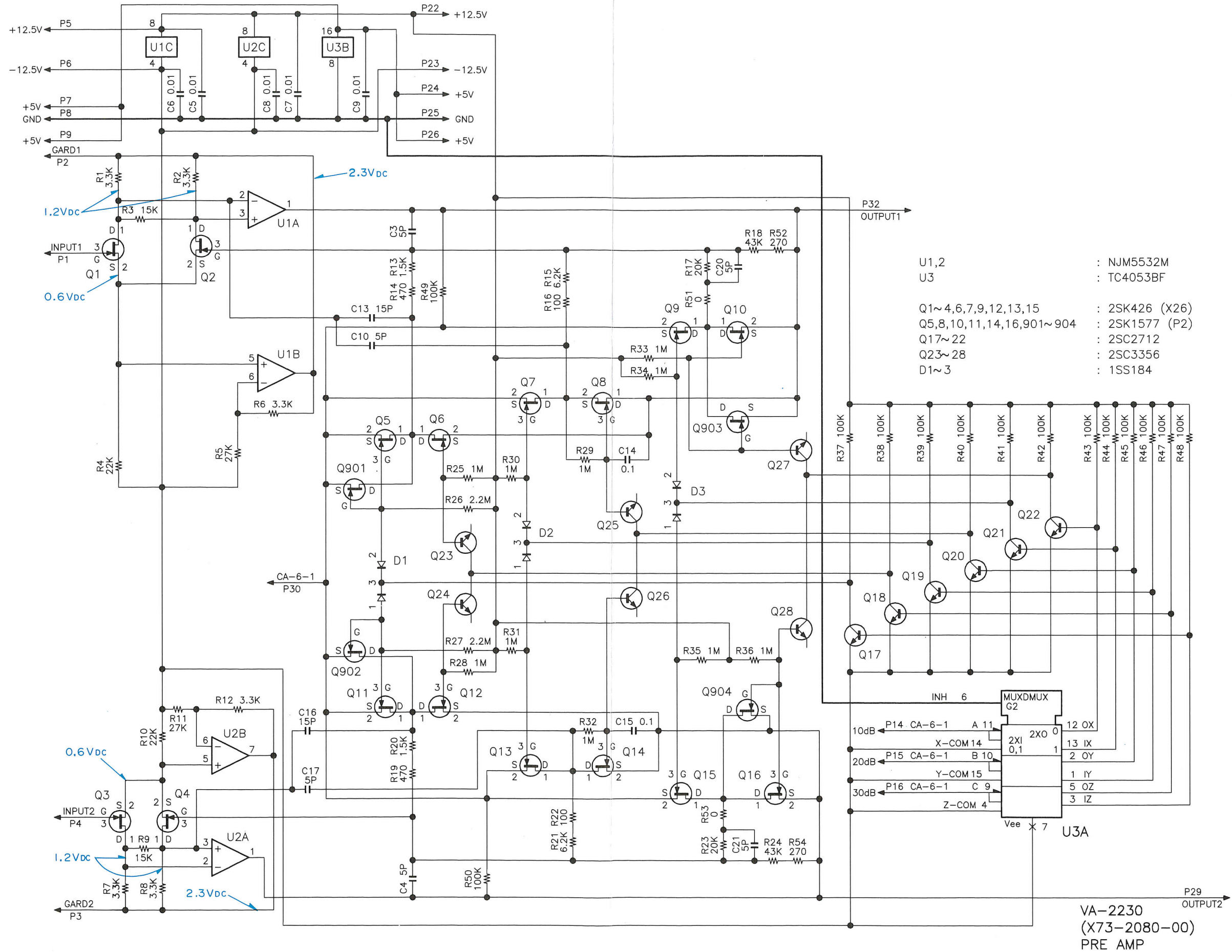
I/O UNIT (X72-1200-00)

- U1 : TC74HC30AF
- U2 : TC74HC138AF
- U3 : TC74HC00AF
- U4 : TC74HC574AF
- U5 : μPD71055L
- U6 : TC74HC148AF
- U7,10 : TC74HC541AF
- U8 : TC4093BF
- U9 : μPD4528BG
- U11 : TC74HC32AF
- D1,2 : 1SS181



VA-2230  
(X72-1200-00)  
I/O UNIT

SCHMATIC DIAGRAM

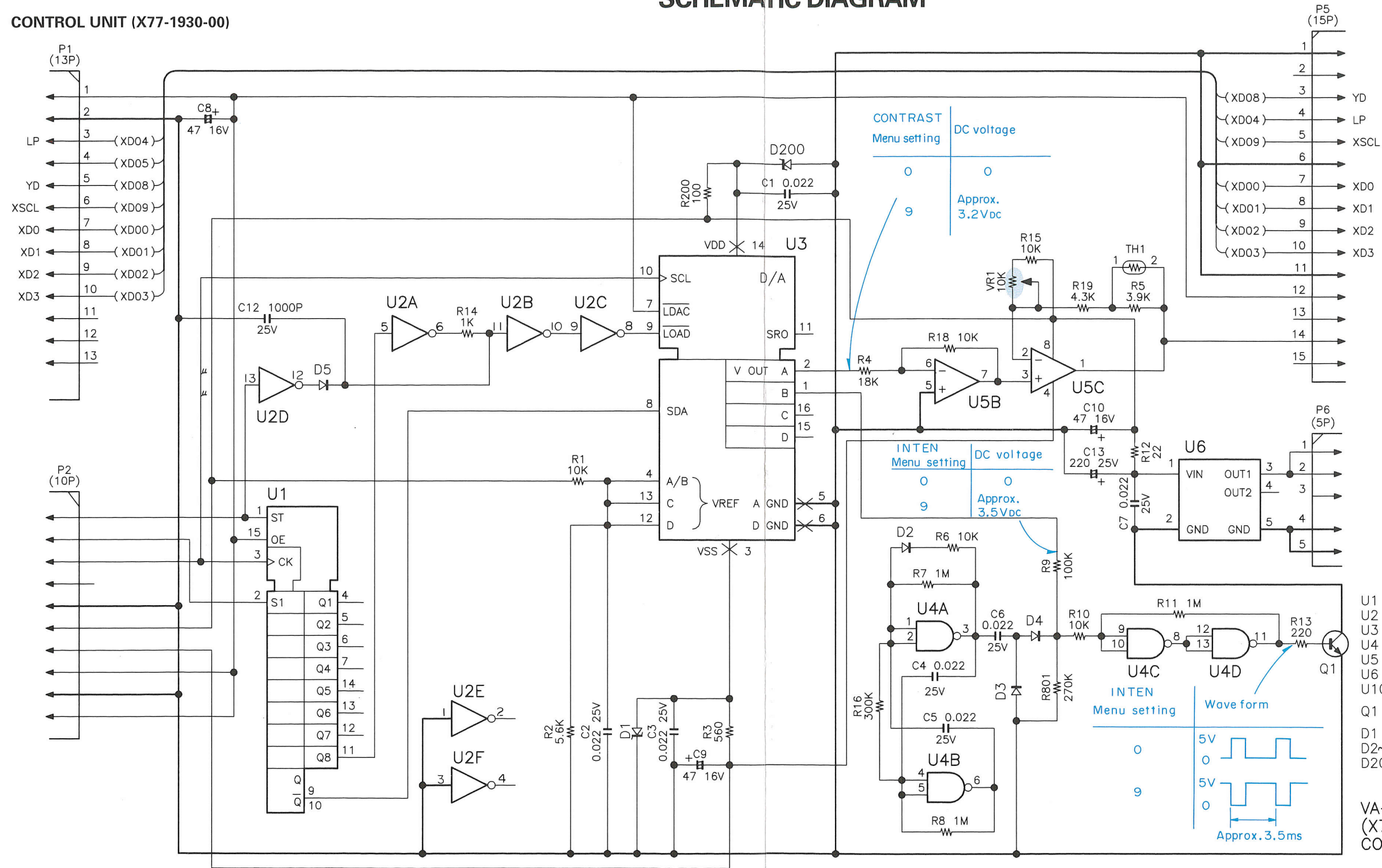


- U1,2 : NJM5532M
- U3 : TC4053BF
- Q1~4,6,7,9,12,13,15 : 2SK426 (X26)
- Q5,8,10,11,14,16,901~904 : 2SK1577 (P2)
- Q17~22 : 2SC2712
- Q23~28 : 2SC3356
- D1~3 : 1SS184

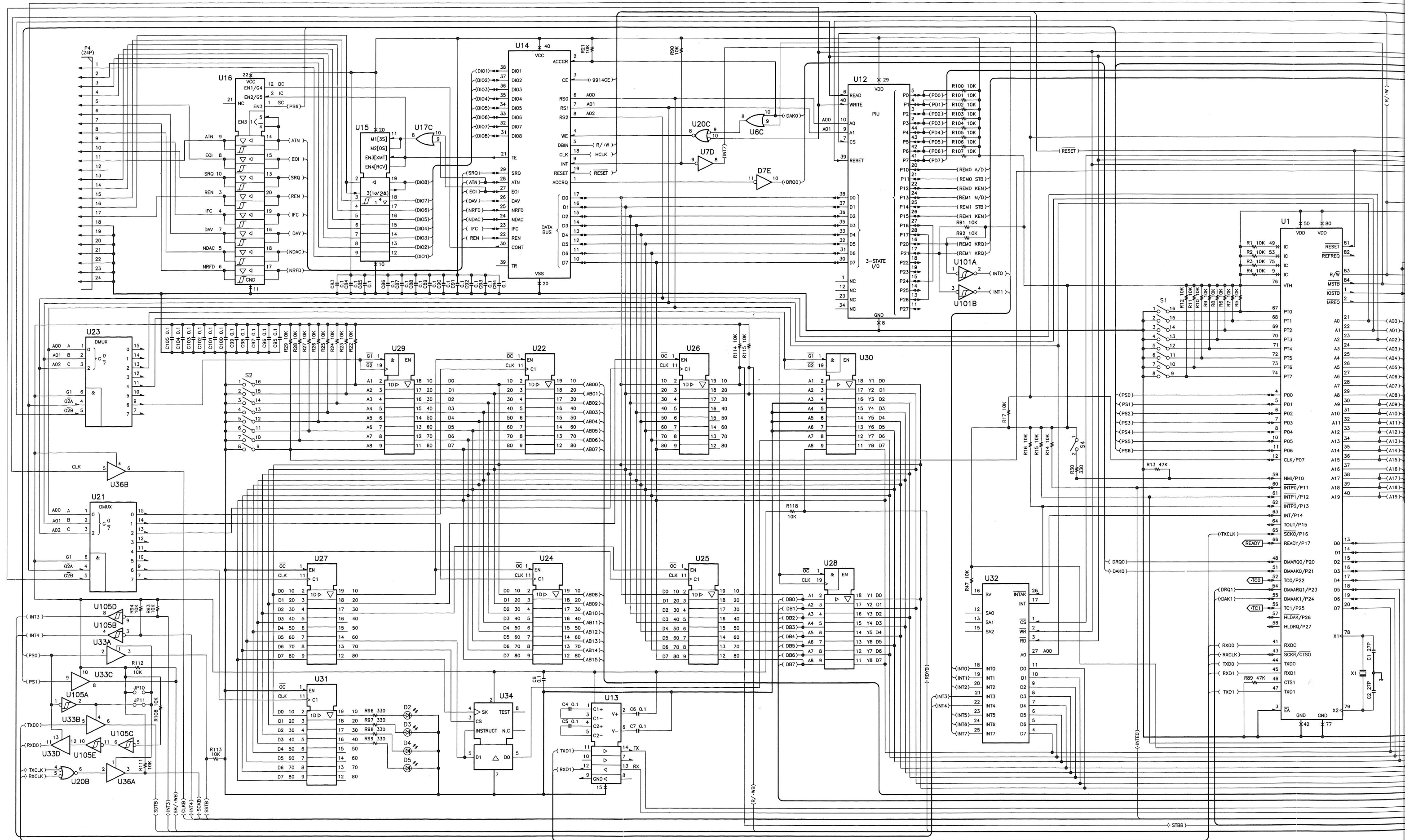
VA-2230  
(X73-2080-00)  
PRE AMP

# SCHEMATIC DIAGRAM

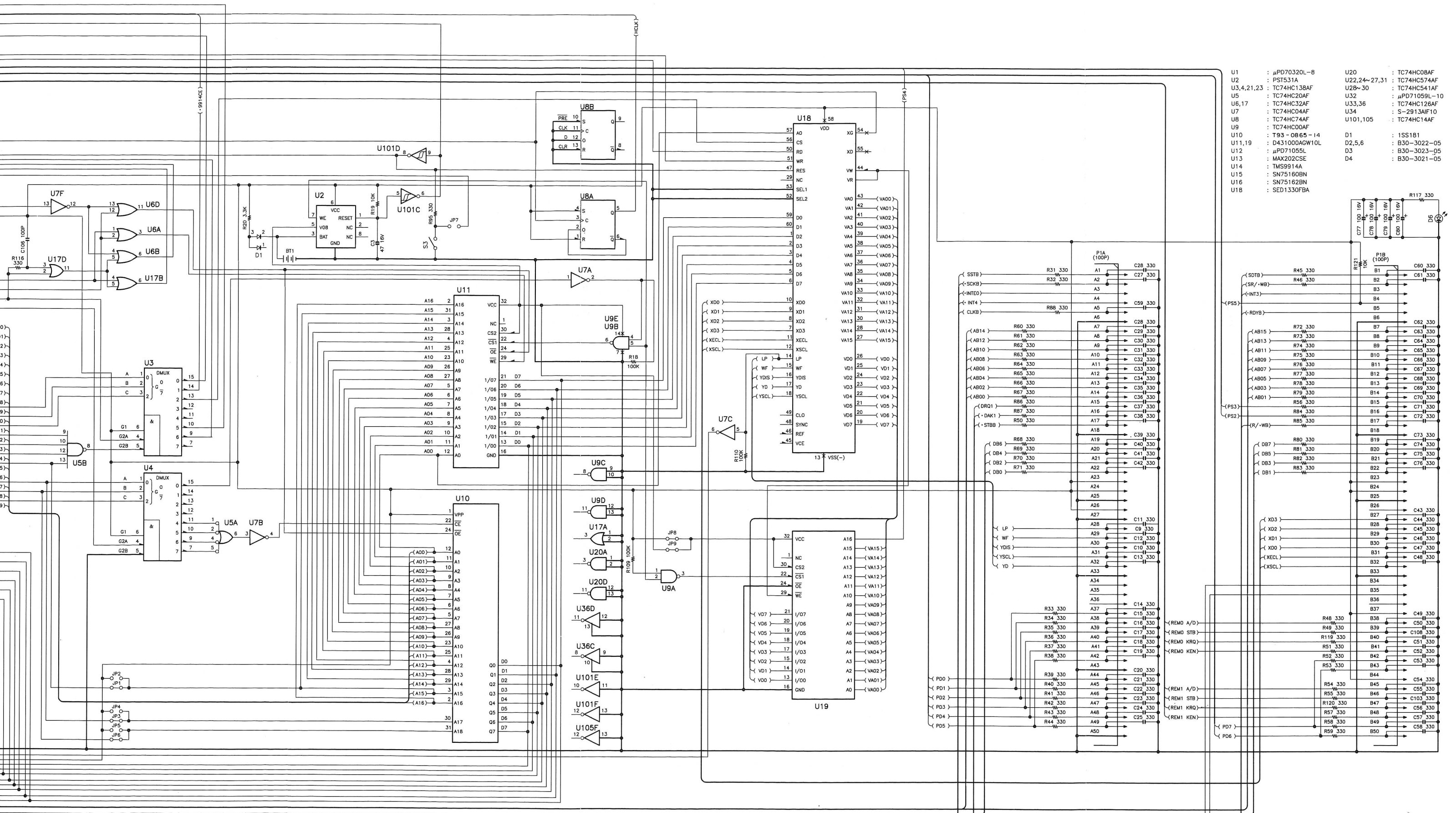
## CONTROL UNIT (X77-1930-00)



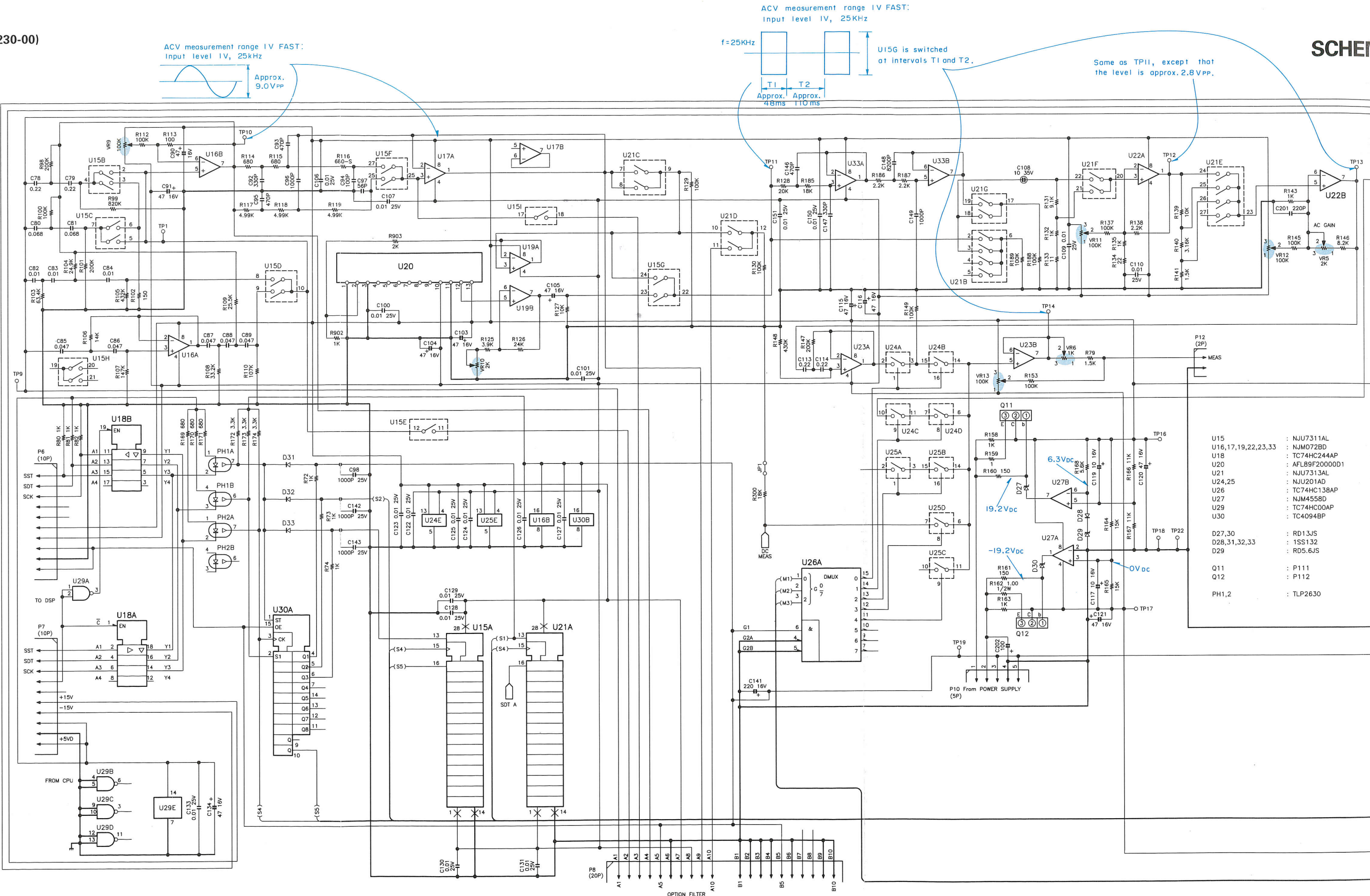
- U1 : TC4094BP
  - U2 : TC74HC04AP
  - U3 : MAX500BCPE
  - U4 : TC74HC00AP
  - U5 : NJM082BD
  - U6 : W02-2305-05
  - U100 : NJM5532D
  - Q1 : 2SC1384(R)
  - D1 : RD5.6JS
  - D2~5,100,101 : 1SS132
  - D200 : MTZ18JB
- VA-2230  
(X77-1930-00)  
CONTROL UNIT



# SCHEMATIC DIAGRAM

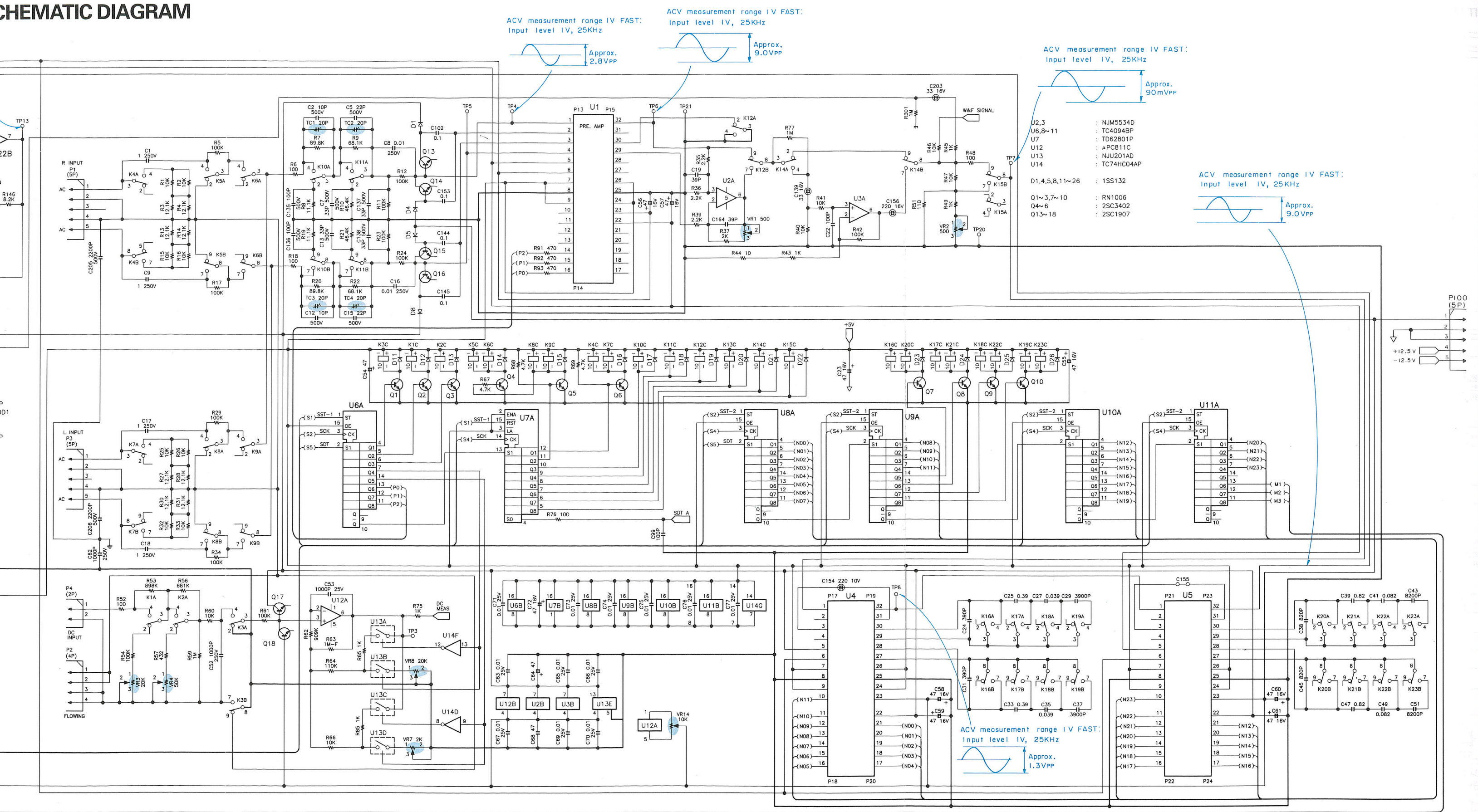


- |                          |                            |
|--------------------------|----------------------------|
| U1 : μPD70320L-B         | U20 : TC74HC08AF           |
| U2 : PST531A             | U22,24~27,31 : TC74HC574AF |
| U3,4,21,23 : TC74HC138AF | U28~30 : TC74HC541AF       |
| U5 : TC74HC20AF          | U32 : μPD71059L-10         |
| U6,17 : TC74HC32AF       | U33,36 : TC74HC126AF       |
| U7 : TC74HC04AF          | U34 : S-2913AIF10          |
| U8 : TC74HC74AF          | U101,105 : TC74HC14AF      |
| U9 : TC74HC00AF          |                            |
| U10 : T93-0865-14        | D1 : 1SS181                |
| U11,19 : D431000AGW10L   | D2,5,6 : B30-3022-05       |
| U12 : μPD71055L          | D3 : B30-3023-05           |
| U13 : MAX202CSE          | D4 : B30-3023-05           |
| U14 : TMS9914A           |                            |
| U15 : SN75160BN          |                            |
| U16 : SN75162BN          |                            |
| U18 : SED1330FBA         |                            |



- |                    |                 |
|--------------------|-----------------|
| U15                | : NJU7311AL     |
| U16,17,19,22,23,33 | : NJM072BD      |
| U18                | : TC74HC244AP   |
| U20                | : AFL89F20000D1 |
| U21                | : NJU7313AL     |
| U24,25             | : NJU201AD      |
| U26                | : TC74HC138AP   |
| U27                | : NJM4558D      |
| U29                | : TC74HC00AP    |
| U30                | : TC4094BP      |
|                    |                 |
| D27,30             | : RD13JS        |
| D28,31,32,33       | : 1SS132        |
| D29                | : RD5.6JS       |
|                    |                 |
| Q11                | : P111          |
| Q12                | : P112          |
|                    |                 |
| PH1,2              | : TLP2630       |

# SCHEMATIC DIAGRAM



ACV measurement range IV FAST:  
Input level IV, 25KHz



ACV measurement range IV FAST:  
Input level IV, 25KHz



ACV measurement range IV FAST:  
Input level IV, 25KHz



ACV measurement range IV FAST:  
Input level IV, 25KHz

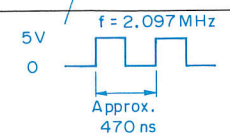
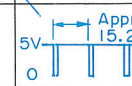
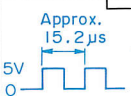
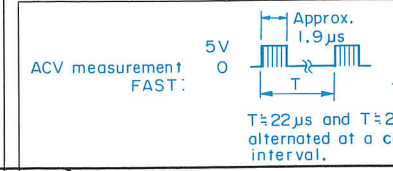
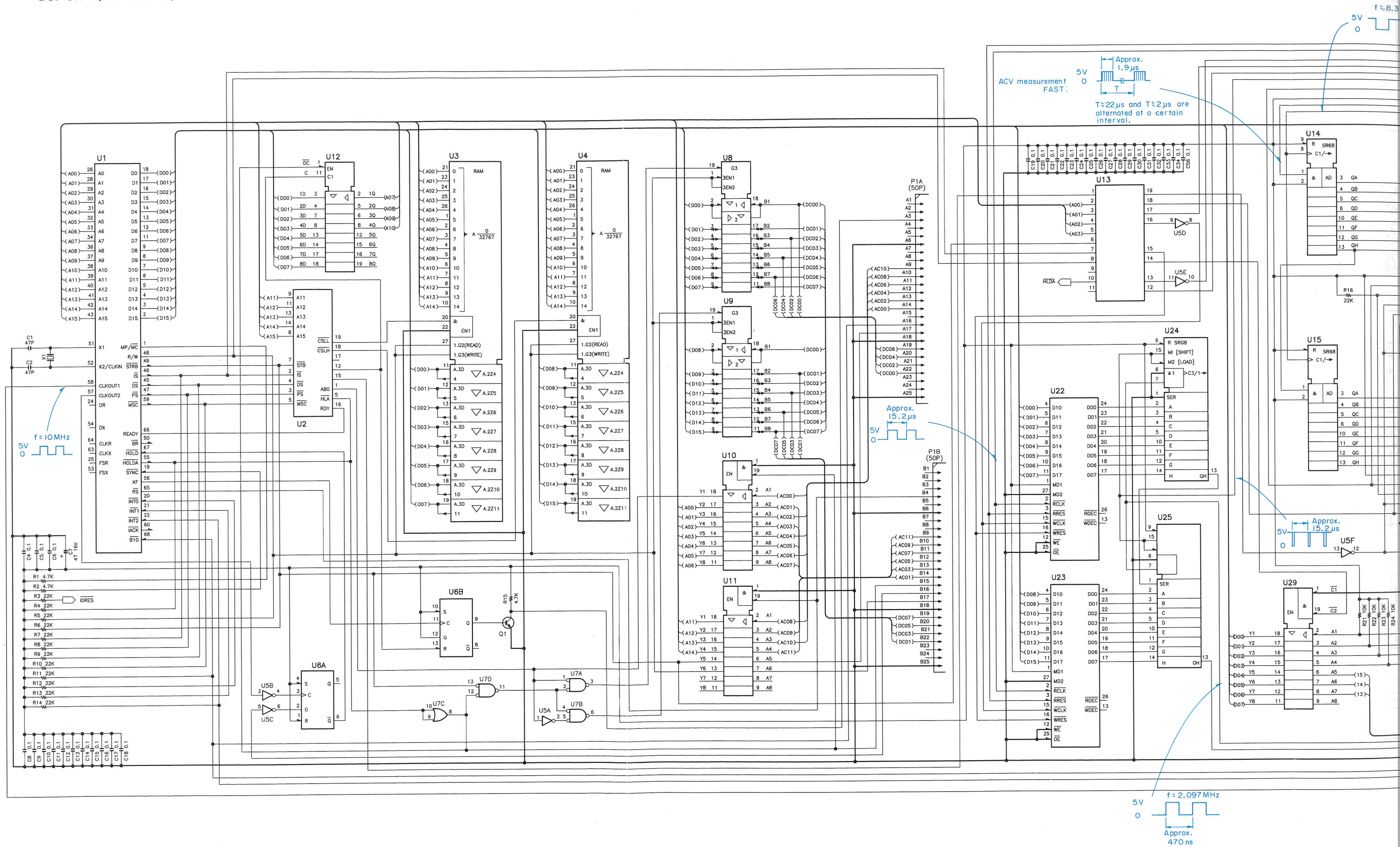


- U2,3 : NJM5534D
- U6,8~11 : TC4094BP
- U7 : TD62801P
- U12 : μPC811C
- U13 : NJU201AD
- U14 : TC74HC04AP
- D1,4,5,8,11~26 : 1SS132
- Q1~3,7~10 : RN1006
- Q4~6 : 2SC3402
- Q13~18 : 2SC1907

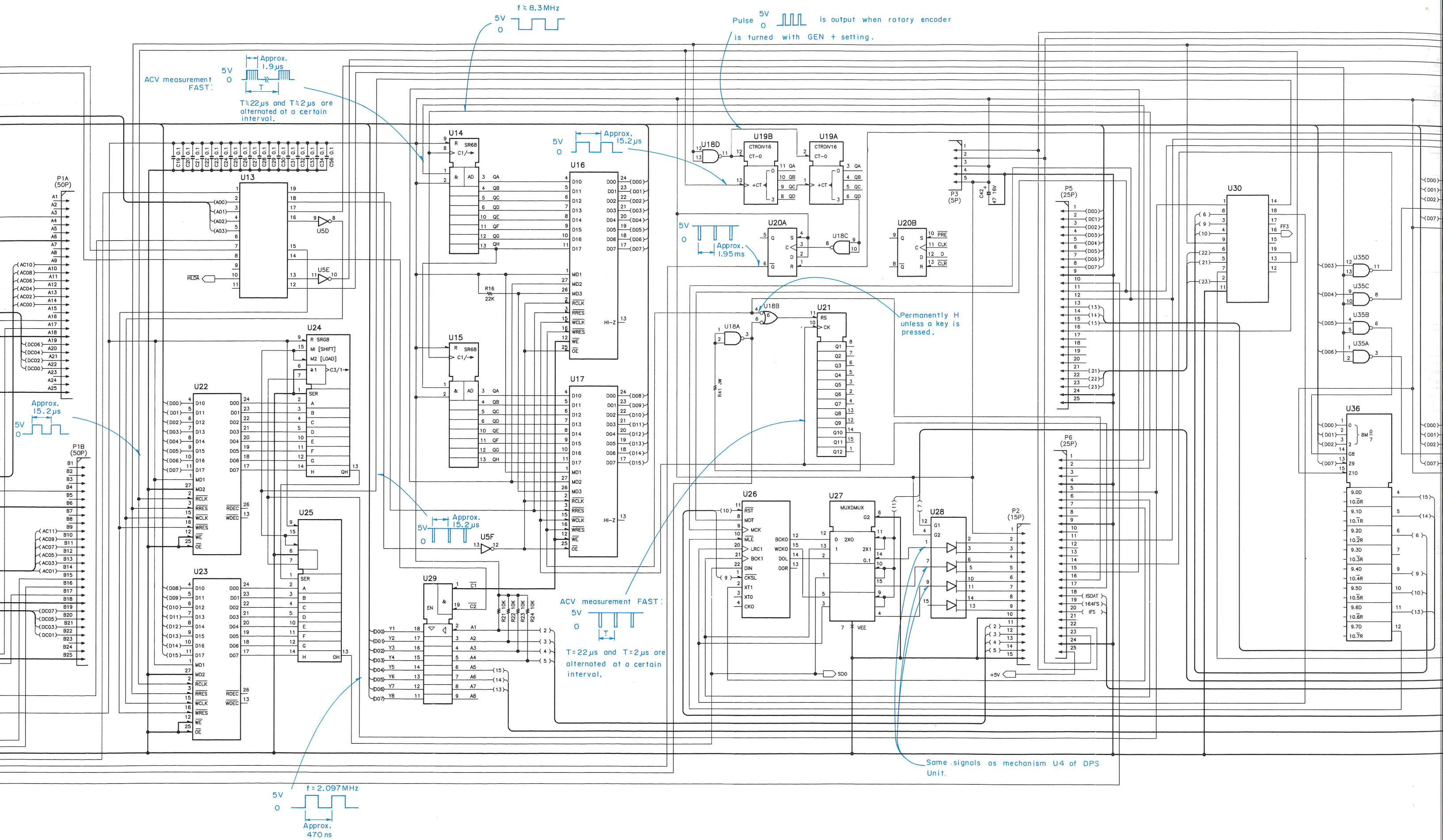


DSP UNIT (X79-1240-00)

SCHEMATIC



# SCHEMATIC DIAGRAM



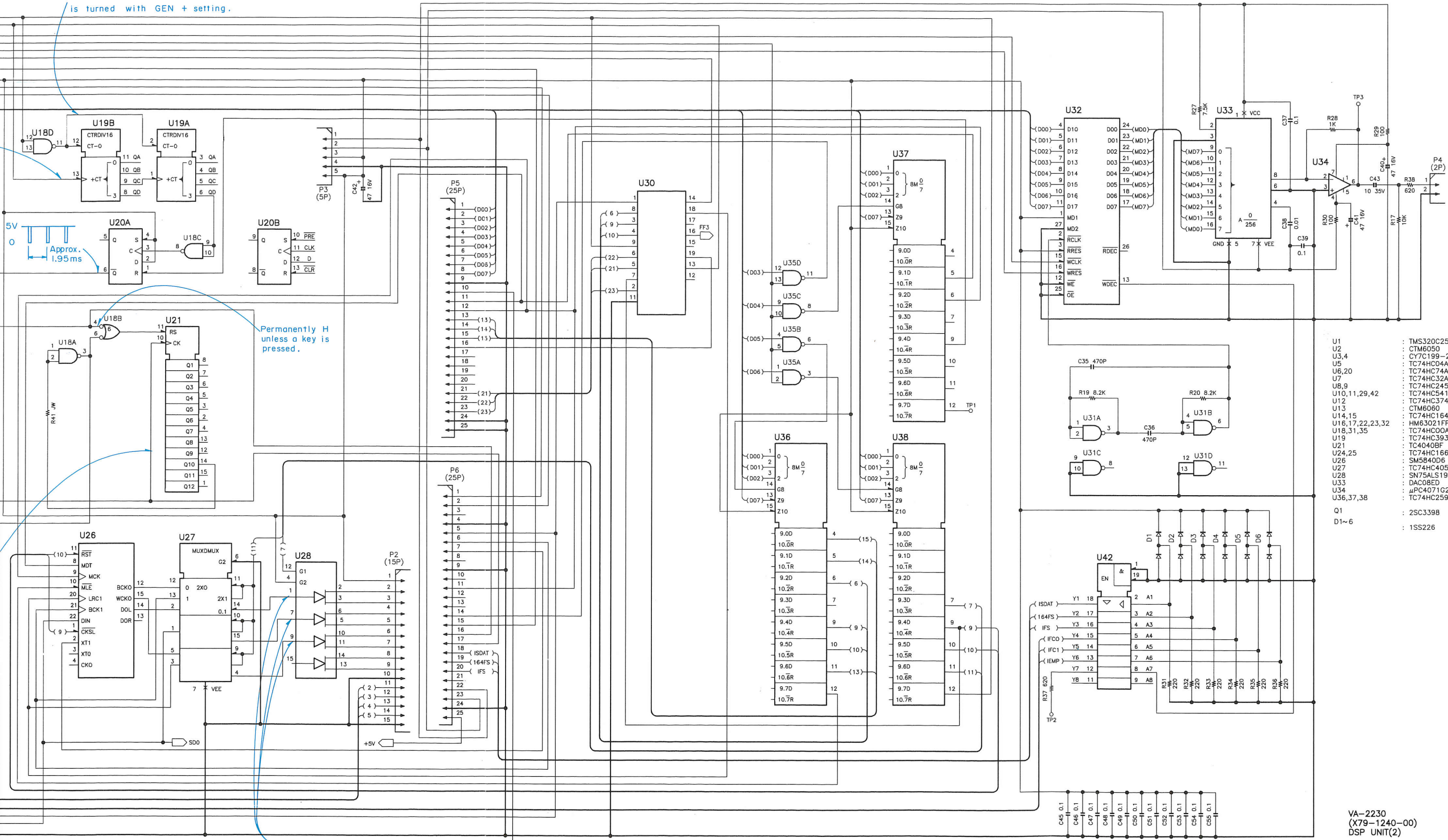
Pulse 5V is output when rotary encoder is turned with GEN + setting.

D00  
D01  
D02  
D03  
D04  
D05  
D06  
D07  
D08  
D09  
D10  
D11  
D12  
D13  
D14  
D15

FAST :  
2µs are certain

Permanently H unless a key is pressed.

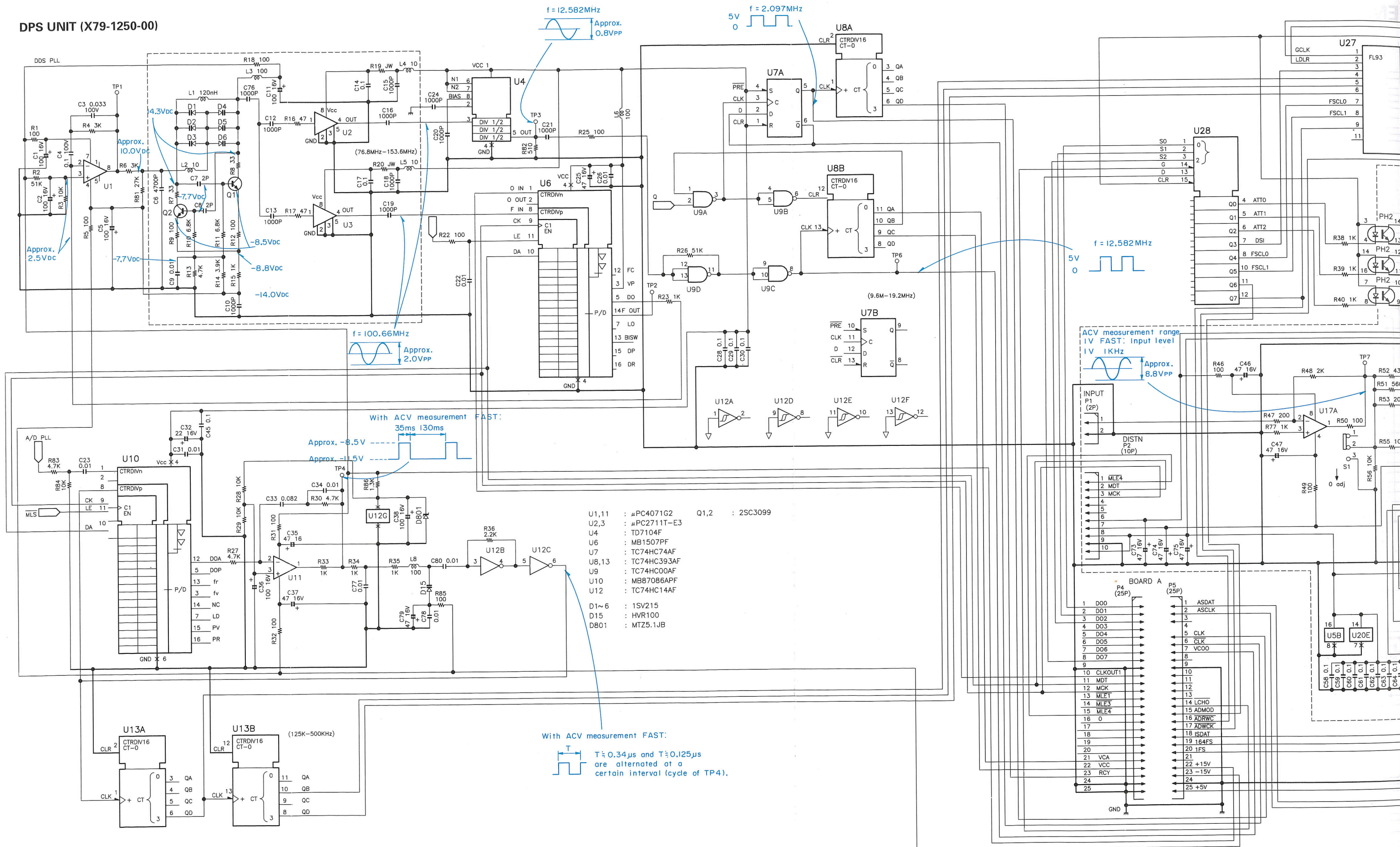
Same signals as mechanism U4 of DPS Unit.



- U1 : TMS320C25FNL
- U2 : CTM6050
- U3,4 : CY7C199-25VC
- U5 : TC74HC04AF
- U6,20 : TC74HC74AF
- U7 : TC74HC32AF
- U8,9 : TC74HC245AF
- U10,11,29,42 : TC74HC541AF
- U12 : TC74HC374AF
- U13 : CTM6060
- U14,15 : TC74HC164AF
- U16,17,22,23,32 : HM63021FF-2B
- U18,31,35 : TC74HC00AF
- U19 : TC74HC393AF
- U21 : TC4040BF
- U24,25 : TC74HC166AF
- U26 : SM5840D6
- U27 : TC74HC4053AF
- U28 : SN75ALS192NS
- U33 : DAC08ED
- U34 : µPC4071G2
- U36,37,38 : TC74HC259AF
- Q1 : 2SC3398
- D1~6 : 1SS226

VA-2230  
(X79-1240-00)  
DSP UNIT(2)

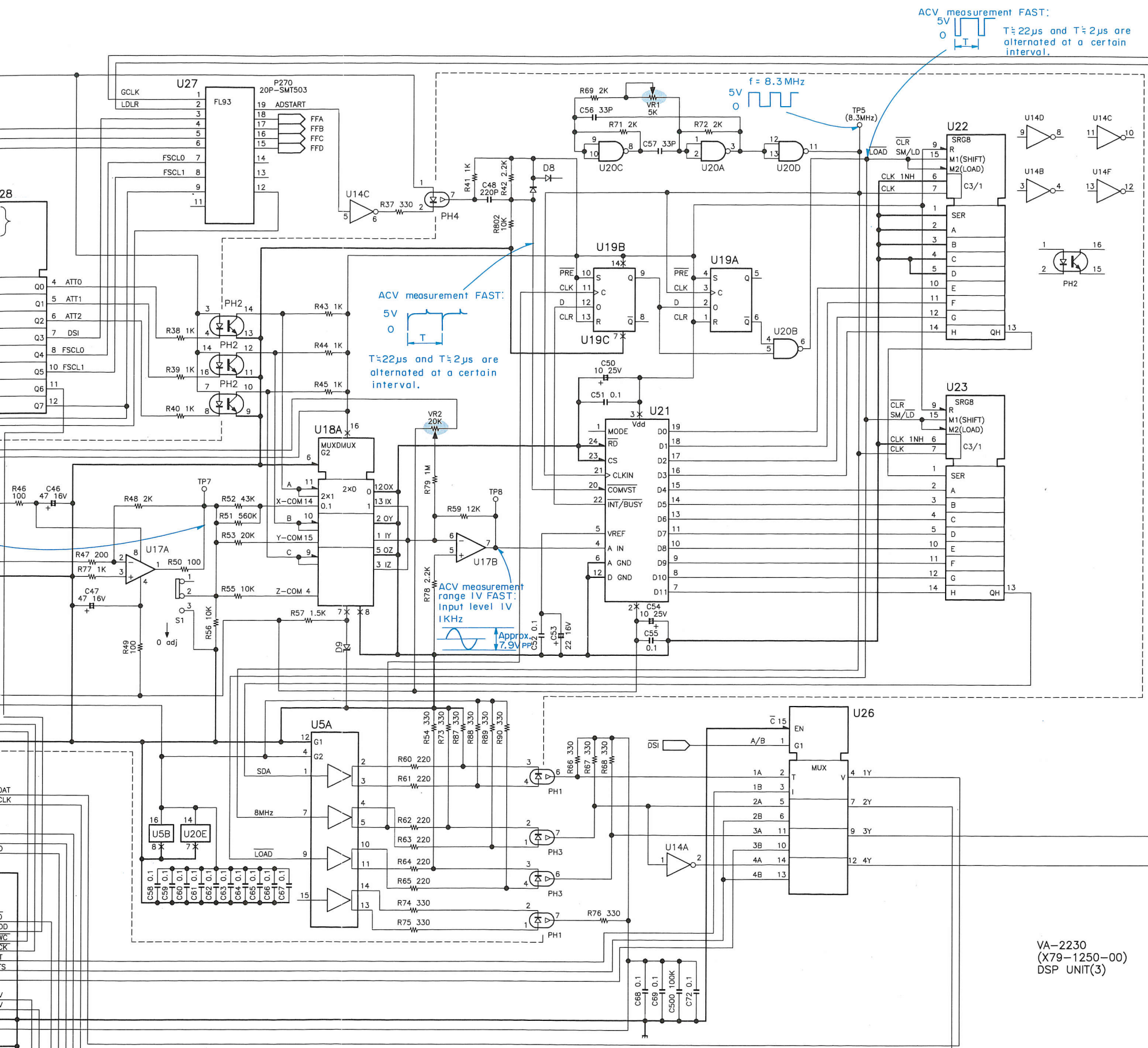
DPS UNIT (X79-1250-00)



- U1,11 :  $\mu\text{PC4071G2}$
- U2,3 :  $\mu\text{PC2711T-E3}$
- U4 : TD7104F
- U6 : MB1507PF
- U7 : TC74HC74AF
- U8,13 : TC74HC393AF
- U9 : TC74HC00AF
- U10 : MBB7086APF
- U12 : TC74HC14AF
- D1~6 : 1SV215
- D15 : HVR100
- D801 : MTZ5.1JB
- Q1,2 : 2SC3099

With ACV measurement FAST:  
 $T \approx 0.34\mu\text{s}$  and  $T \approx 0.125\mu\text{s}$   
 are alternated at a  
 certain interval (cycle of TP4).

# SCHEMATIC DIAGRAM



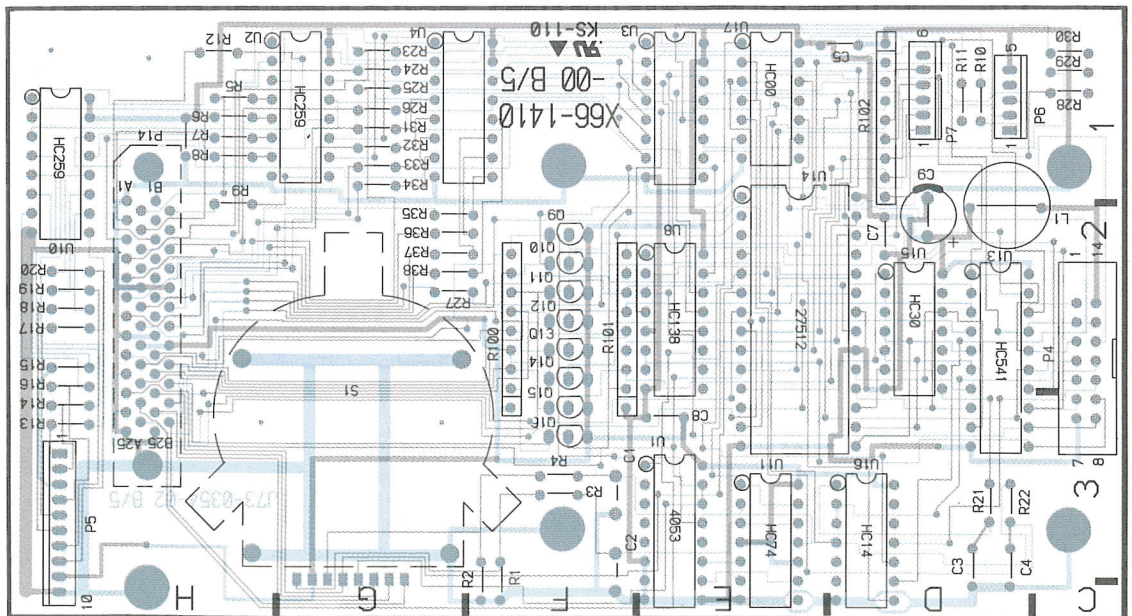
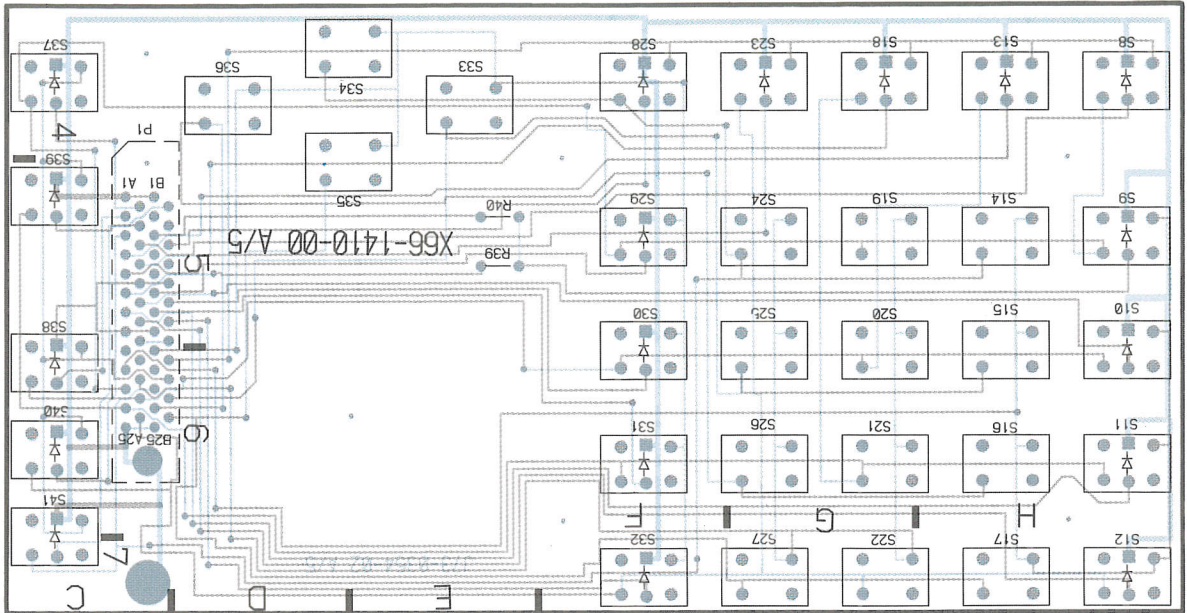
- U5 : SN75ALS192NS
- U14 : TC74HC04AF
- U17 :  $\mu\text{PC4072G2}$
- U18 : TC4053BF
- U19 : TC74HC74AF
- U20 : TC74HC00AF
- U21 : MAX120CWG
- U22,23 : TC74HC166AF
- U26 : TC74HC257AF
- U27 : CTM6080
- U28 : TC74HC259AF
- D8 : 1SS226
- D9 : RD5.1M
- PH1,3,4 : TLP2630
- PH2 : TLP521-4

VA-2230  
(X79-1250-00)  
DSP UNIT(3)

# P. C. BOARD

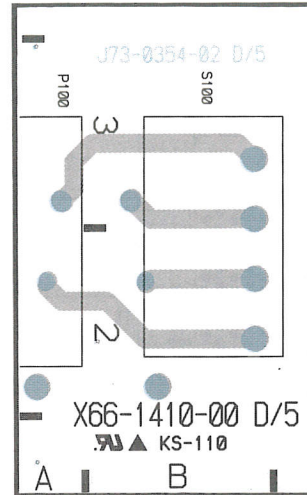
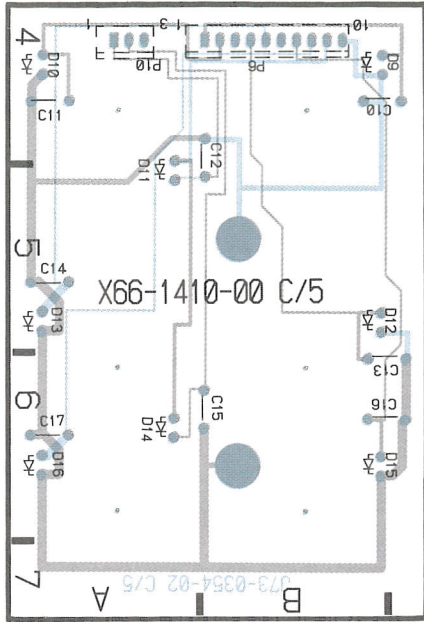
PANEL UNIT (X66-1410-00)

Pattern side view

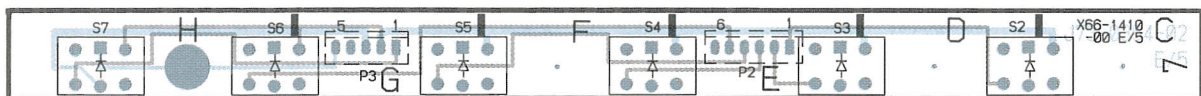


# P. C. BOARD

Pattern side view

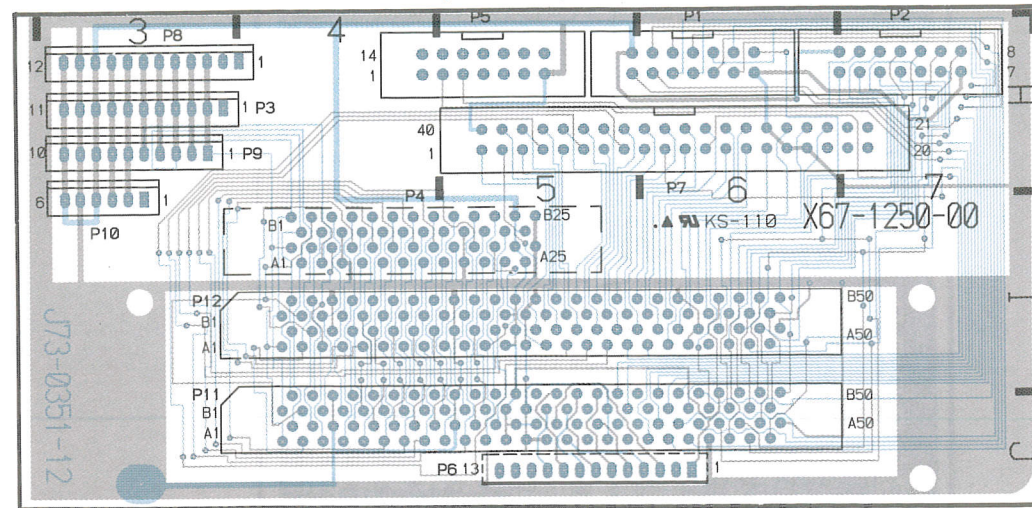


Pattern side view

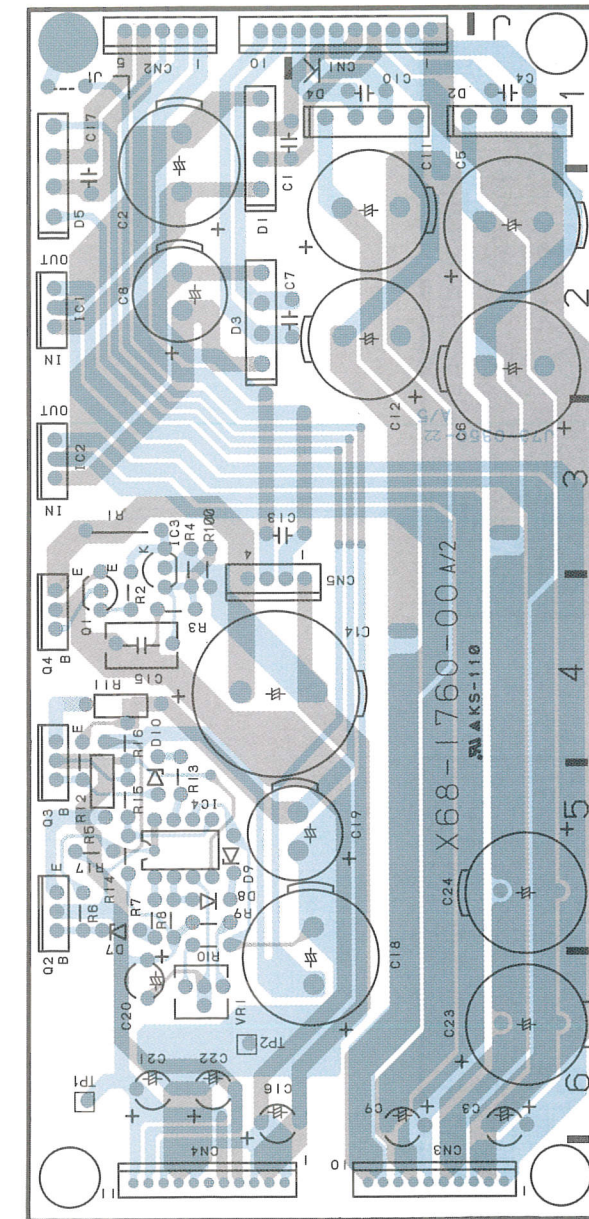


# P. C. BOARD

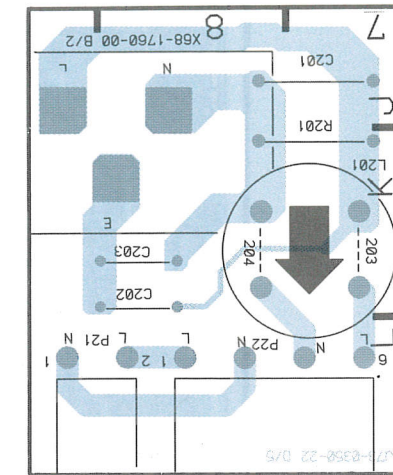
CONNECTION UNIT (X67-1250-00)



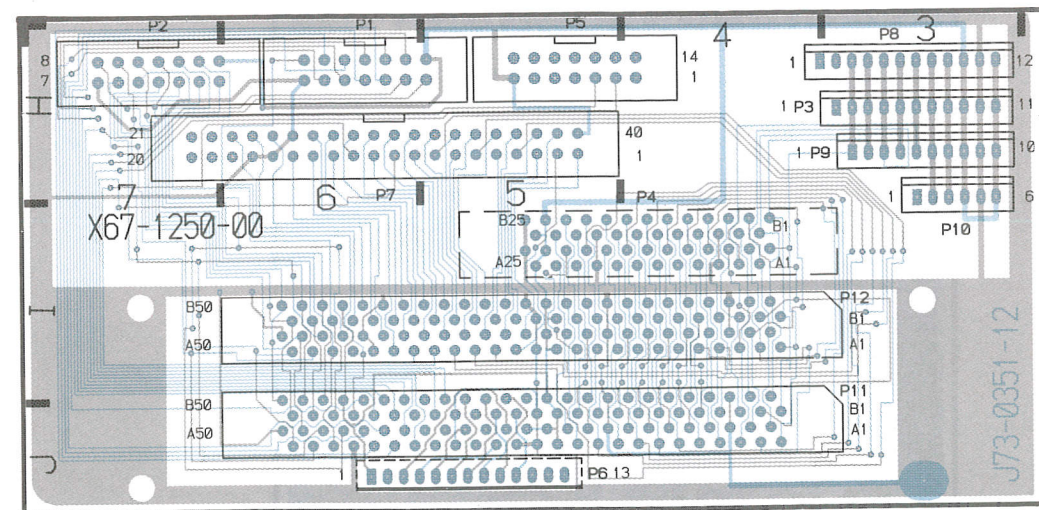
POWER UNIT (X68-1760-00)



Pattern side view



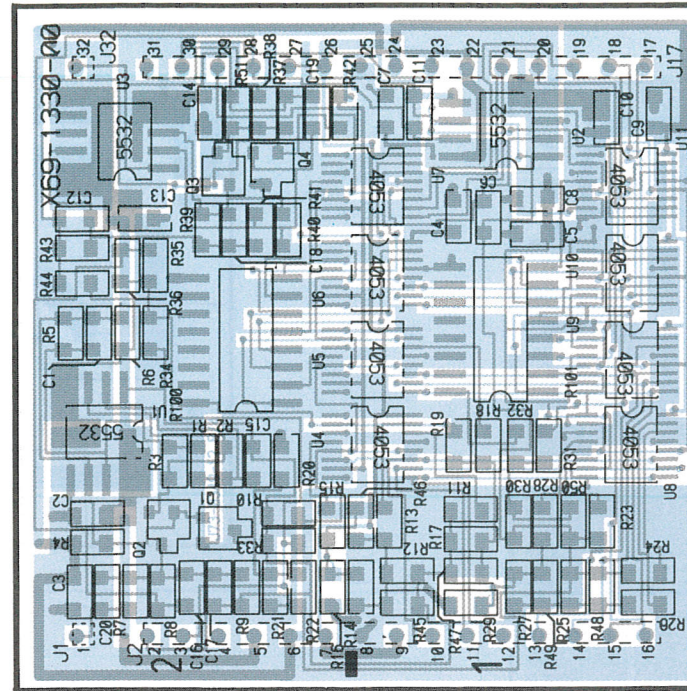
Pattern side view



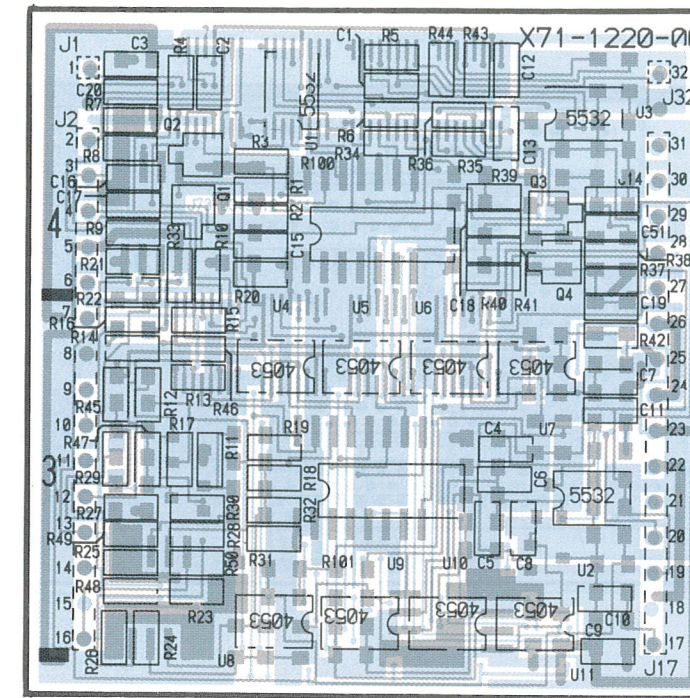


# P. C. BOARD

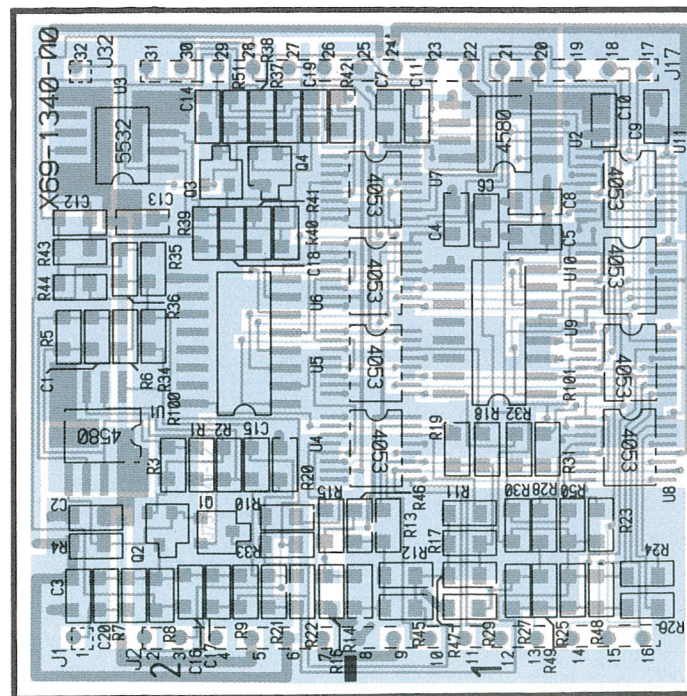
FILTER UNIT (X69-1330-00)



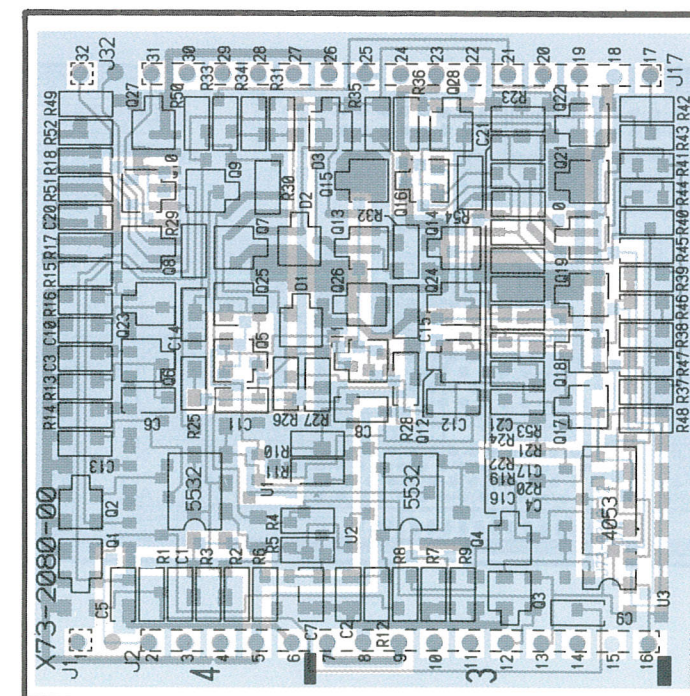
OSCILLATOR UNIT (X71-1220-00)



FILTER UNIT (X69-1340-00)

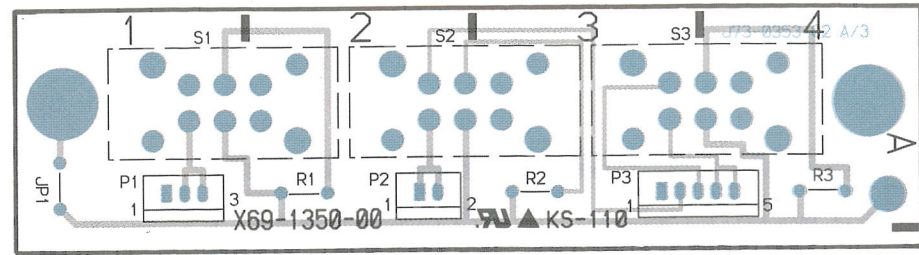


PRE AMP UNIT (X73-2080-00)

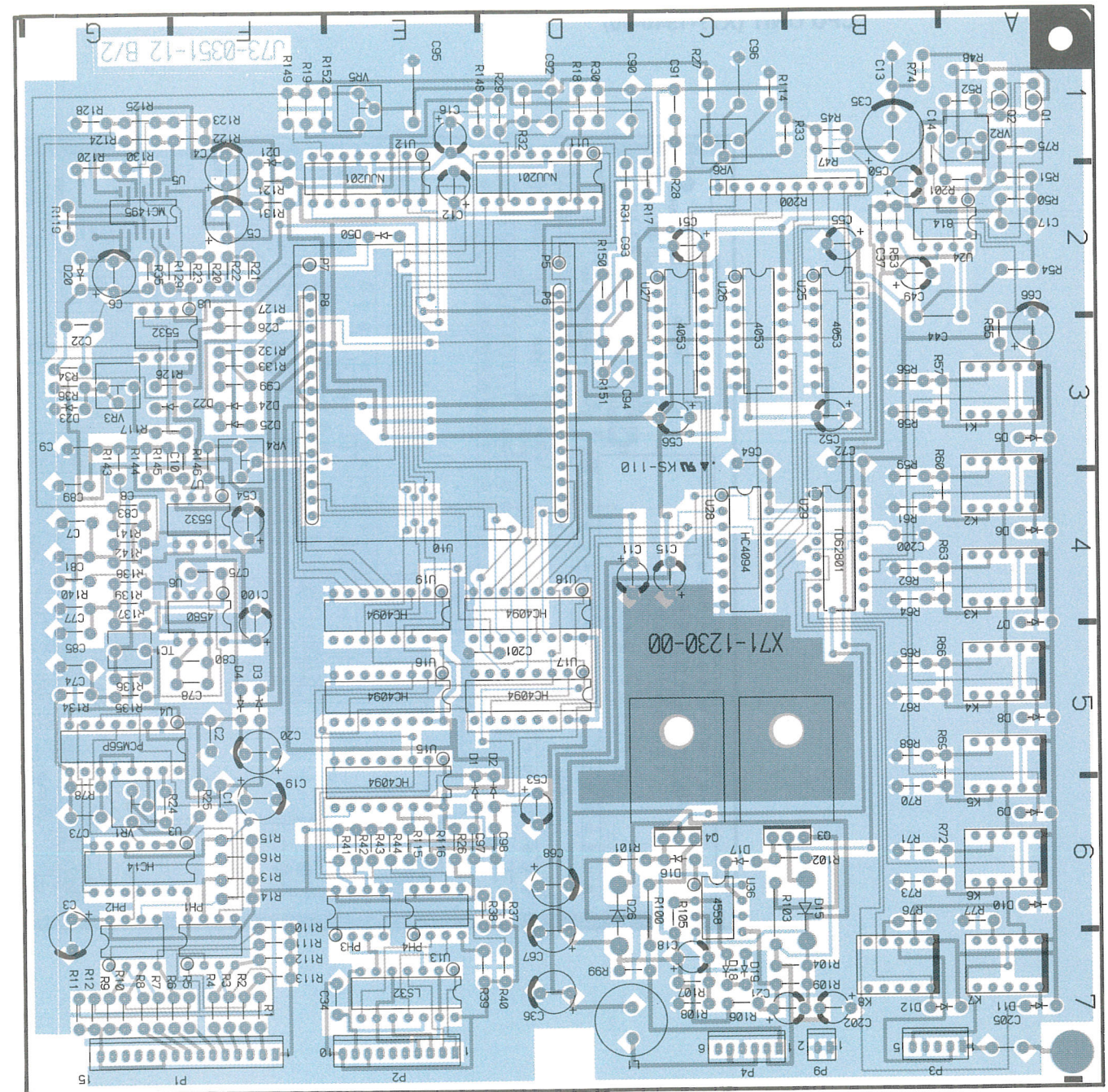


# P. C. BOARD

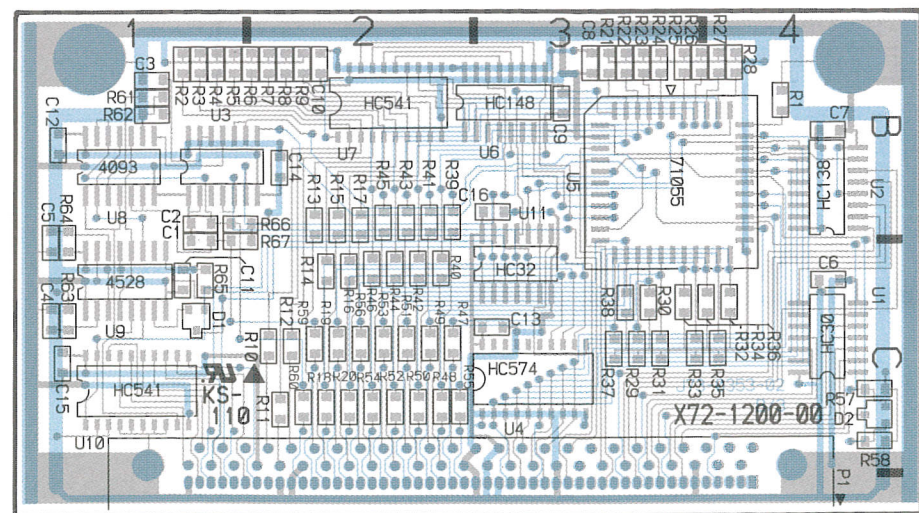
SWITCH UNIT (X69-1350-00)



OSCILLATOR UNIT (X71-1230-00)

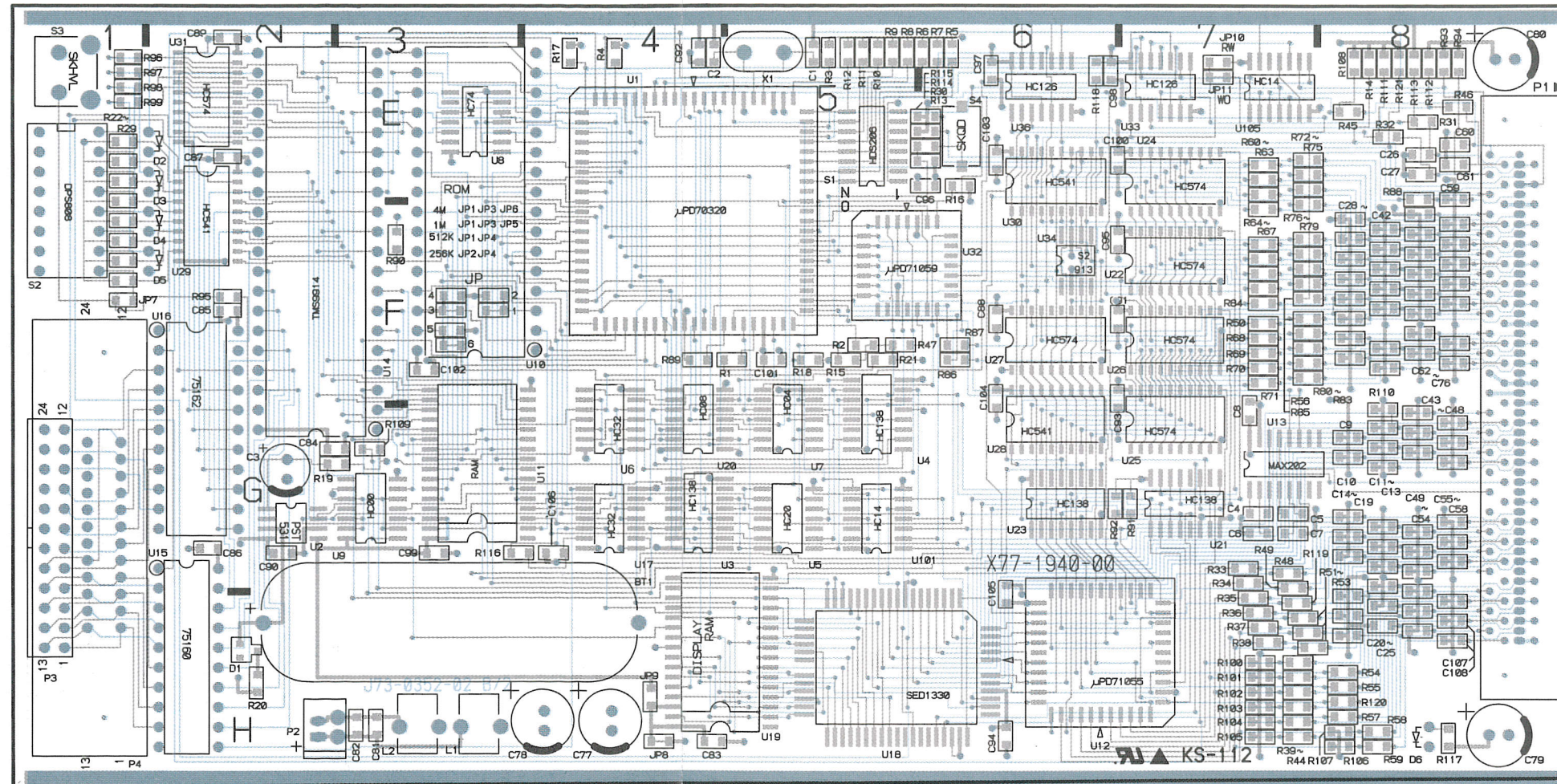


I/O UNIT (X72-1200-00)



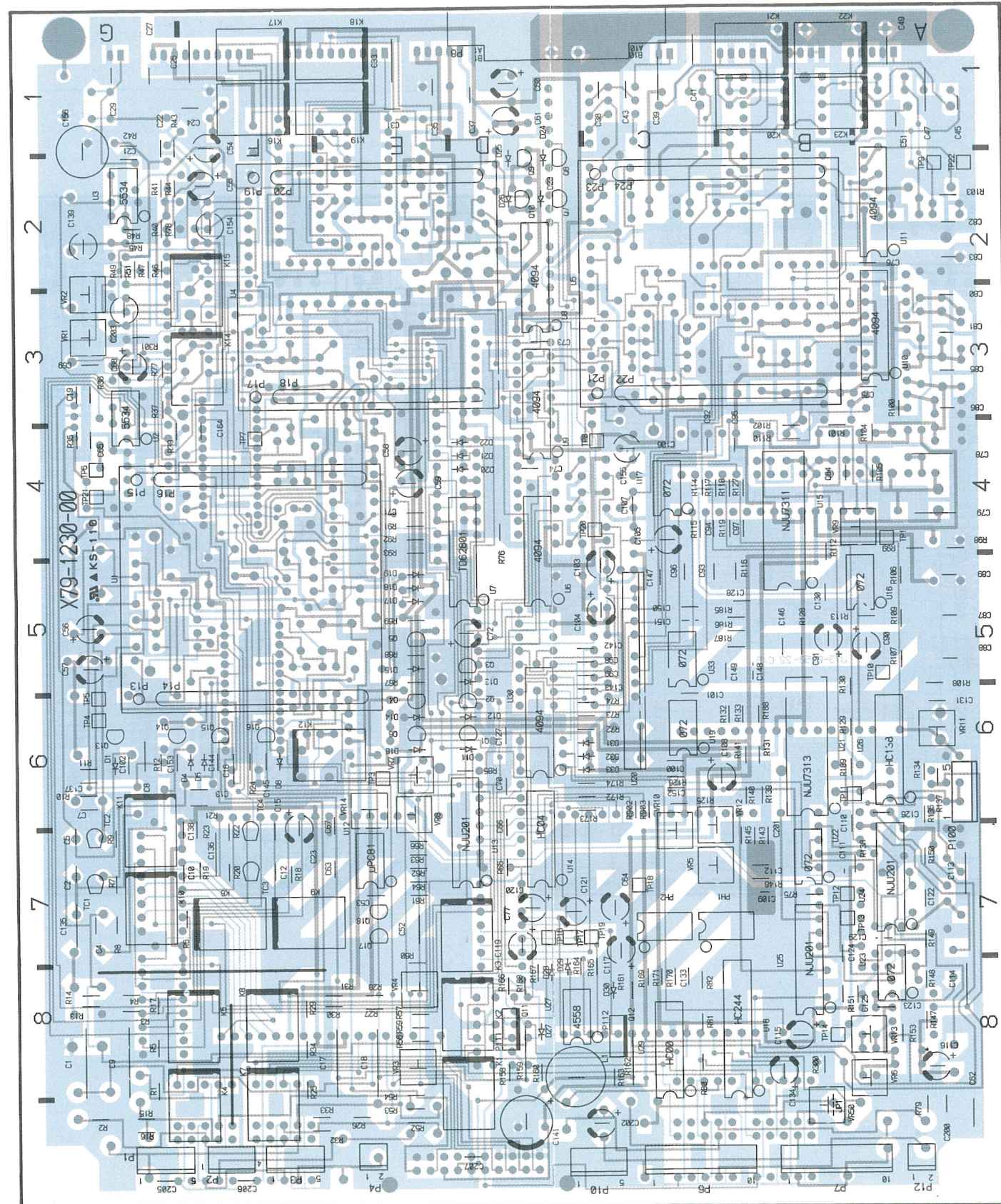
# P. C. BOARD

CPU UNIT (X77-1940-00)



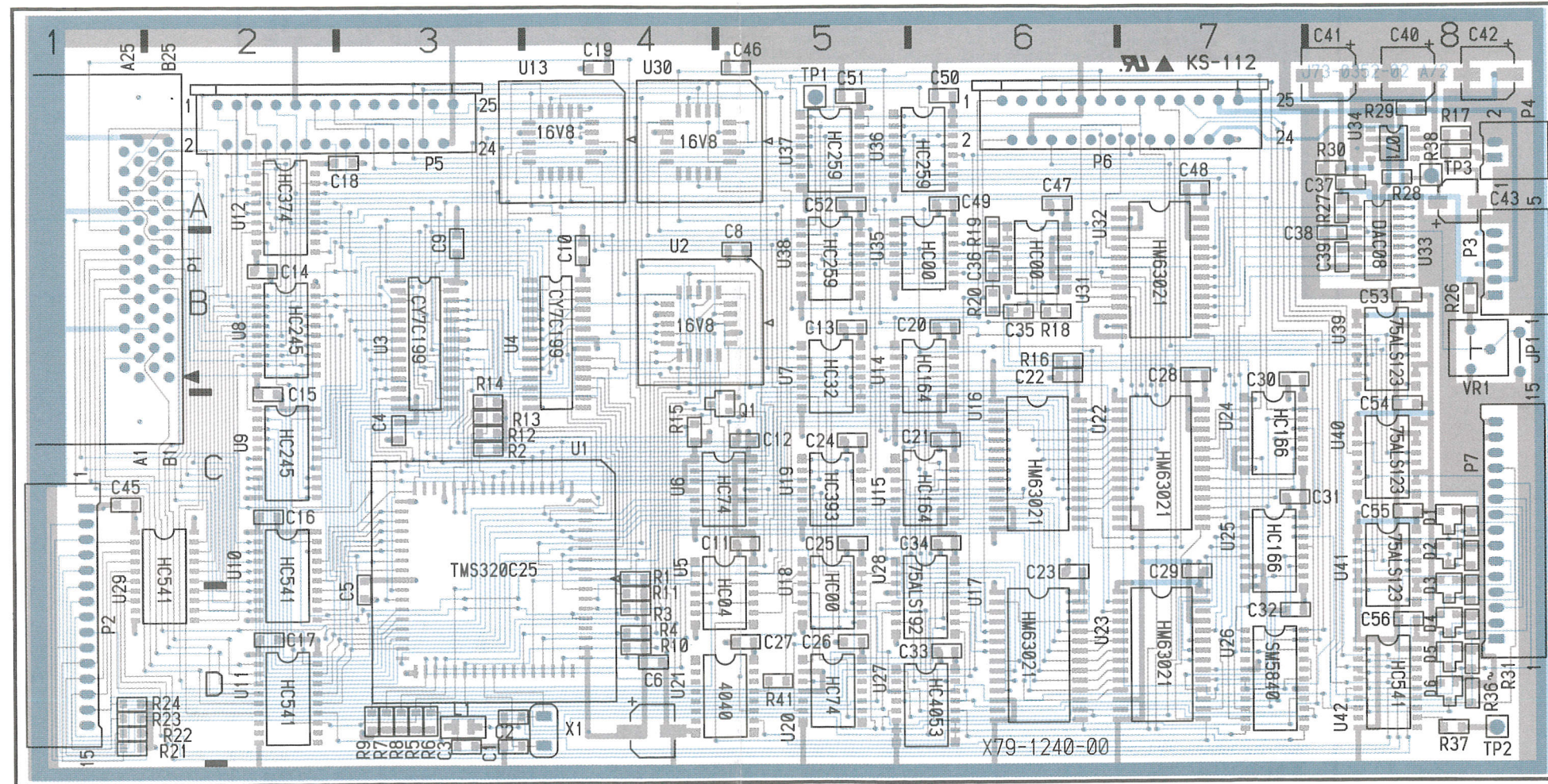
# P. C. BOARD

DSP UNIT (X79-1230-00)



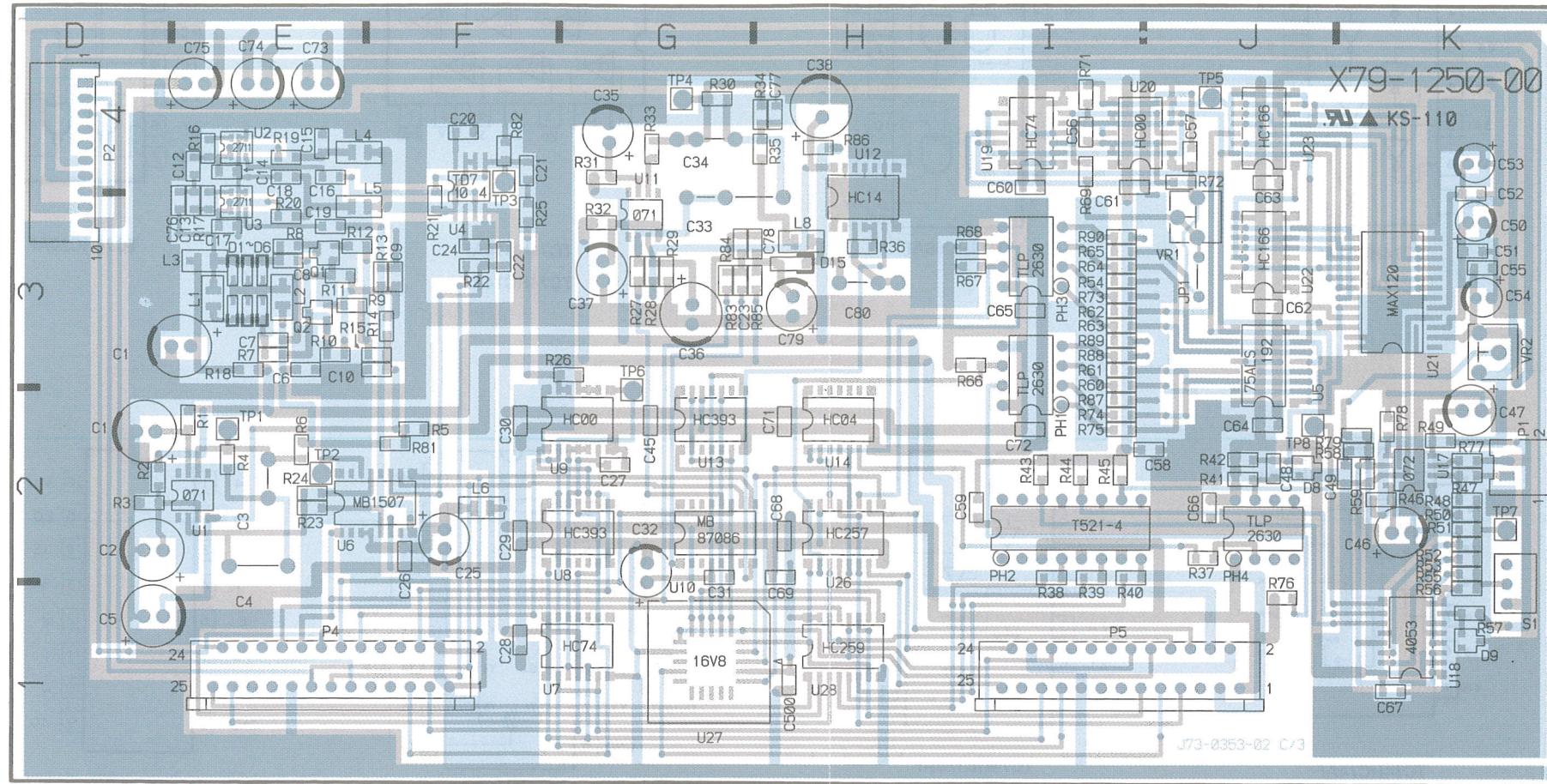
# P. C. BOARD

DSP UNIT (X79-1240-00)

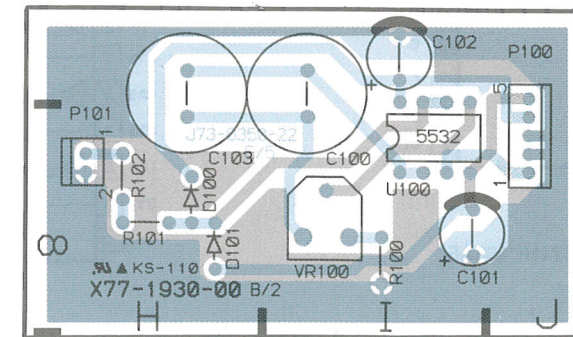
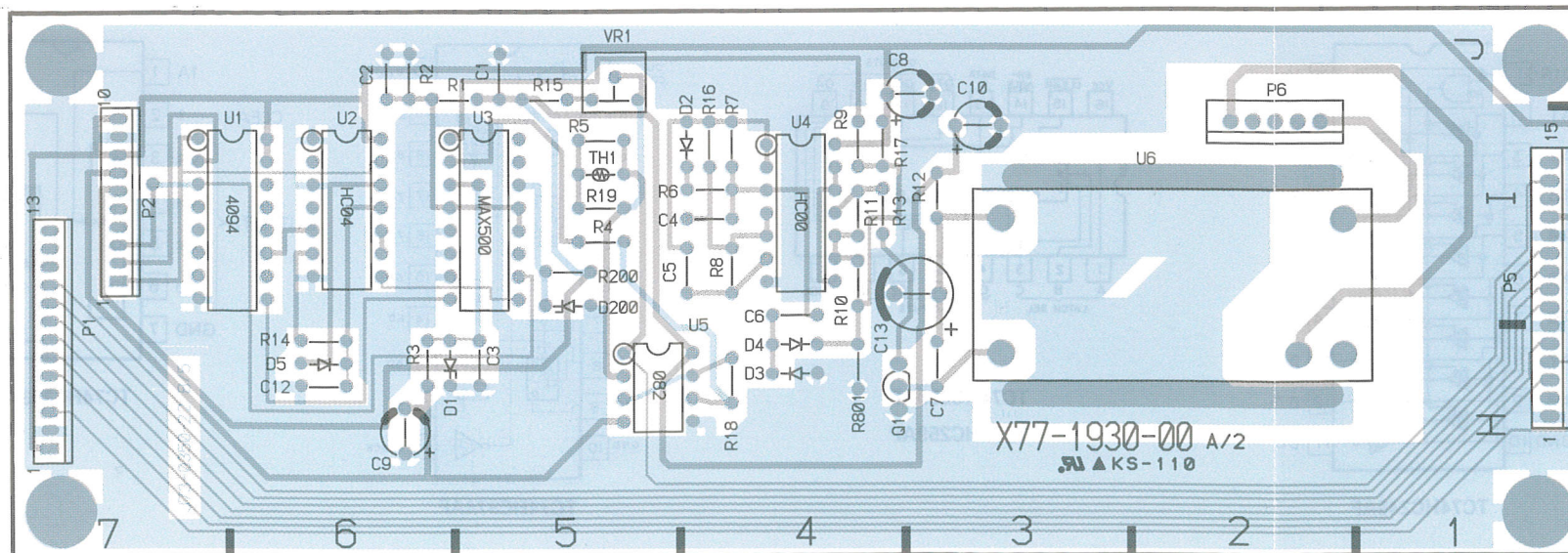


# P. C. BOARD

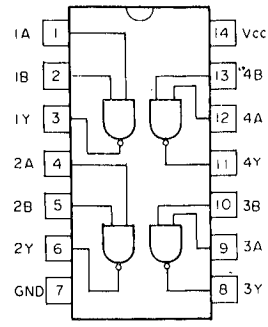
DSP UNIT (X79-1250-00)



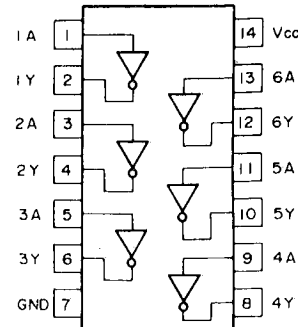
CONTROL UNIT (X77-1930-00)



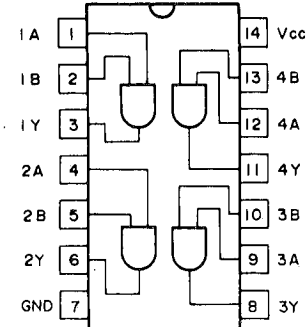
# SEMICONDUCTORS



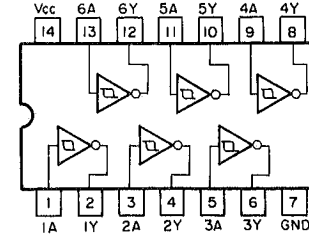
TC74HC00AF  
TC74HC00AP



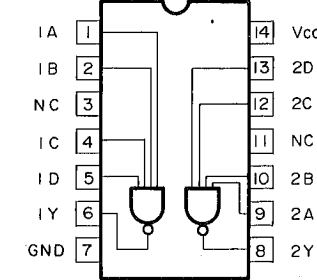
TC74HC04AF  
TC74HC04AP



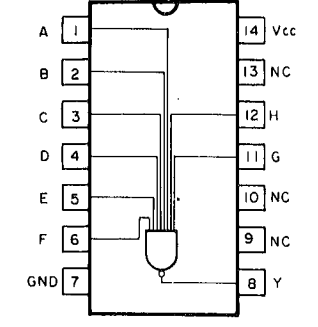
TC74HC08AF



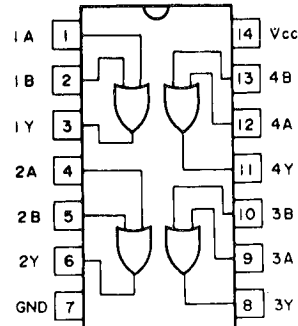
TC74HC14AF  
TC74HC14AP



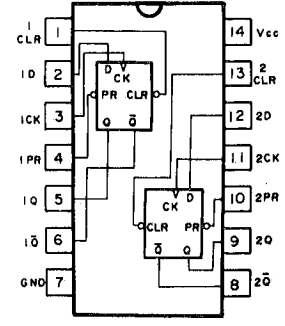
TC74HC20AF



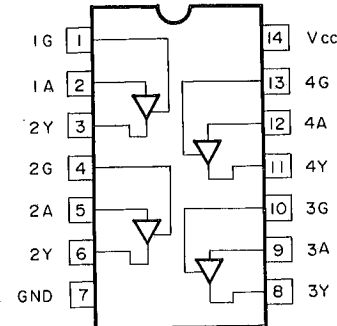
TC74HC30AF  
TC74HC30AP



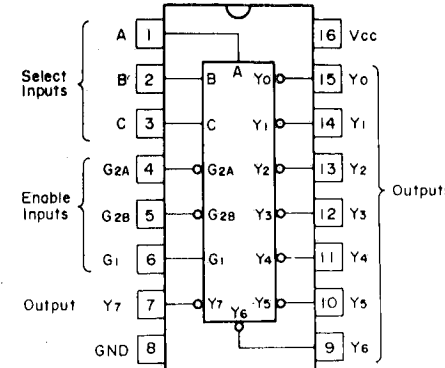
TC74HC32AF  
SN74LS32N



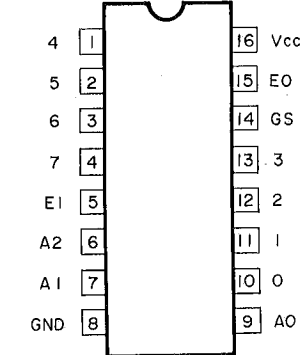
TC74HC74AF  
TC74HC74AP



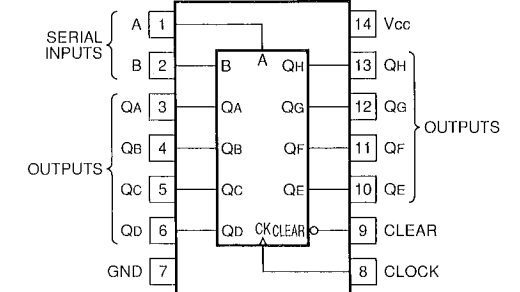
TC74HC126AF



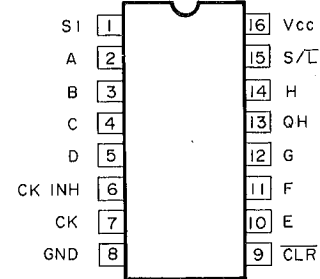
TC74HC138AF  
TC74HC138AP



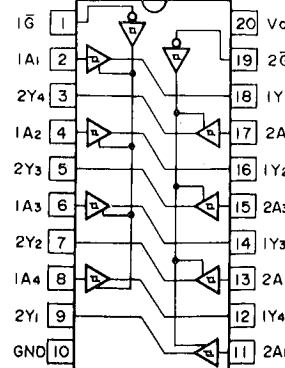
TC74HC148AF



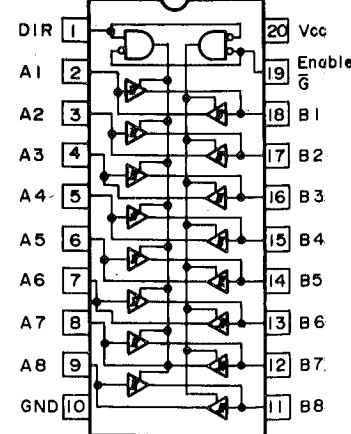
TC74HC164AF



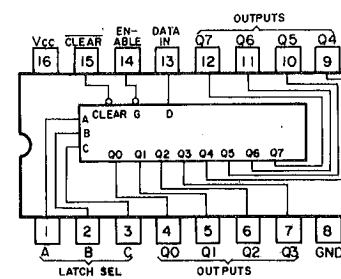
TC74HC166AF



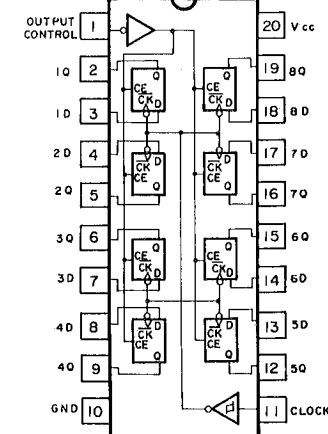
TC74HC244AF



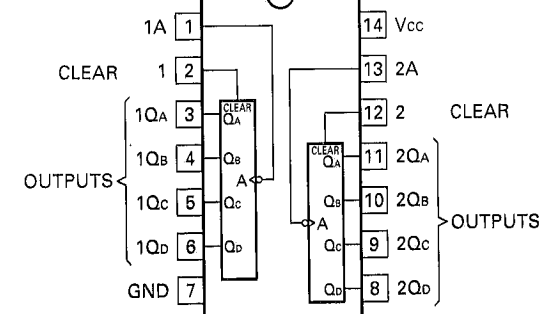
TC74HC245AF



TC74HC259AF  
TC74HC259AP

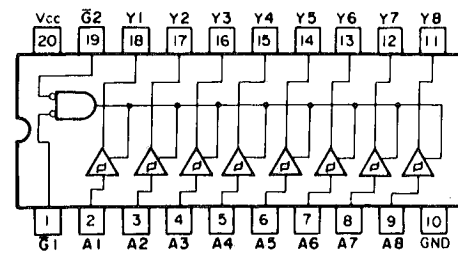


TC74HC374AF

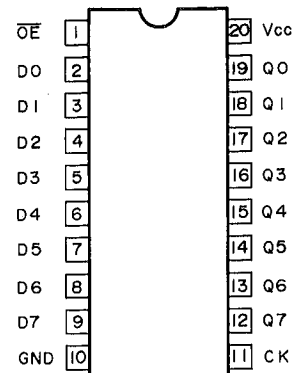


TC74HC393AF

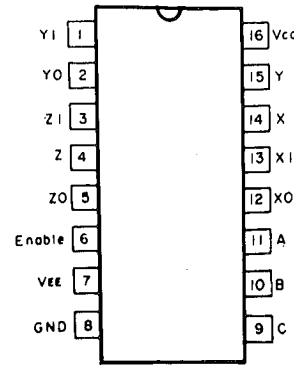
# SEMICONDUCTORS



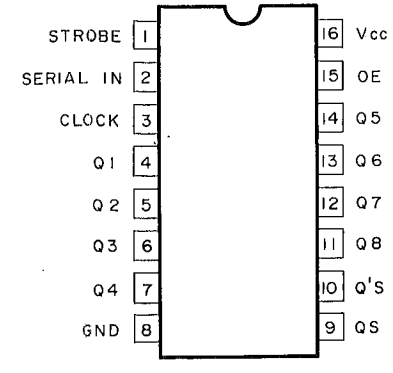
TC74HC541AF  
TC74HC541AP



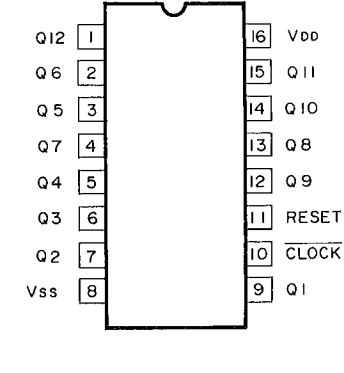
TC74HC574AF



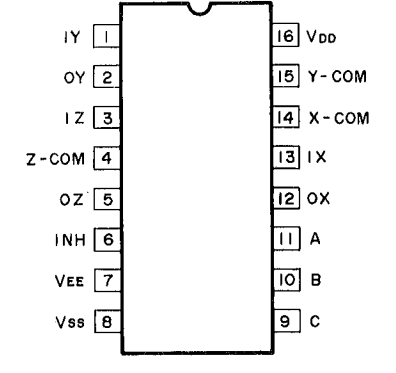
TC74HC4053AF



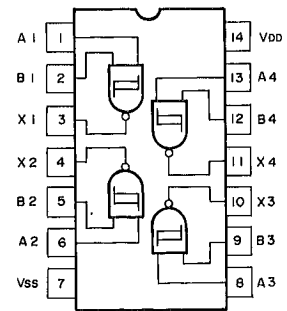
TC74HC4094AP



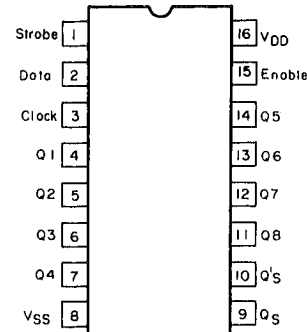
TC4040BF



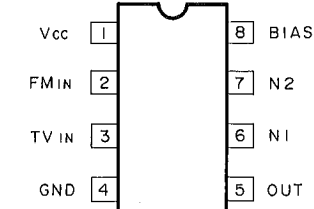
TC4053BF  
TC4053BFS  
TC4053BP



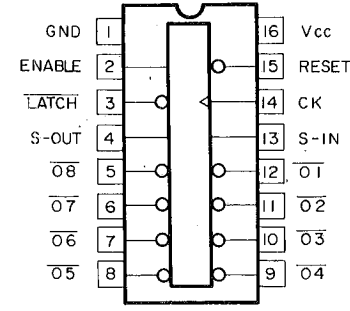
TC4093BF



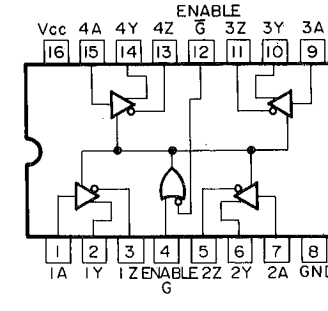
TC4094BP



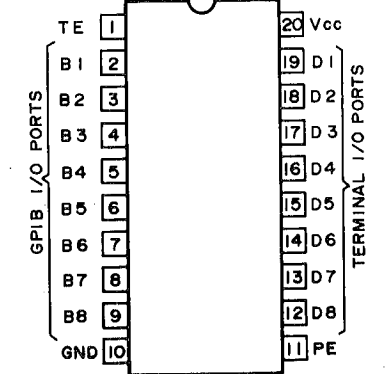
TD7104F



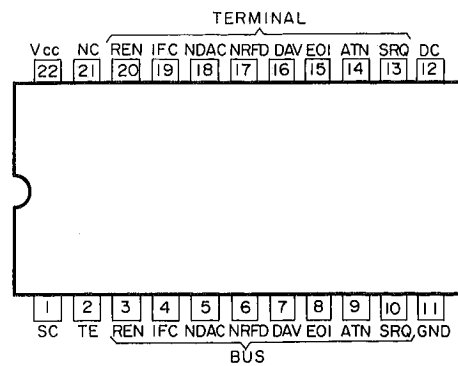
TD62801P



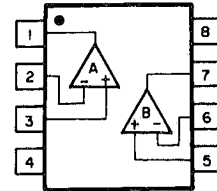
SN75ALS192NS



SN75160BN

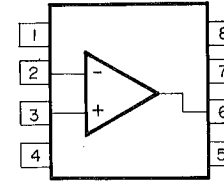


SN75162BN



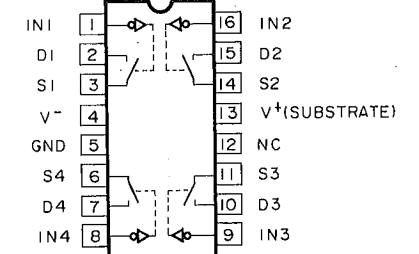
- Pin name
1. A OUTPUT
  2. A (-) INPUT
  3. A (+) INPUT
  4. V-
  5. B (+) INPUT
  6. B (-) INPUT
  7. B OUTPUT
  8. V+

- NJM072BD  
NJM082BD  
NJM4558D  
NJM4580D  
NJM4580E  
NJM5532D  
NJM5532DD  
NJM5532M



NJM5534D

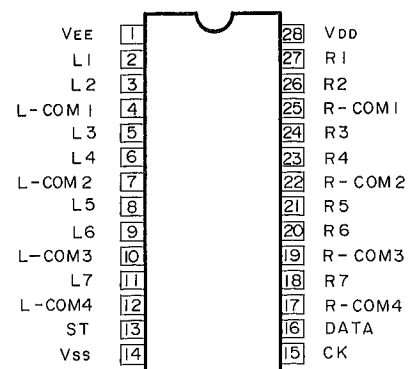
1. V<sub>10</sub> Trim
2. - INPUT
3. + INPUT
4. V-
5. COMPENSATION
6. OUTPUT
7. V+
8. V<sub>10</sub> Trim/COMPENSATION



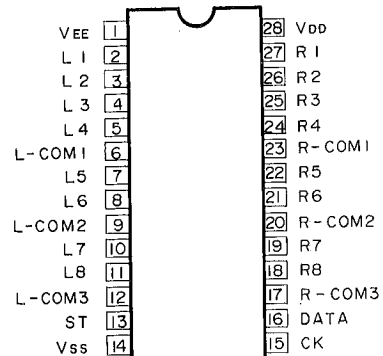
NJMU201AD



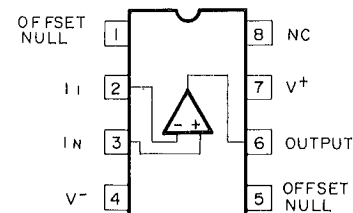
# SEMICONDUCTORS



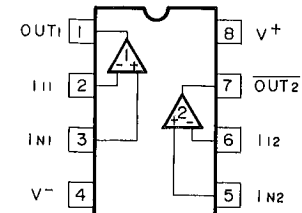
NJU7311AL



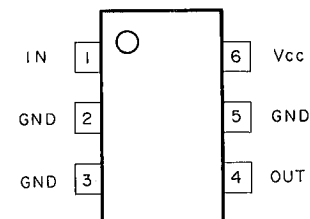
NJU7313AL



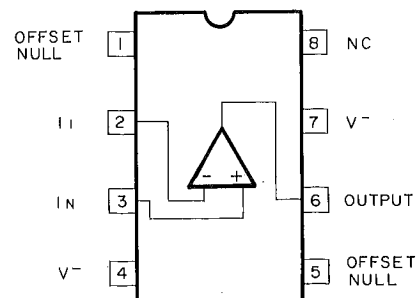
μPC811C



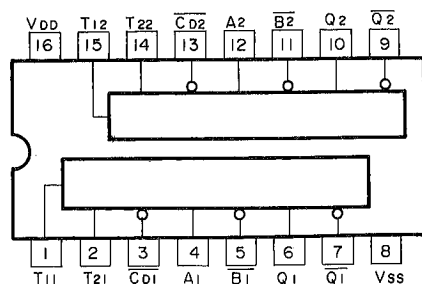
μPC814C



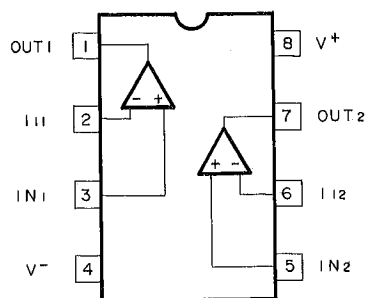
μPC2711T-E3



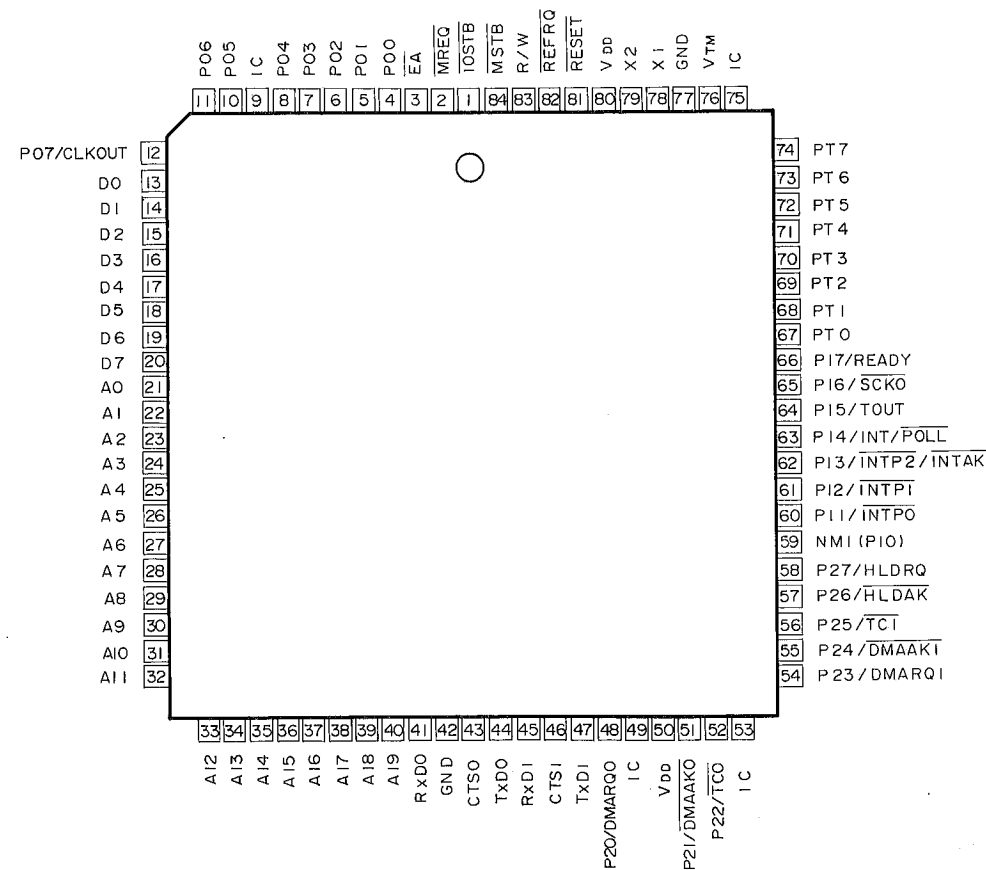
μPC4071G2



μPB4528BG

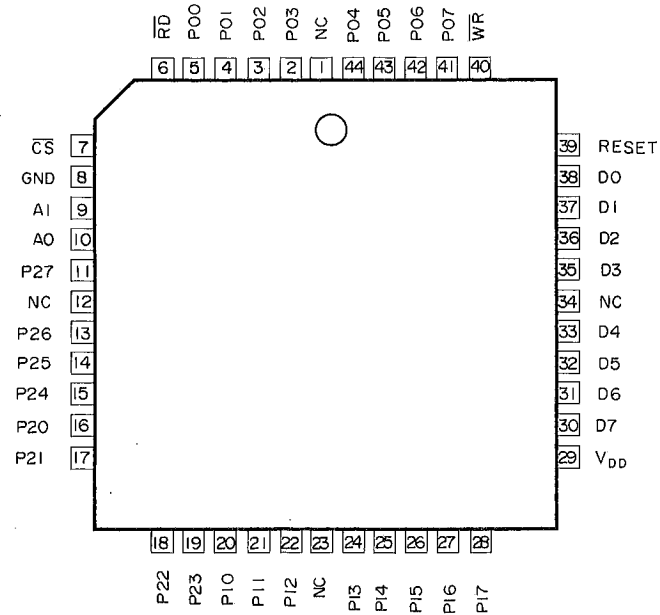


μPC4072G2

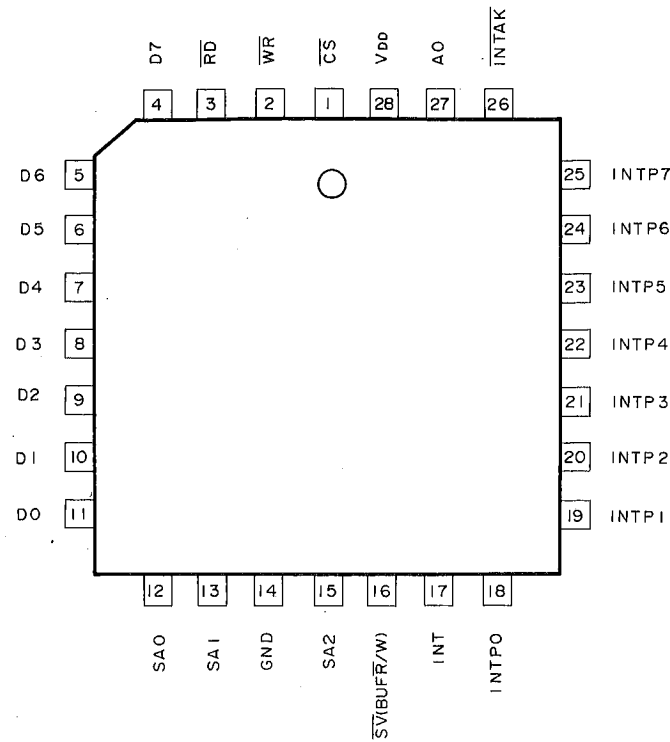


μPD70320L-8

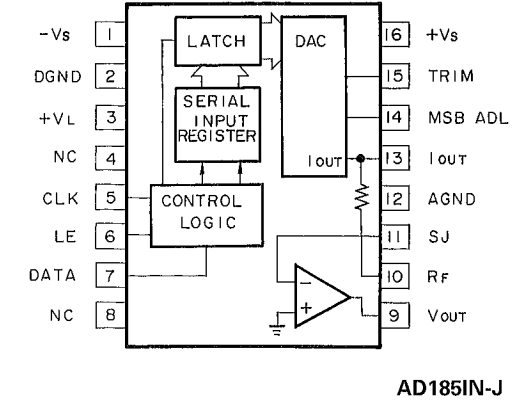
# SEMICONDUCTORS



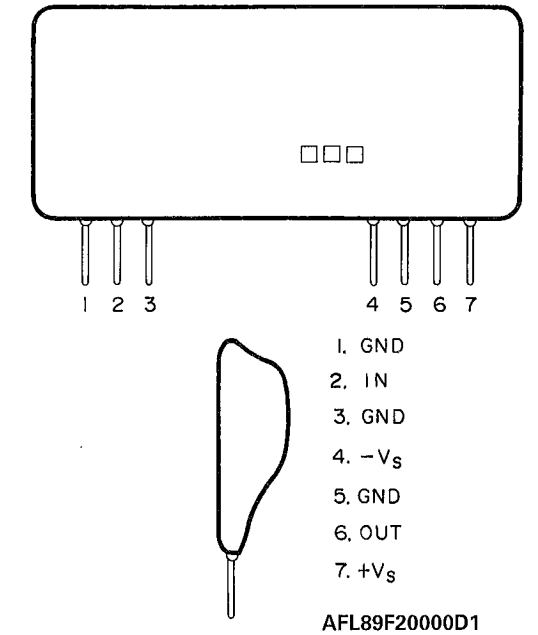
$\mu$ PD71055L



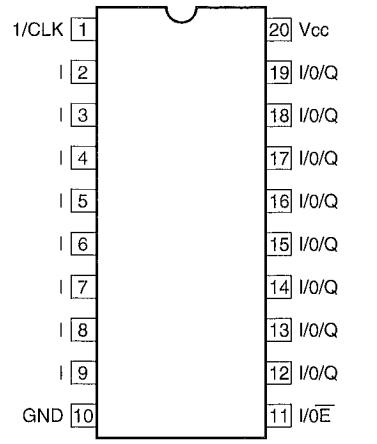
$\mu$ PD71059L-10



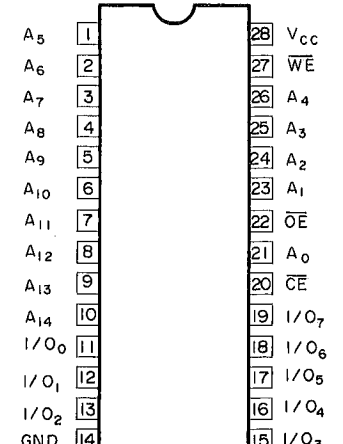
AD185IN-J



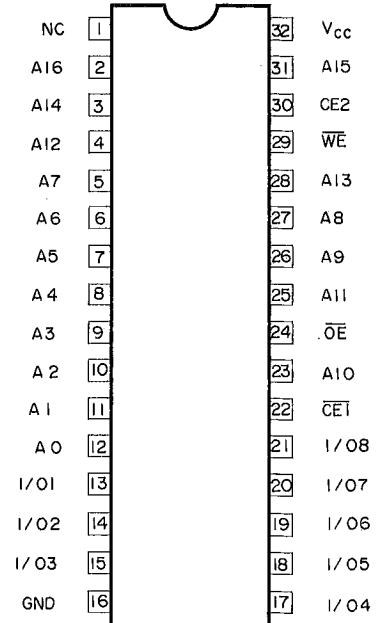
AFL89F20000D1



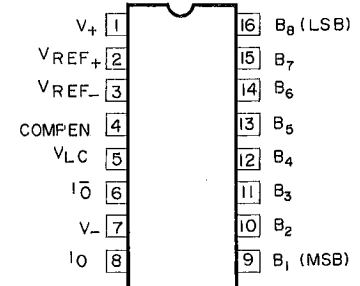
CTM6050  
CTM6060  
CTM6070  
CTM6080



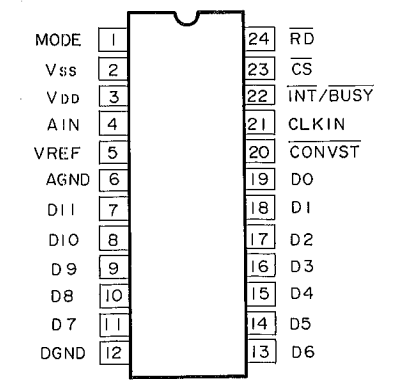
CY7C199-25VC



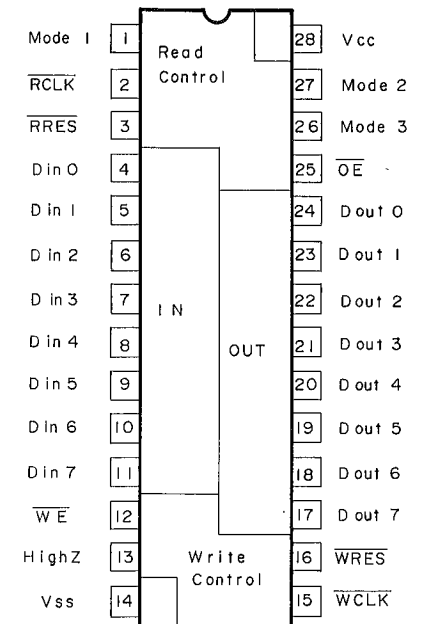
D431000AGW10L



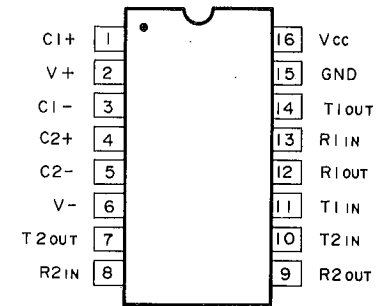
DAC08ED



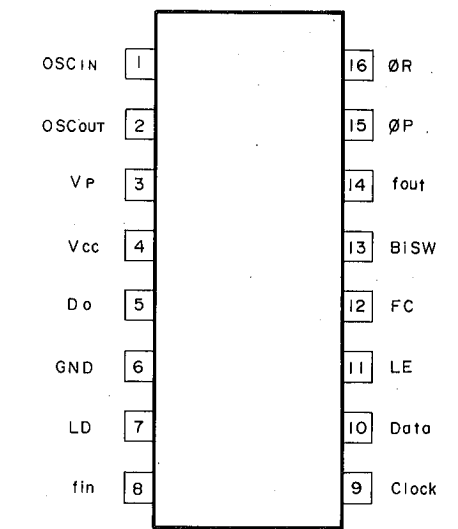
MAX120CWG



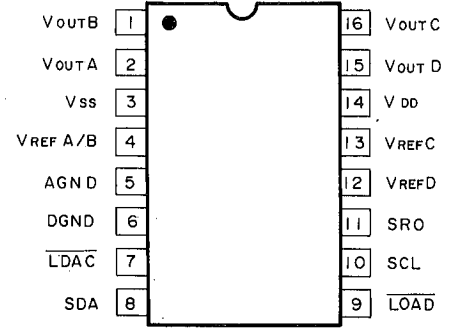
HM63021FP-28



MAX500BCPE

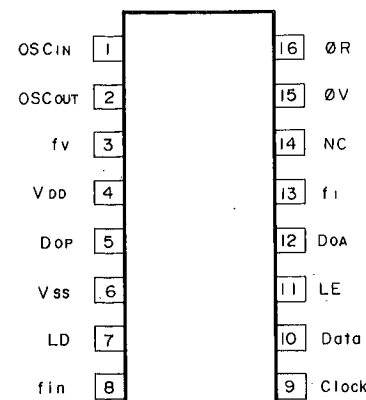


MB1507PF

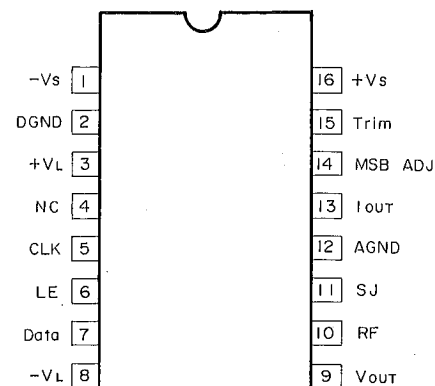


MAX202CSE

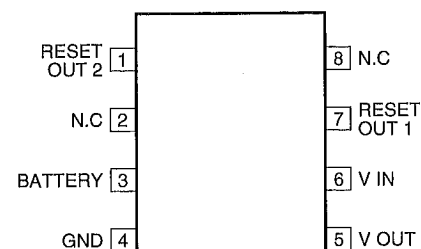
# SEMICONDUCTORS



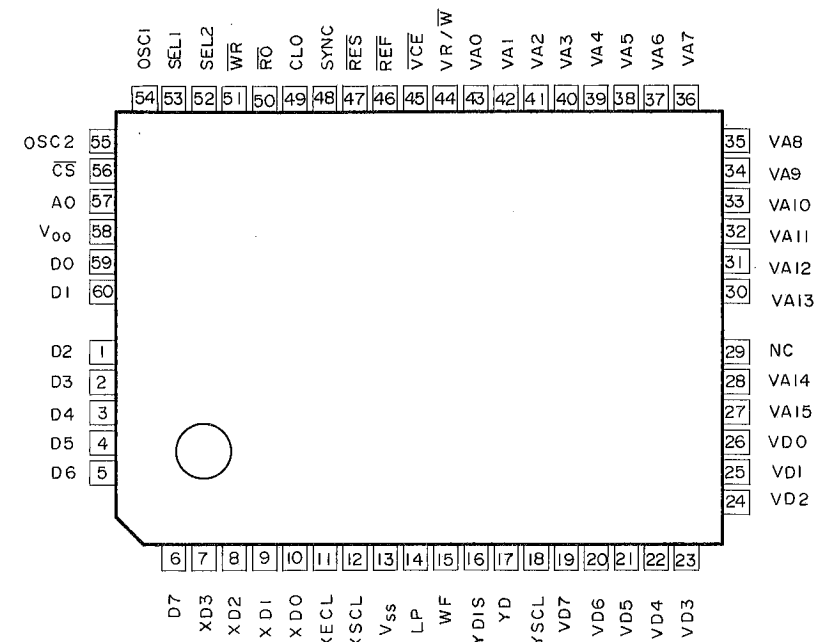
MB87086APF



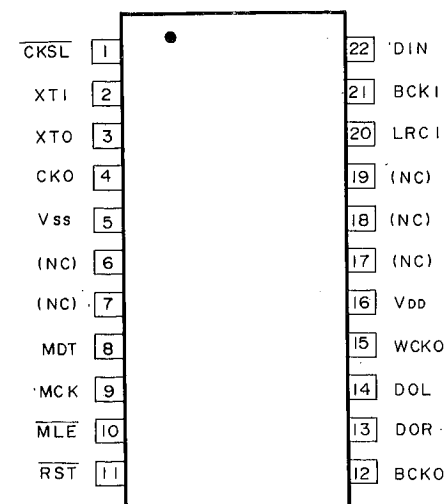
PCM56P-K



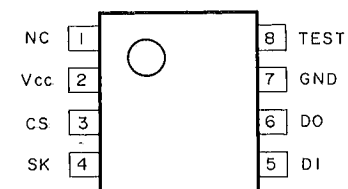
PST531A



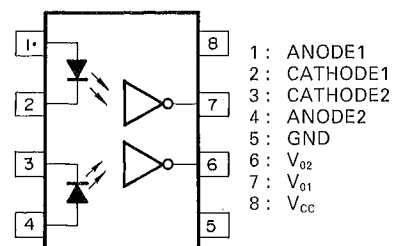
SED1330FBA



SM5840DS



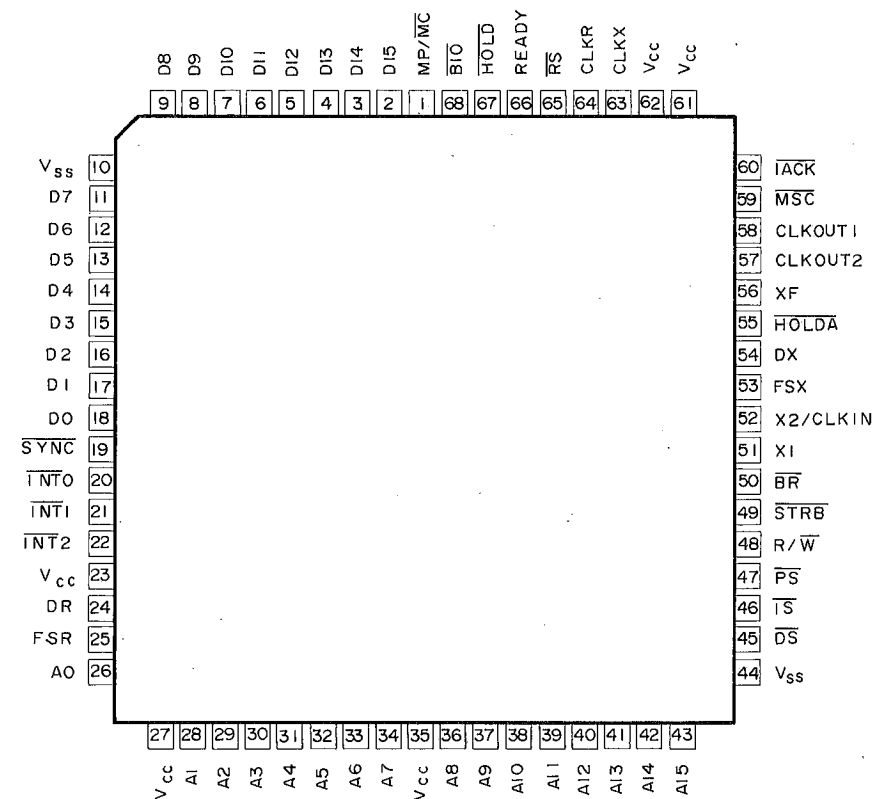
S-2913AIF10



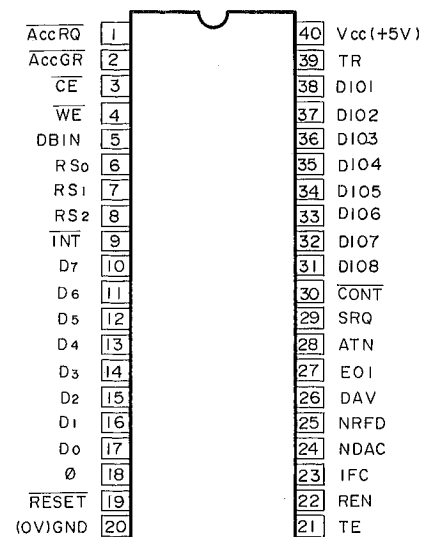
TLP2630



W02-2305-05

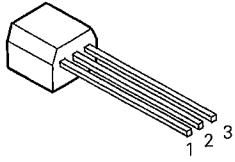


TMS320C25FNL



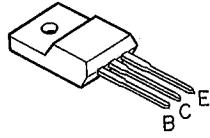
TMS9914A

# SEMICONDUCTORS

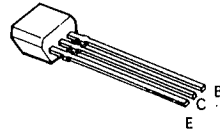


$\mu$ PC1093J

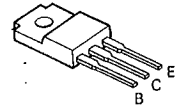
1. REFERENCE
2. ANODE
3. CATHODE



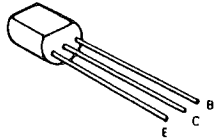
TA7805S



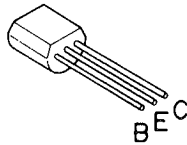
2SA1346  
2SC3402  
RN1006



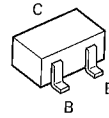
2SB1015 (Y)  
2SD1406 (Y)



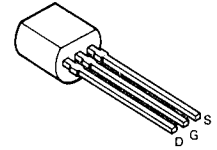
2SC1384 (R)



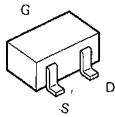
2SC1907



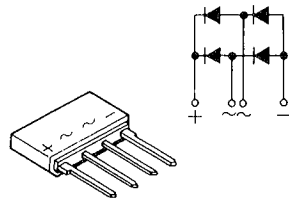
2SC2712  
2SC3099  
2SC3356  
2SC3398



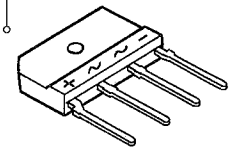
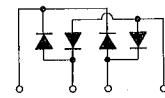
2SK190H



2SK426 (X26)  
2SK1577 (P1)  
2SK1577 (P2)



SIVB20



D4SBS6

---

A product of  
**KENWOOD CORPORATION**  
14-6, Dogenzaka 1-chome, Shibuya-ku, Tokyo 150, Japan

---

B51-1125-00 (MC)